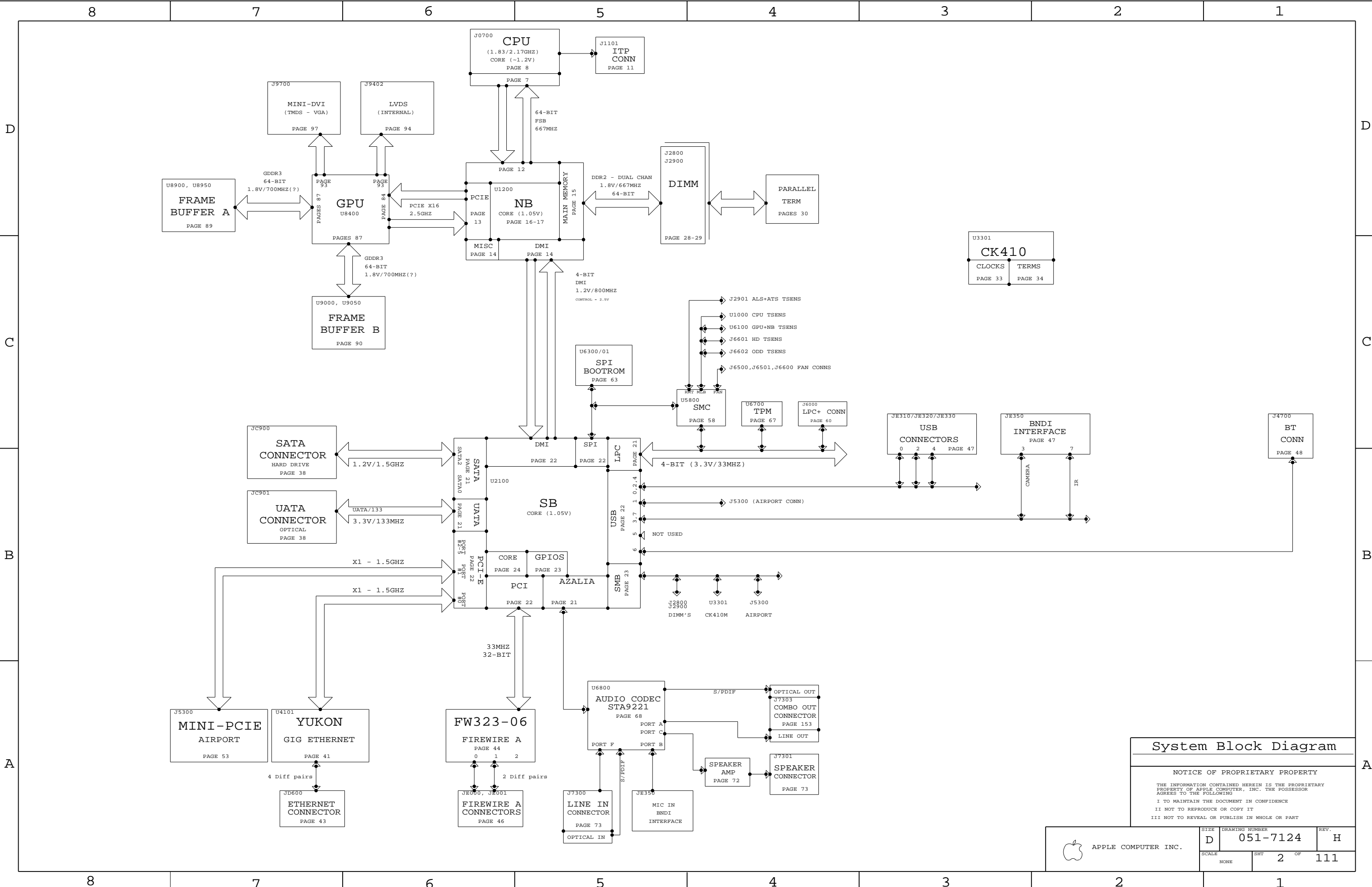


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System Block Diagram

NOTICE OF PROPRIETARY PROPERTY

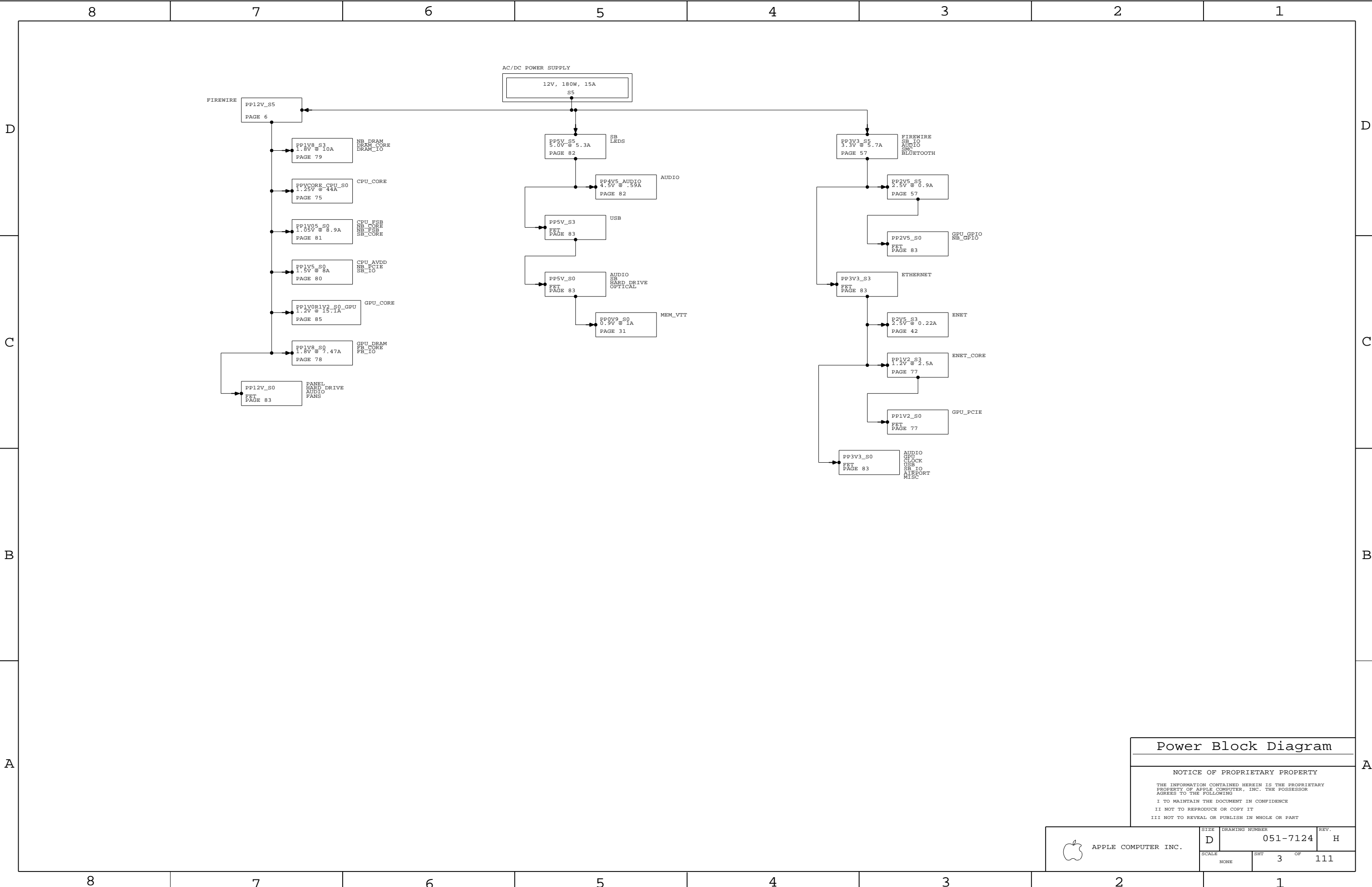
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 2	OF 111



Power Block Diagram

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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE NONE	SHT 3	OF 111

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	<table><tr><th>PART#</th><th>QTY</th><th>DESCRIPTION</th><th>REFERENCE DESIGNATOR(S)</th><th>CRITICAL</th><th>BOM OPTION</th></tr><tr><td>511S0025</td><td>1</td><td>IC,CPU-SKT,479BGA</td><td>J0700</td><td>CRITICAL</td><td></td></tr><tr><td>338S0328</td><td>1</td><td>IC,945PM,NORTHBRIDGE</td><td>U1200</td><td>CRITICAL</td><td></td></tr><tr><td>343S0385</td><td>1</td><td>IC,SB,652BGA</td><td>U2100</td><td>CRITICAL</td><td></td></tr><tr><td>338S0345</td><td>1</td><td>IC,ATI,M56D,GRAPHXCTLR,880BGA,LF</td><td>U8400</td><td>CRITICAL</td><td></td></tr><tr><td>359S0101</td><td>1</td><td>IC,CY28445-5,CLK_GEN,68PIN QFP</td><td>U3301</td><td>CRITICAL</td><td></td></tr><tr><td>338S0270</td><td>1</td><td>IC,88E8053,GIGABIT ENET XCVR,64P QFN,NO</td><td>U4101</td><td>CRITICAL</td><td></td></tr><tr><td>(335S0382) 341S1797</td><td>1</td><td>IC,ENET LAN ROM</td><td>U4102</td><td>CRITICAL</td><td></td></tr><tr><td>338S0279</td><td>1</td><td>IC,FW32306,1394A LINK,TQFP</td><td>U4400</td><td>CRITICAL</td><td></td></tr></table>																PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION	511S0025	1	IC,CPU-SKT,479BGA	J0700	CRITICAL		338S0328	1	IC,945PM,NORTHBRIDGE	U1200	CRITICAL		343S0385	1	IC,SB,652BGA	U2100	CRITICAL		338S0345	1	IC,ATI,M56D,GRAPHXCTLR,880BGA,LF	U8400	CRITICAL		359S0101	1	IC,CY28445-5,CLK_GEN,68PIN QFP	U3301	CRITICAL		338S0270	1	IC,88E8053,GIGABIT ENET XCVR,64P QFN,NO	U4101	CRITICAL		(335S0382) 341S1797	1	IC,ENET LAN ROM	U4102	CRITICAL		338S0279	1	IC,FW32306,1394A LINK,TQFP	U4400	CRITICAL																																										
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<table><tr><td>152S0138</td><td>2</td><td>IND,PWR,SHLD,3.30H,20%,6A,30MOHM,LF,SMD</td><td>L5703,L8203</td><td>CRITICAL</td><td></td></tr></table>																152S0138	2	IND,PWR,SHLD,3.30H,20%,6A,30MOHM,LF,SMD	L5703,L8203	CRITICAL																																																																																											
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(341S1904 - DEV)
(341S1905 - PVT)
(335S0384 - BLNK)
(341S1903 - PROG)
(338S0274 - BLNK)

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-7124	1	PCB,SCHEM,MLB,M60	SCH1		20_INCH_LCD
820-2031	1	PCB,FAB,MLB,M60	MLB1		20_INCH_LCD
341T0036	1	EFI ROM,M60	U6301	CRITICAL	20_INCH_LCD
341T0035	1	IC,SMC,M60	U5800	CRITICAL	20_INCH_LCD
338S0315	1	IC,ATI,M56LP,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B26_LP
114S0264	1	3.01K,1%,1/16W,402,MF-LF	R8522		GPU_VCORE_1P2V
337S3390	1	2.16GHZ MEROM	CPU	CRITICAL	2P16_CPU
337S3392	1	2.33GHZ MEROM	CPU	CRITICAL	2P33_CPU

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0354	4	IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_SAMSUNG
333S0358	4	IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_HYNIX
333S0376	4	IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_INFINEON

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0350	4	IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_SAMSUNG
333S0351	4	IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_HYNIX
333S0377	4	IC,SGRAM,GDDR3,16MX32,600MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_INFINEON

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0086	126S0078		C340,C1900,C1901	C1968 SANYO W6CE330FS 330UF 6.3V LF
128S0080	128S0078		C7517,C7518	SANYO 168VP330W 330UF 16V SMD LF
124-0338	124-0333			CAP,AL,EL,680UF,16V,RAD,10X12.5MM
138S0580	138S0552			22UF 0805
124-0361	124-0339		C7807	SANYO
353S1321	353S1105		U7910	LM339
338S0344	338S0345		U8400	M56 B26 P - DIFF P/N
353S1461	353S1465		U7500	CPU REGULATOR ~ 15L9504
378S0141	378S0140		LED601,LED602,LED603	LED
138S0598	138S0512			SAMSUNG
155S0232	155S0289	FLR011,FLR021,FL4610,FL4620	L4712,L4722,L4732	EMI CHOKE

Table Items

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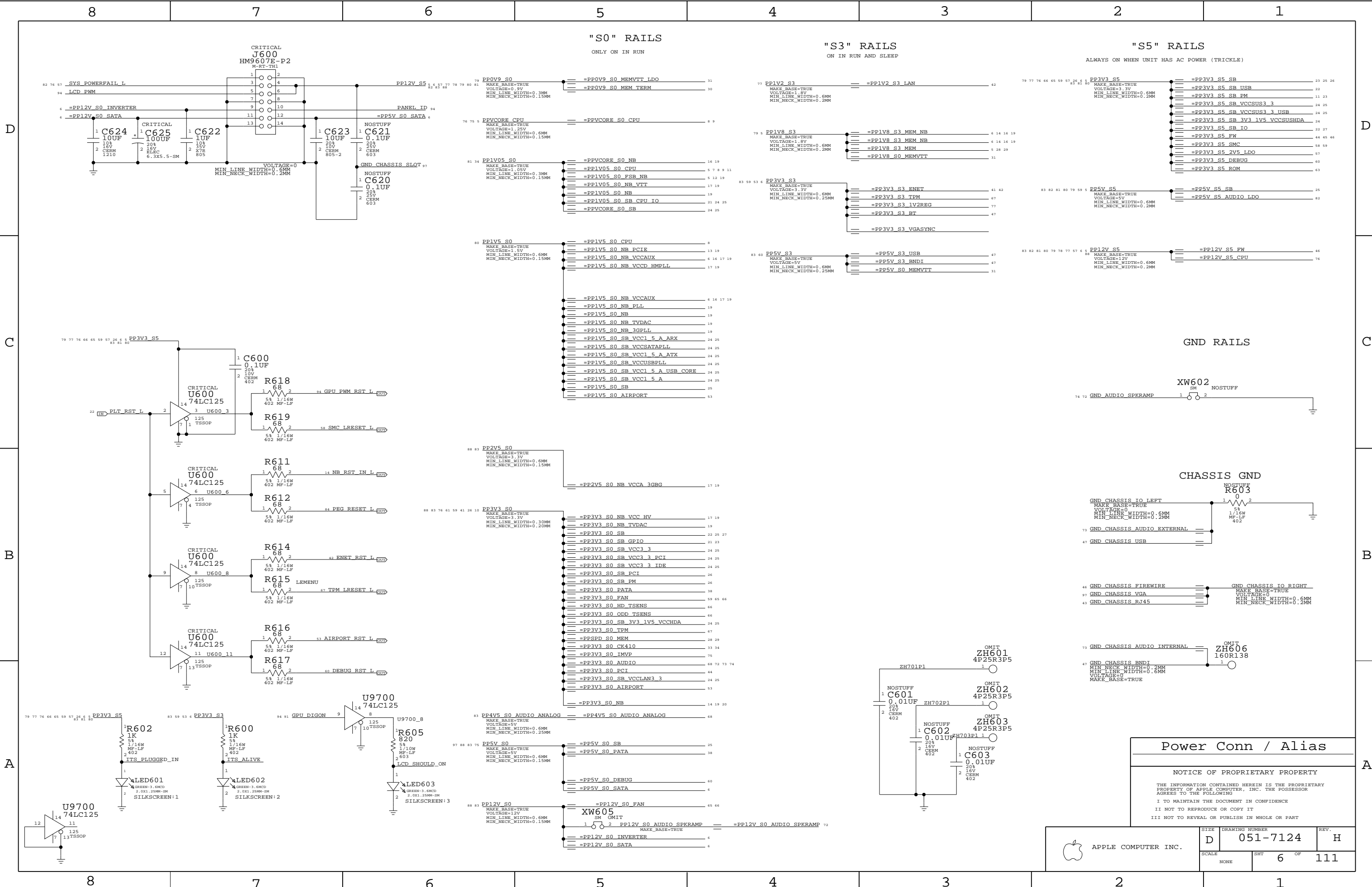
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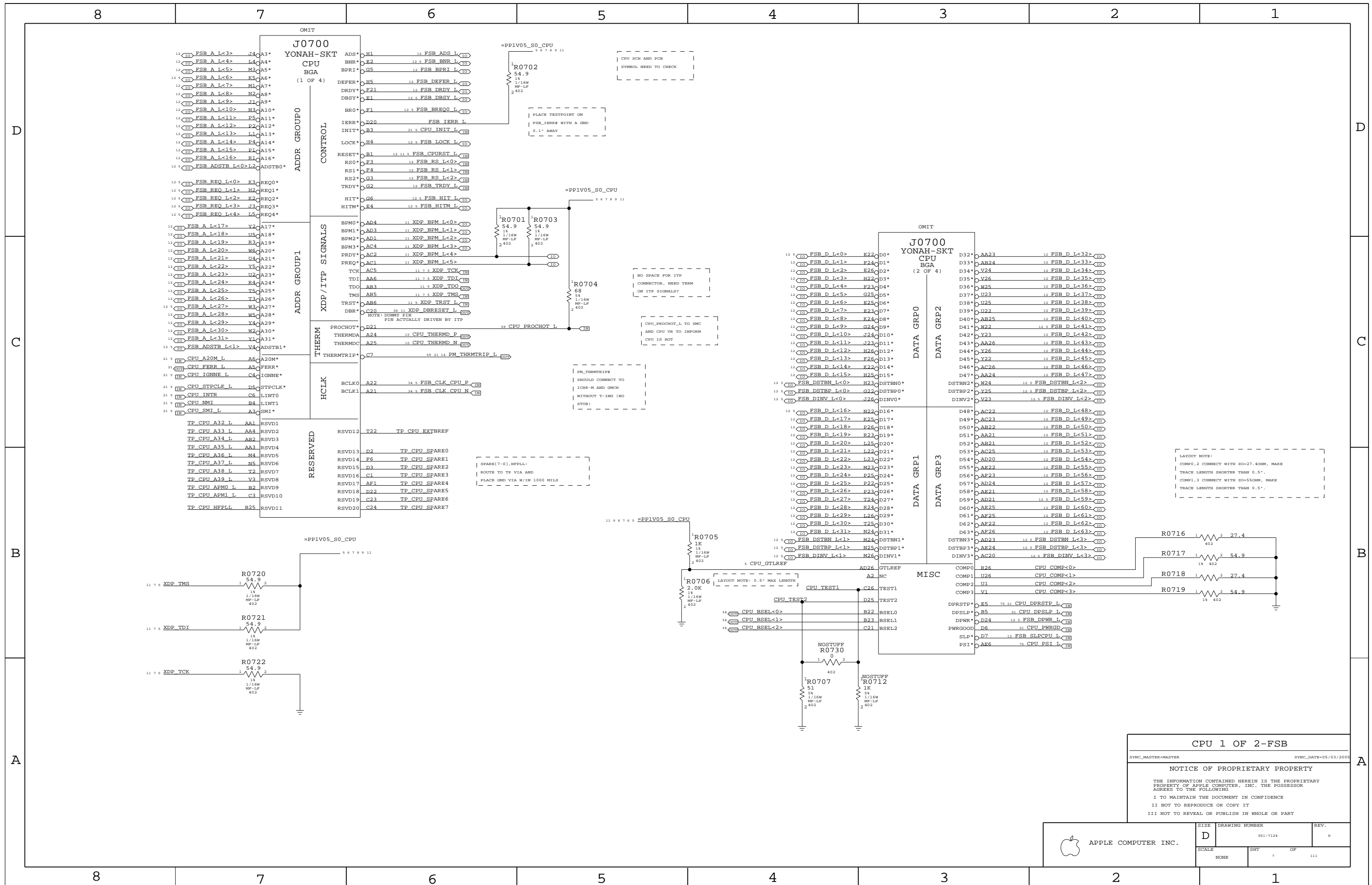
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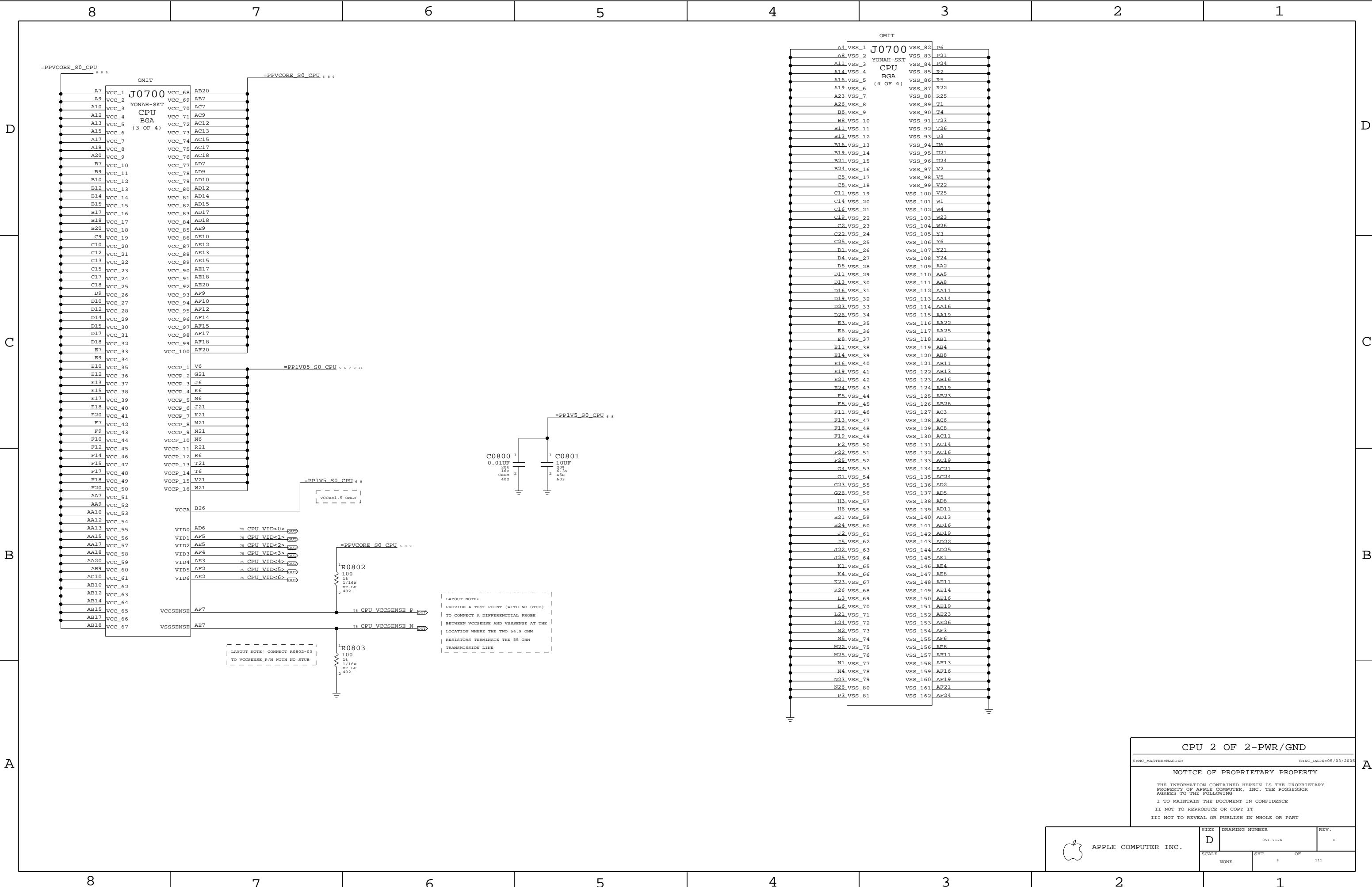


APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	4	111







CPU 2 OF 2-PWR/GND

SYNC_MASTER=MASTER

SYNC_DATE=05/03/2005

NOTICE OF PROPRIETARY PROPERTY

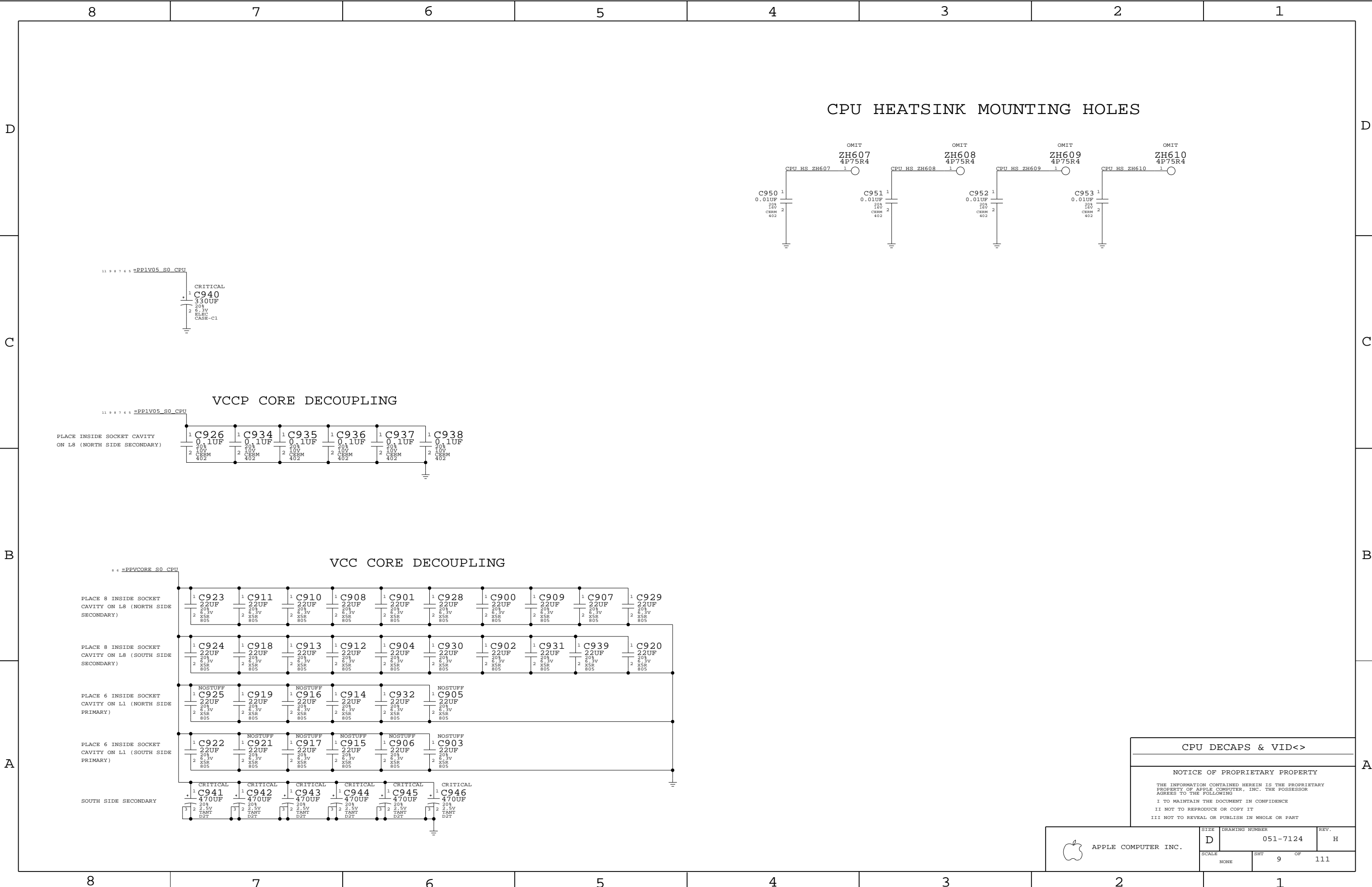
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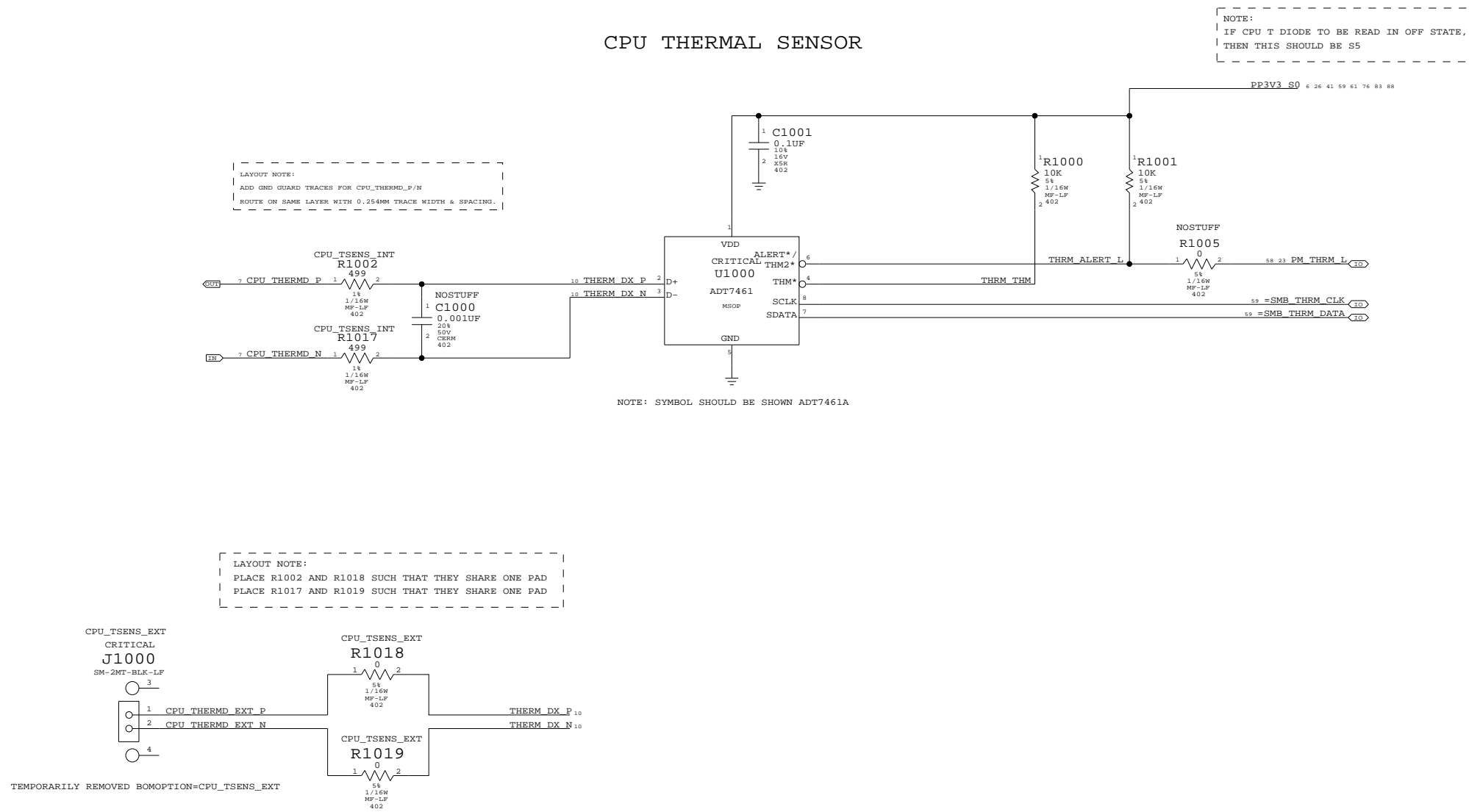
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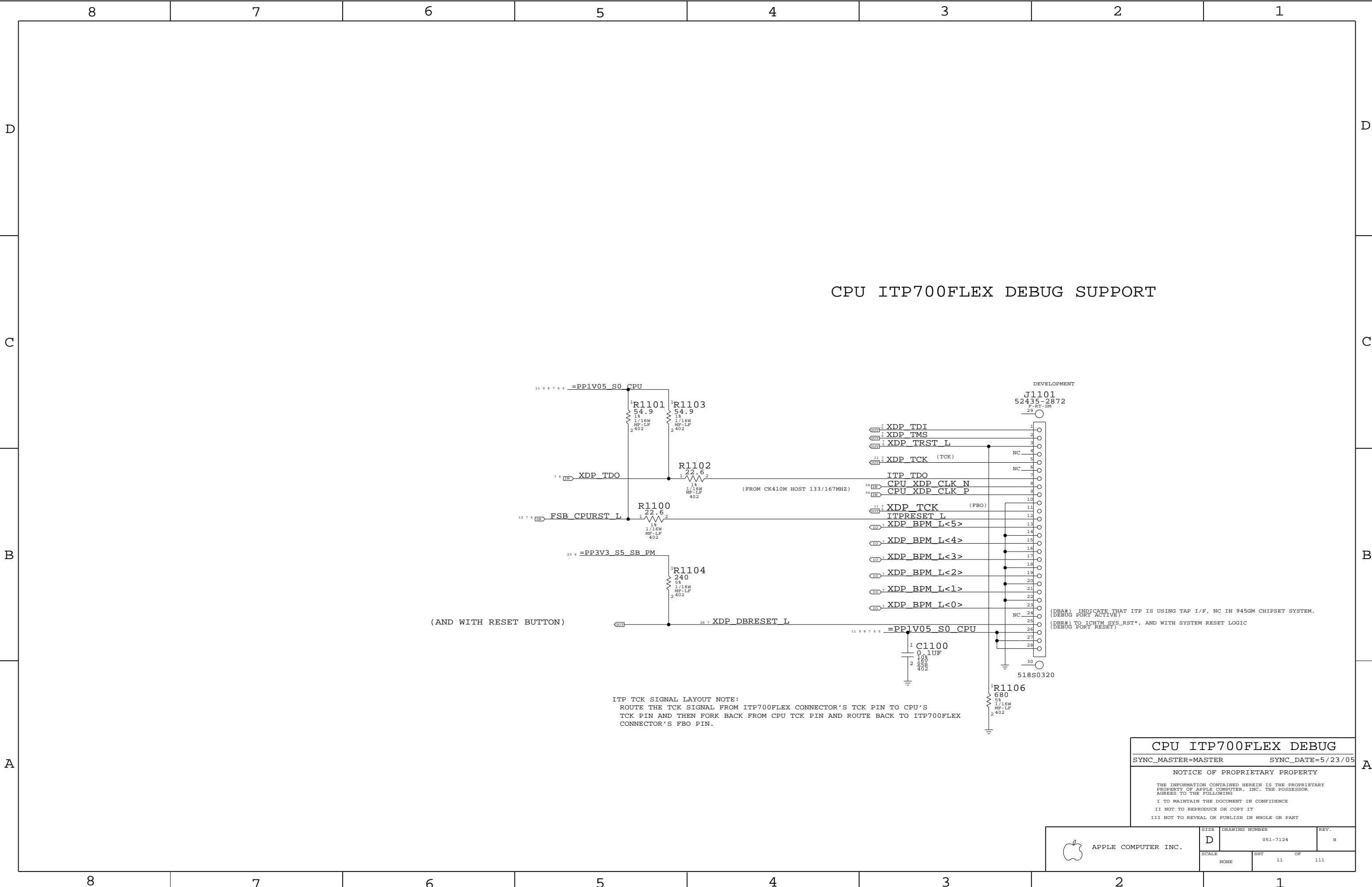
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
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SCALE		SHT	OF
NONE		8	111







CPU ITP700FLEX DEBUG

SYNC_MASTER=MASTER SYNC_DATE=5/23/05

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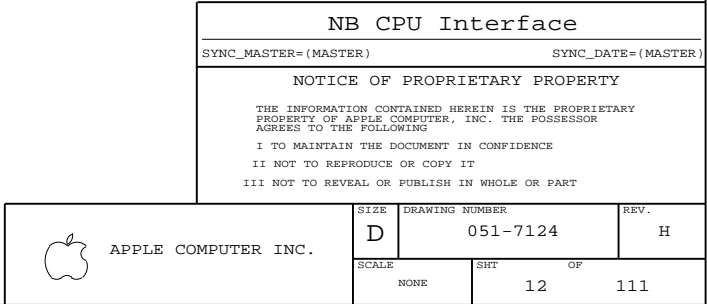
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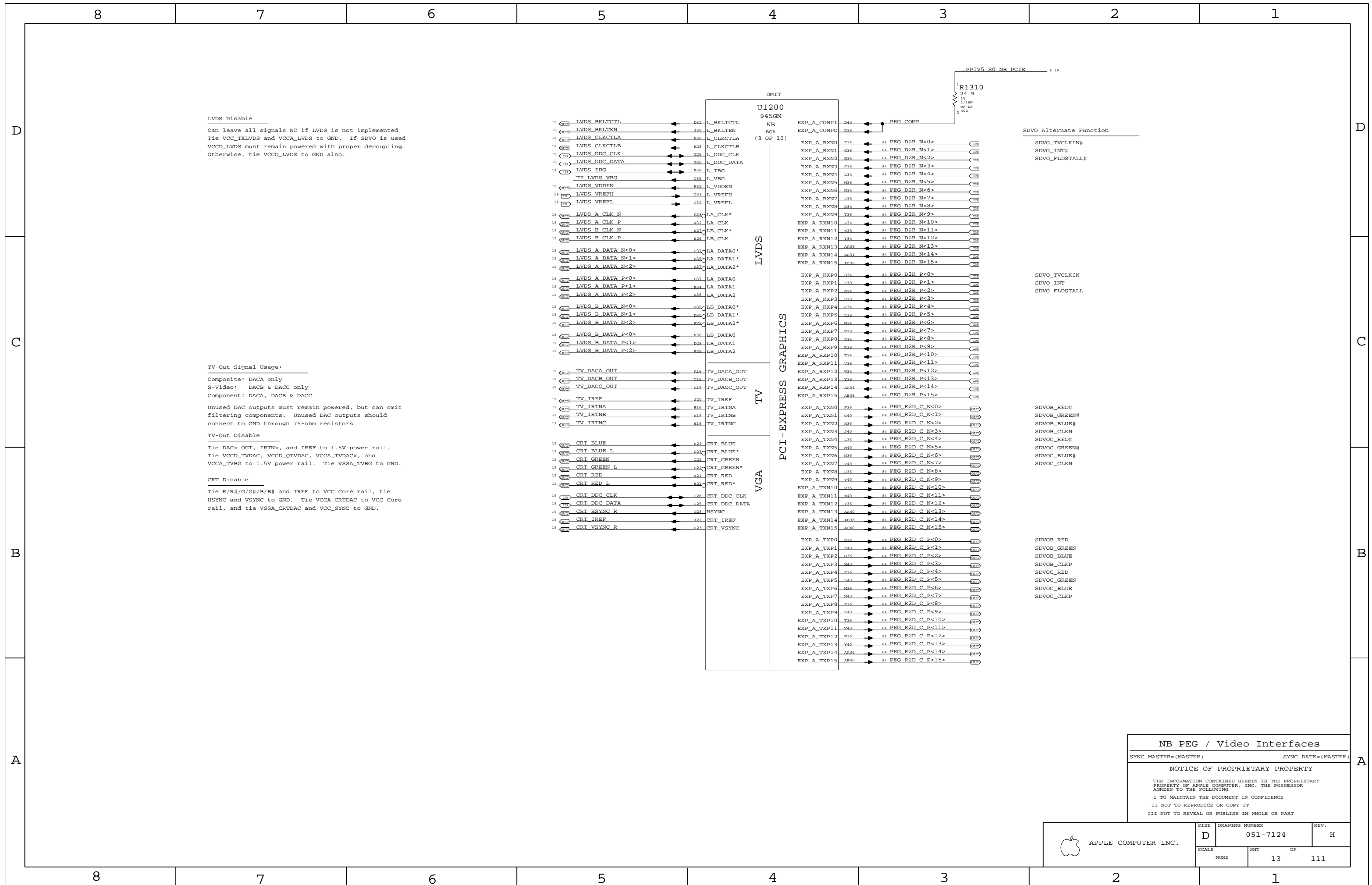
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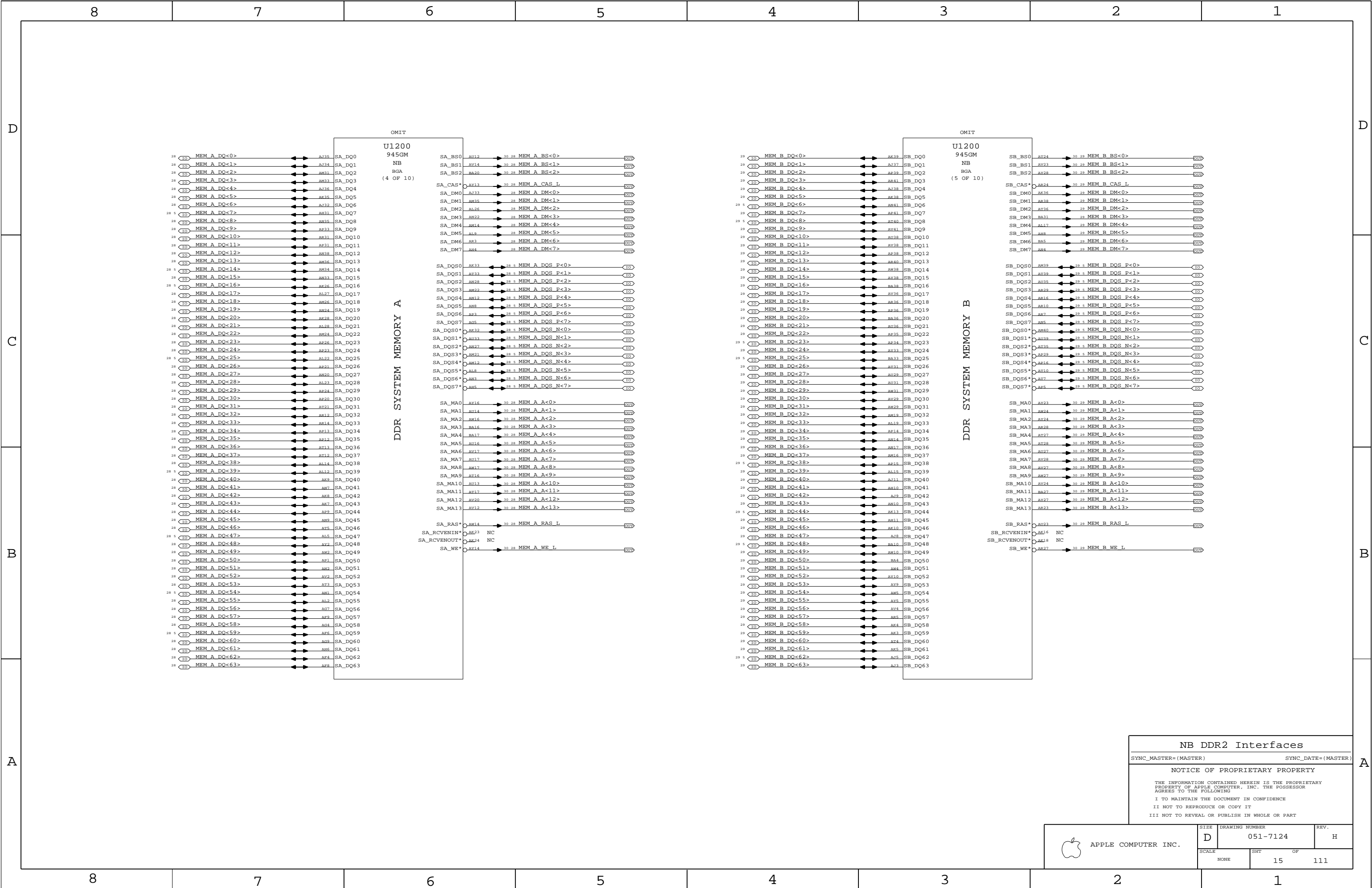
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
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NB DDR2 Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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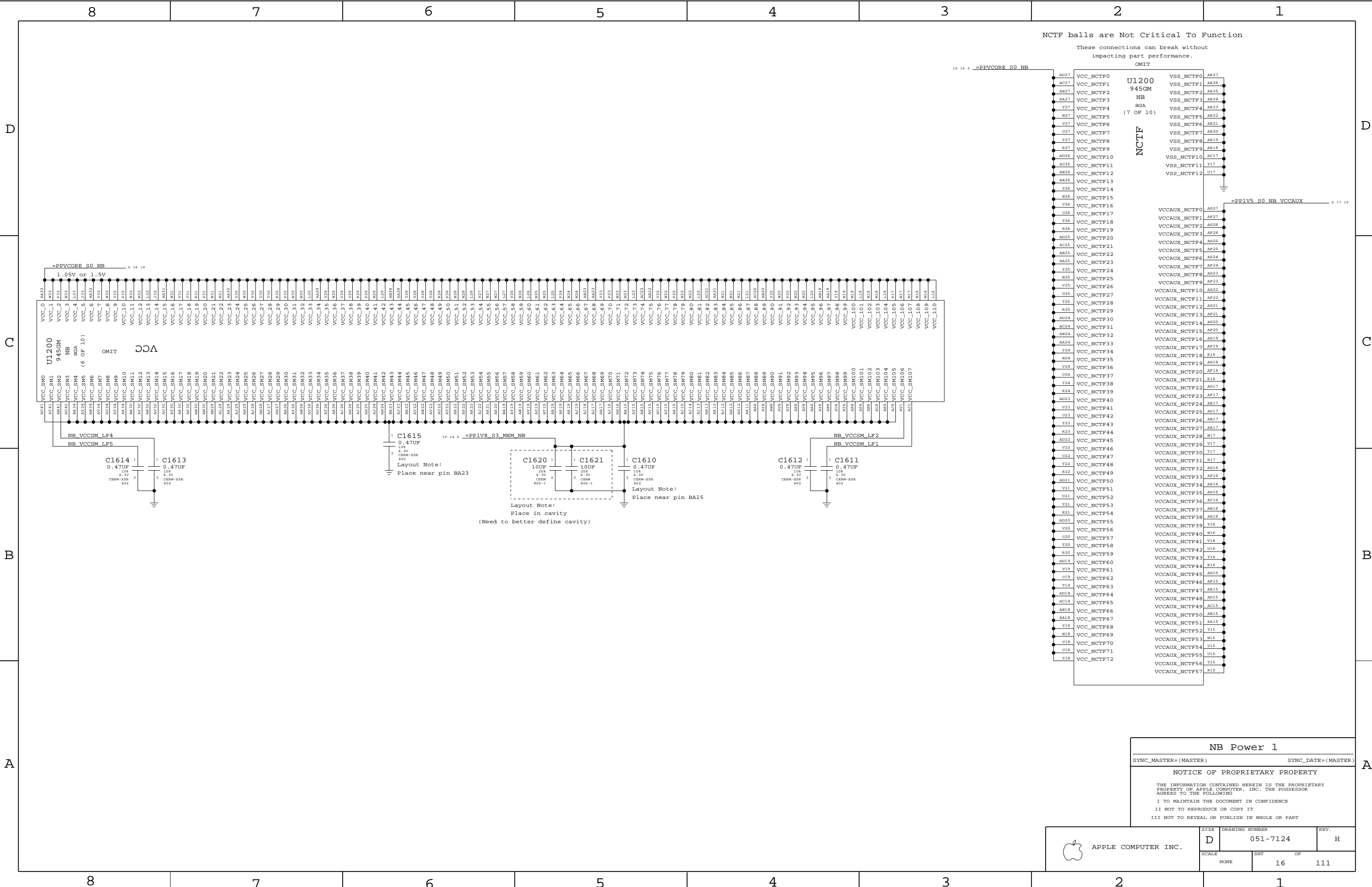
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D	051-7124	H
SCALE	SHT	OF
NONE	15	111



NB Power 1

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

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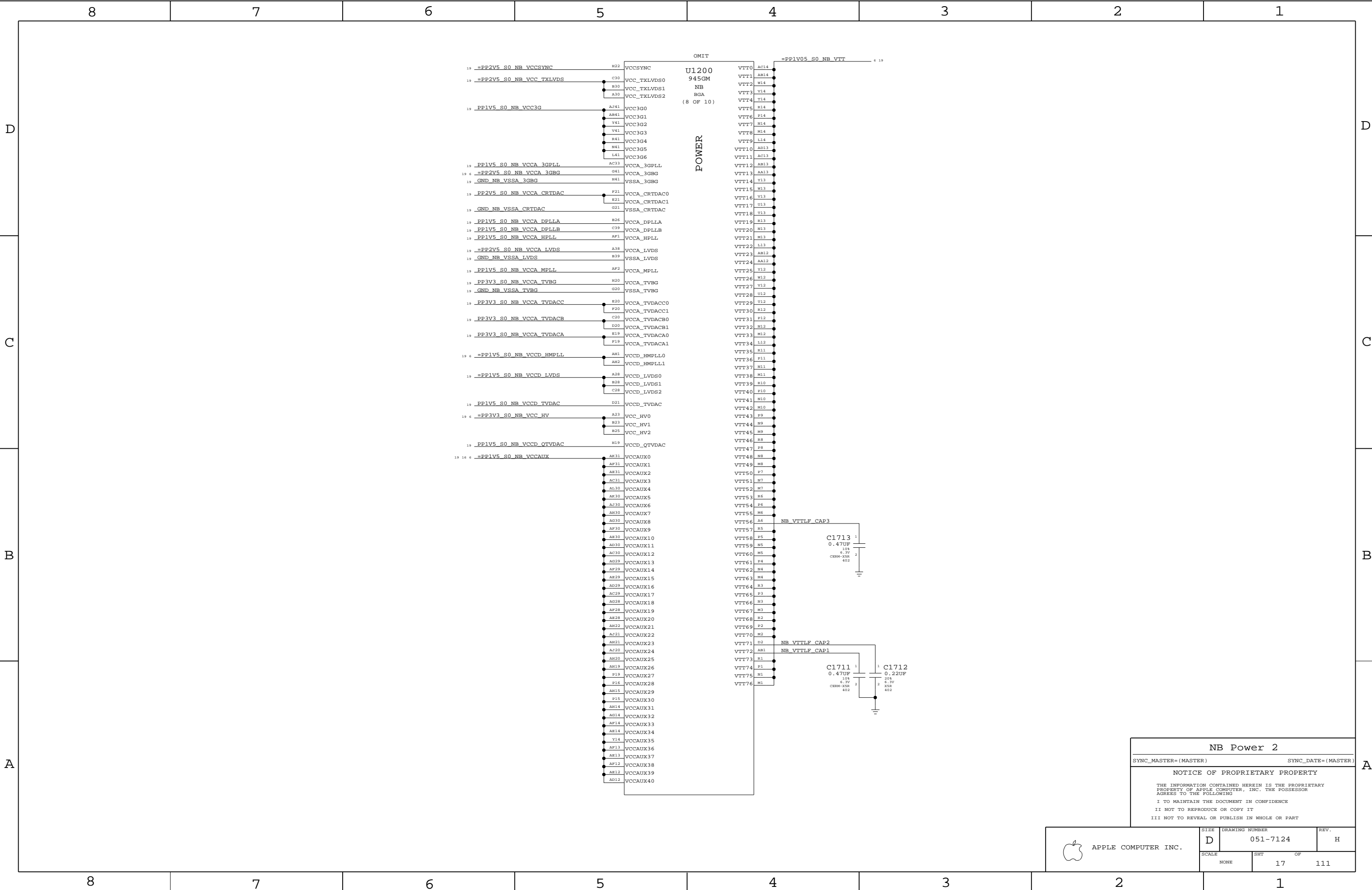
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	NONE	16	111



NB Power 2

SYNC_MASTER=(MASTER)SYNC_DATE=(MASTER)


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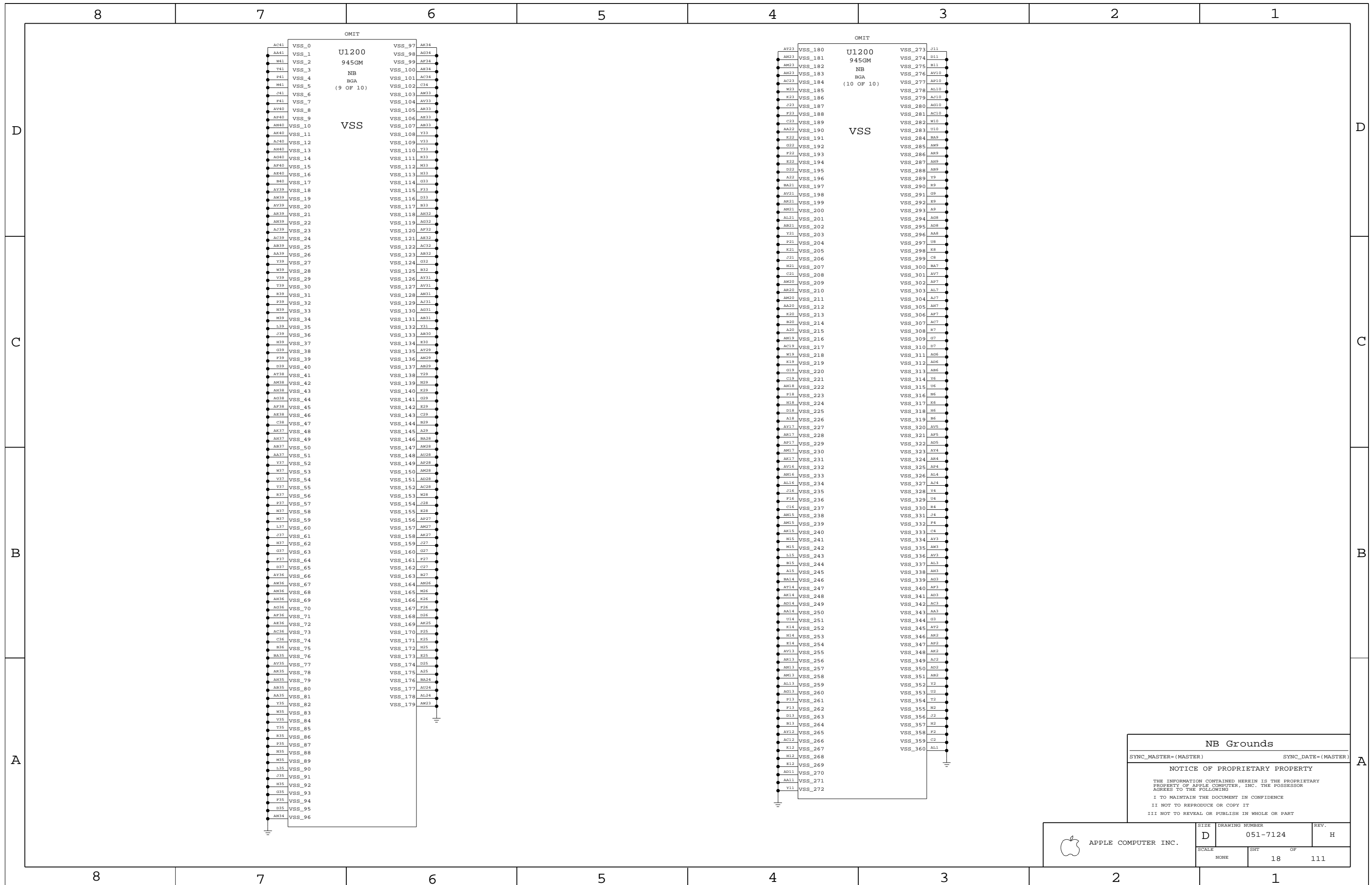
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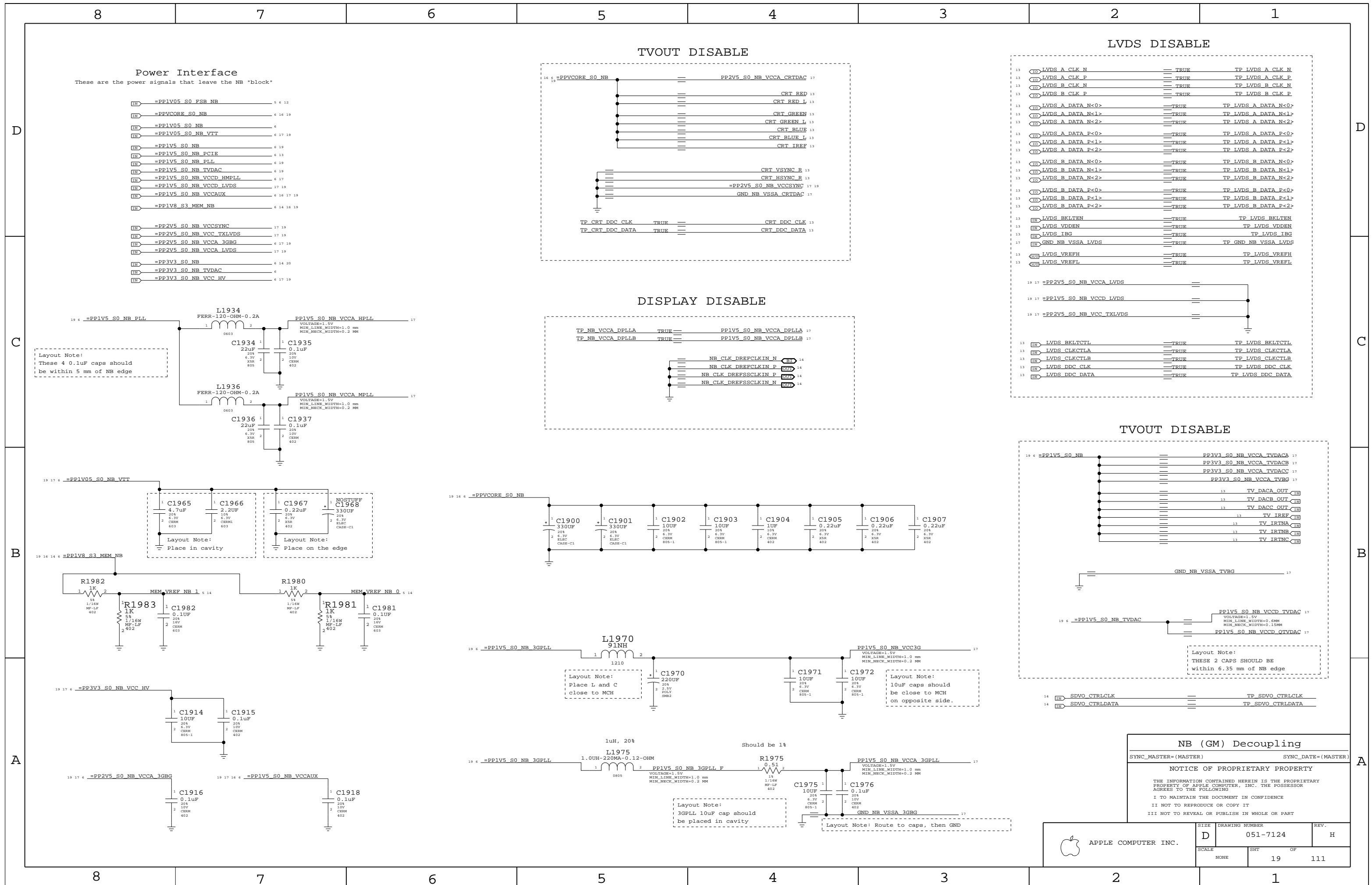
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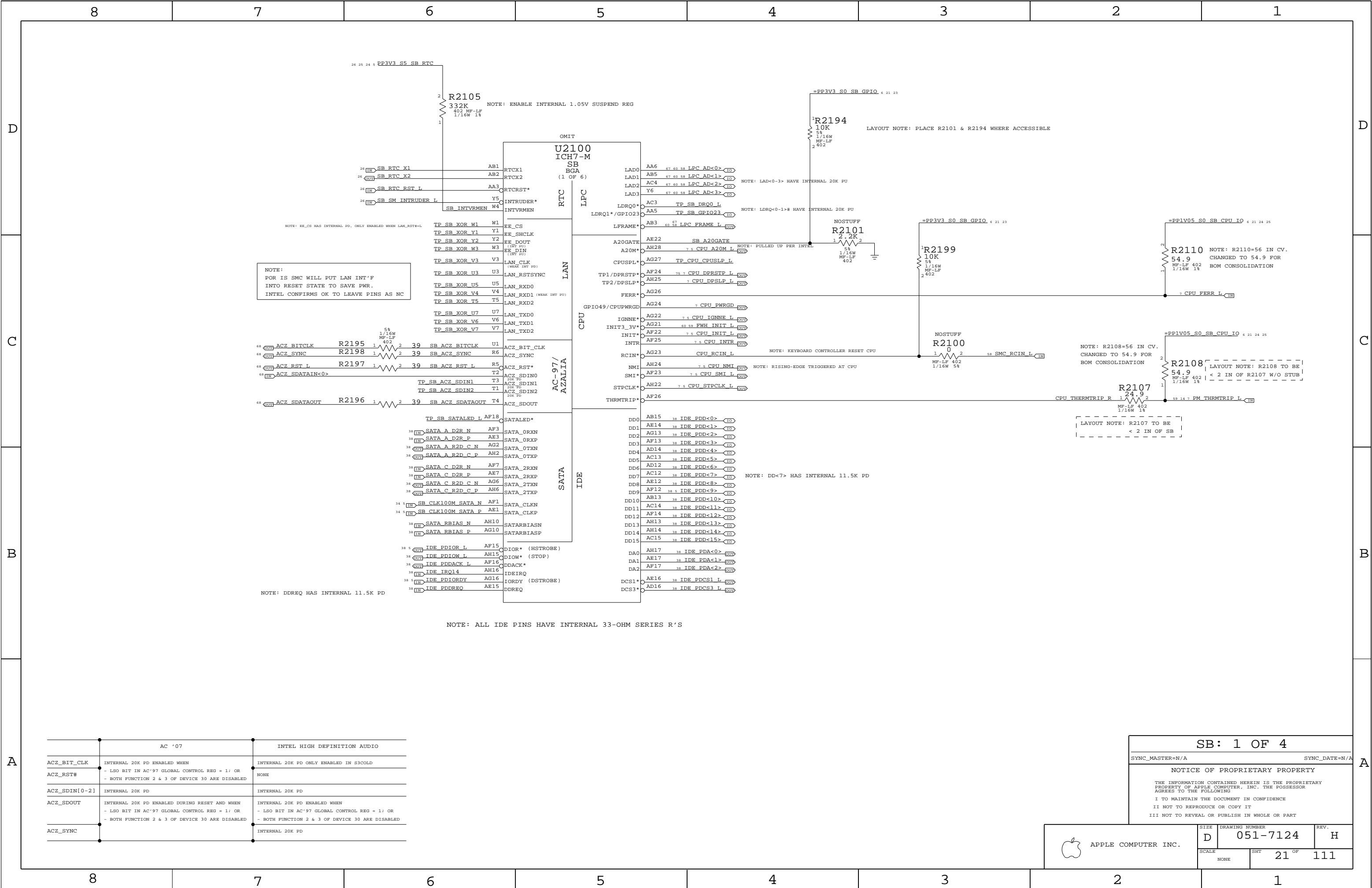
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

 APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 17	OF 111







AC '07		INTEL HIGH DEFINITION AUDIO	
ACZ_BIT_CLK	INTERNAL 20K PD ENABLED WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR	INTERNAL 20K PD ONLY ENABLED IN S3COLD	
ACZ_RST#	- BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED	NONE	
ACZ_SDIN[0-2]	INTERNAL 20K PD	INTERNAL 20K PD	
ACZ_SDOUT	INTERNAL 20K PD ENABLED DURING RESET AND WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR	INTERNAL 20K PD ENABLED WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR	
	- BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED	- BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED	
ACZ_SYNC		INTERNAL 20K PD	

SB: 1 OF 4

SYNC_MASTER=N/A SYNC_DATE=N/A

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SIZE

D

DRAWING NUMBER

051-7124

REV.

H

SCALE

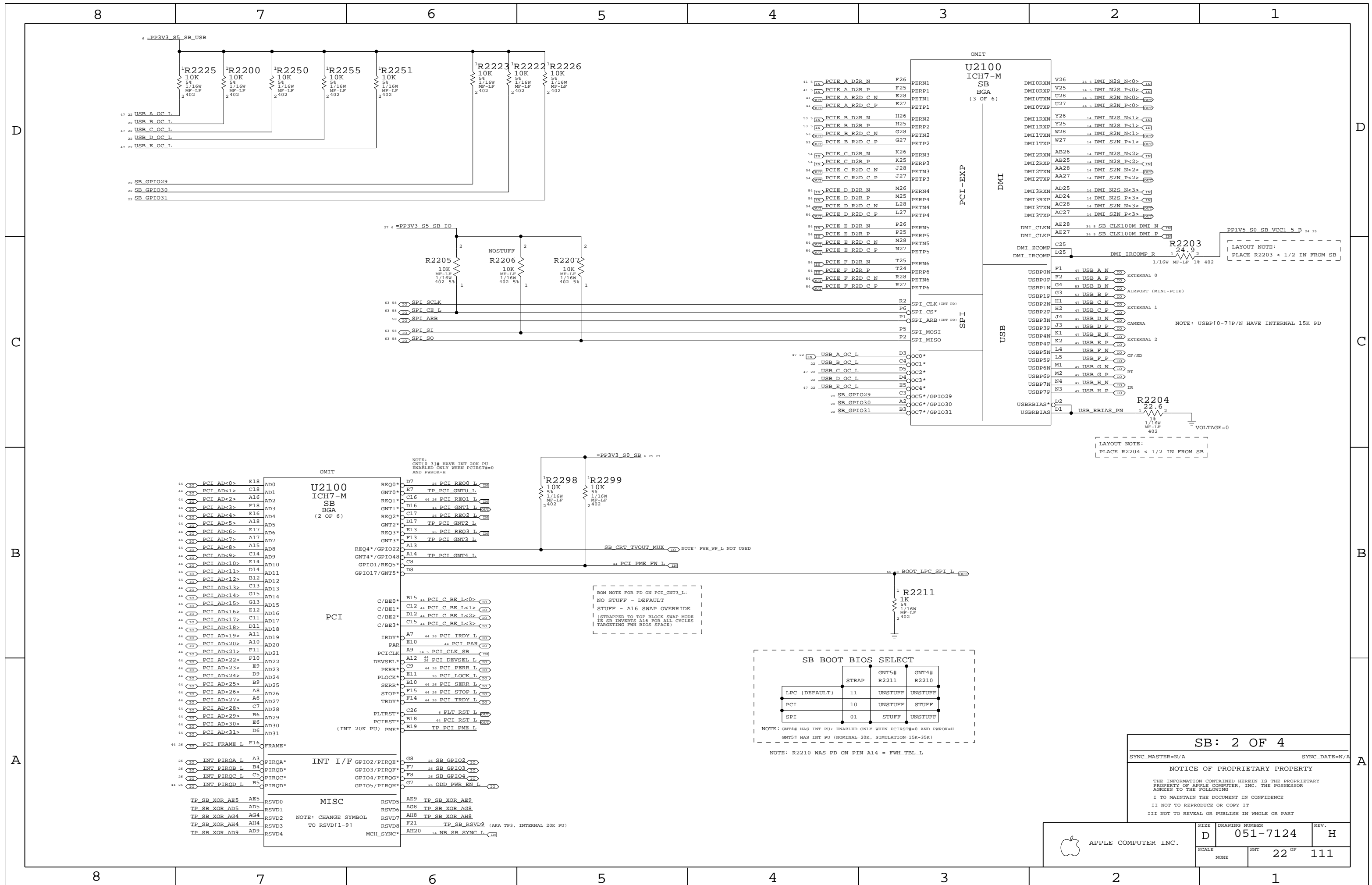
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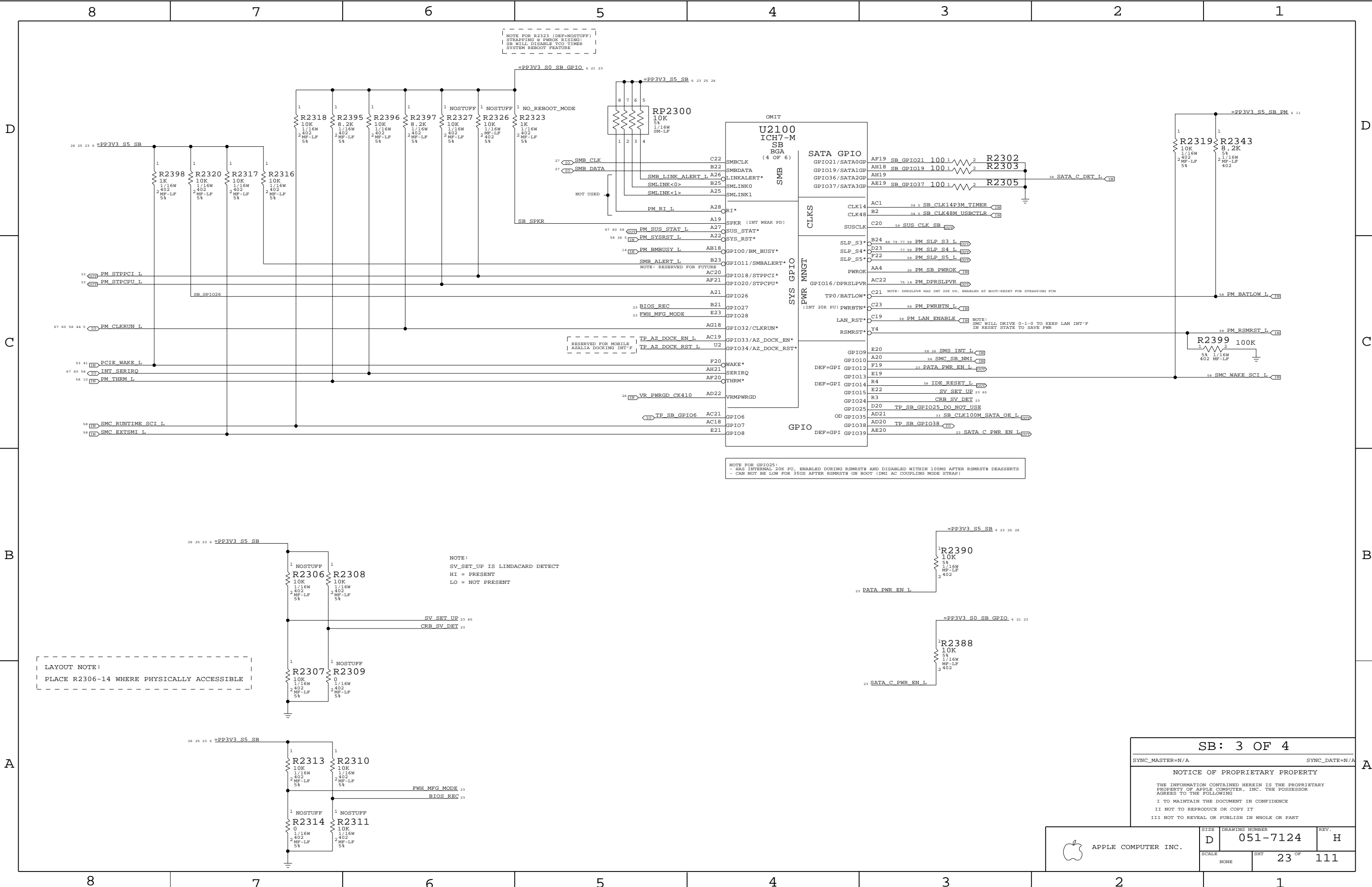
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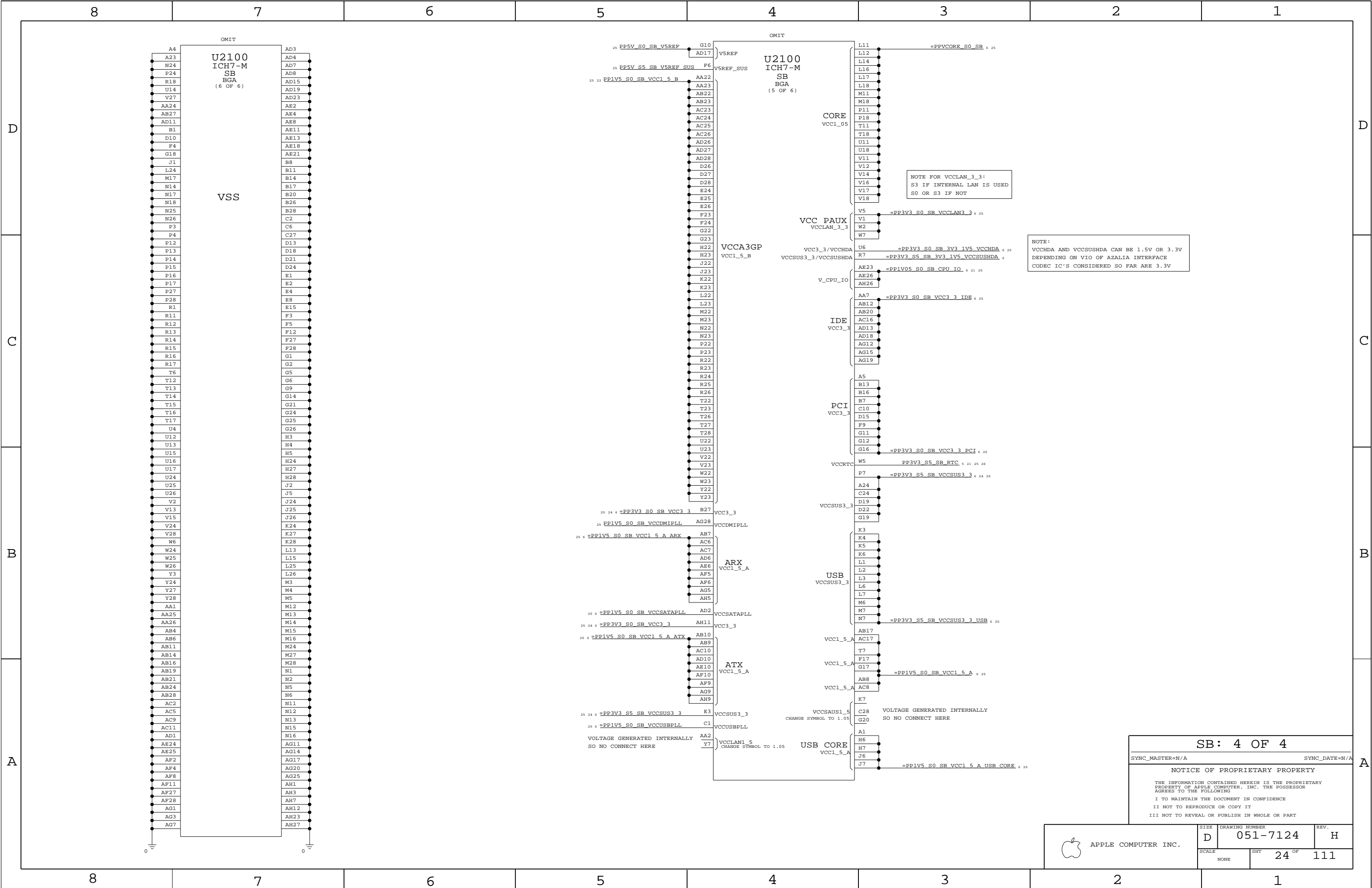
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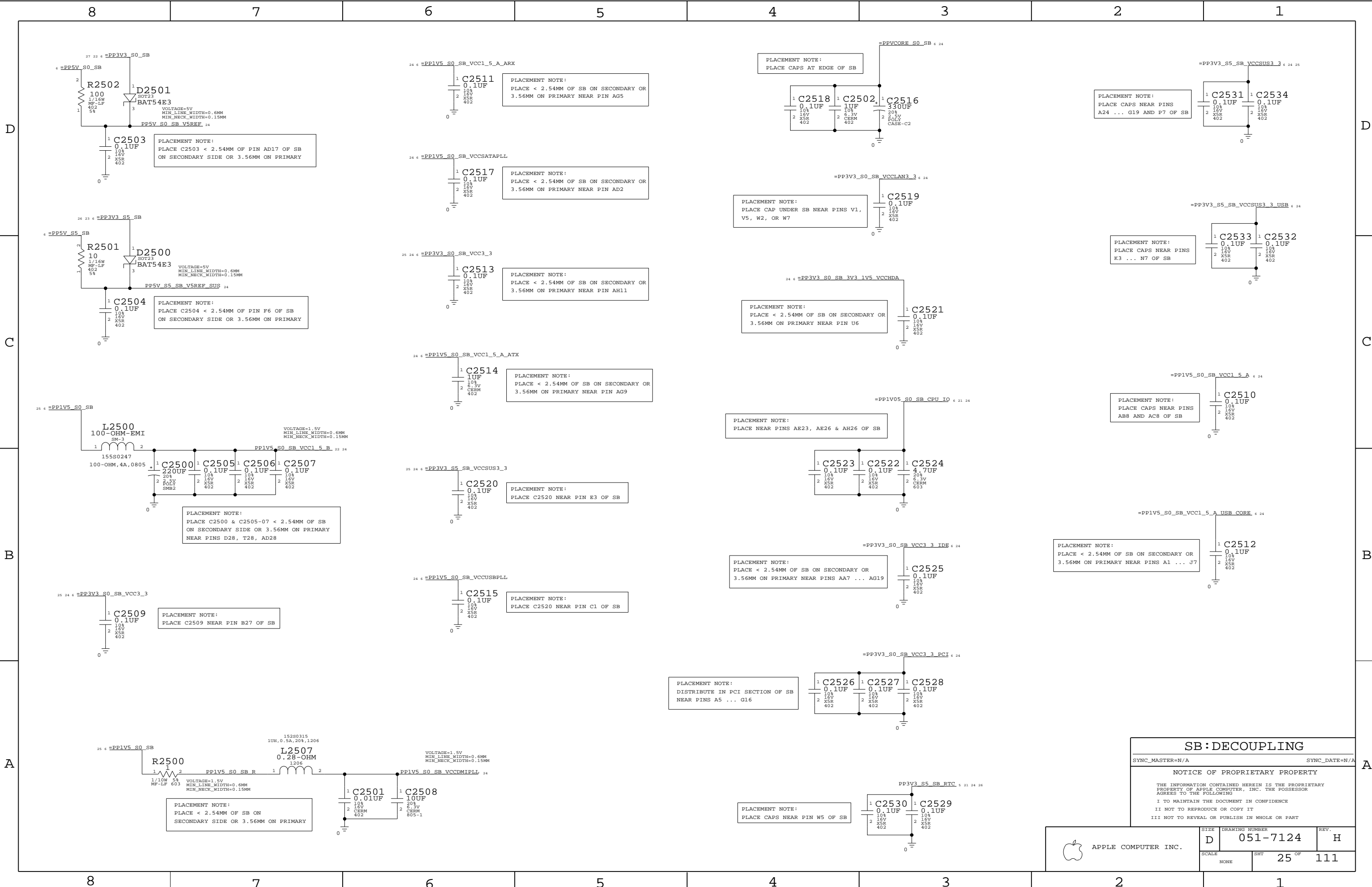
OF

111









SB: DECOUPLING

SYNC_MASTER=N/A

SYNC_DATE=N/A

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SCALE: NONE

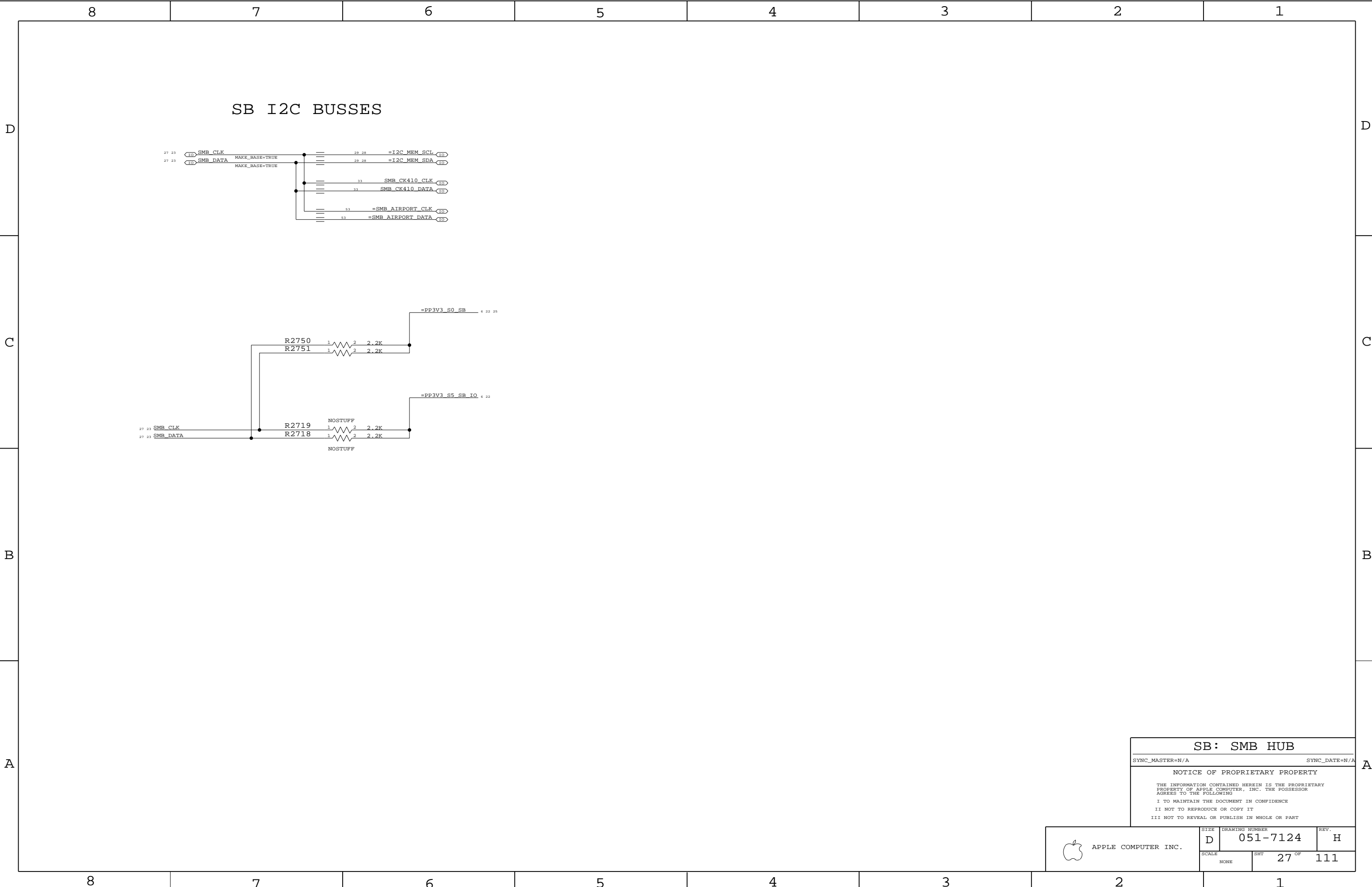
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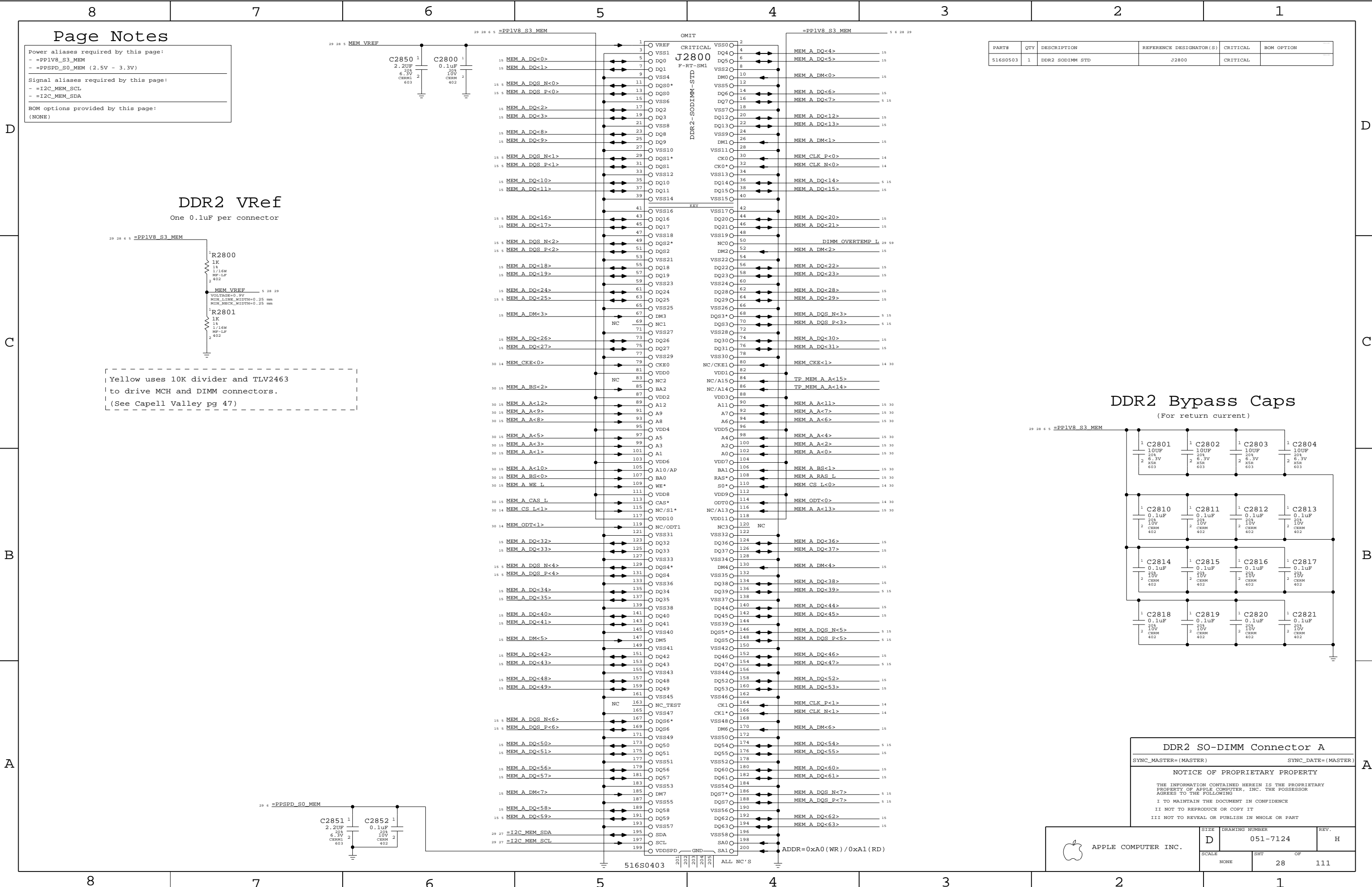
SHT: 25 OF 111

SIZE: D

DRAWING NUMBER: 051-7124

REV.: H





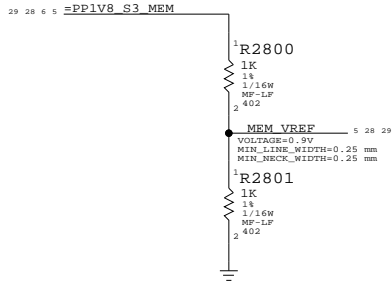
Page Notes

Power aliases required by this page:
- =PPIV8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:
- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:
(NONE)

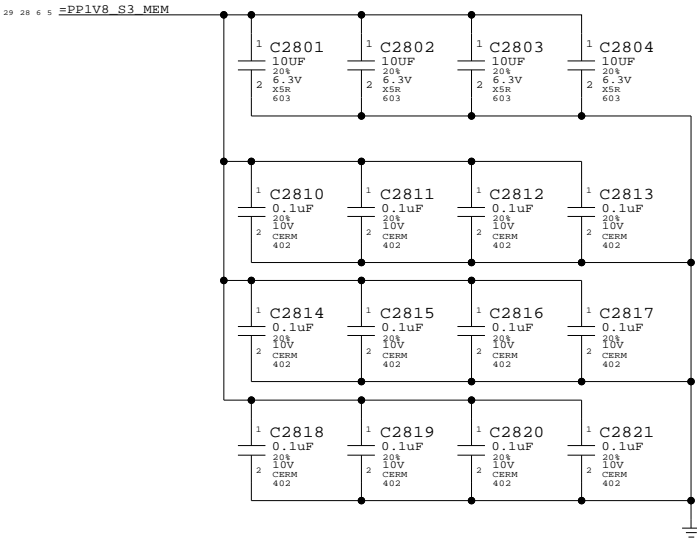
DDR2 VRef
One 0.1uF per connector




Yellow uses 10K divider and TLV2463 to drive MCH and DIMM connectors.
(See Capell Valley pg 47)

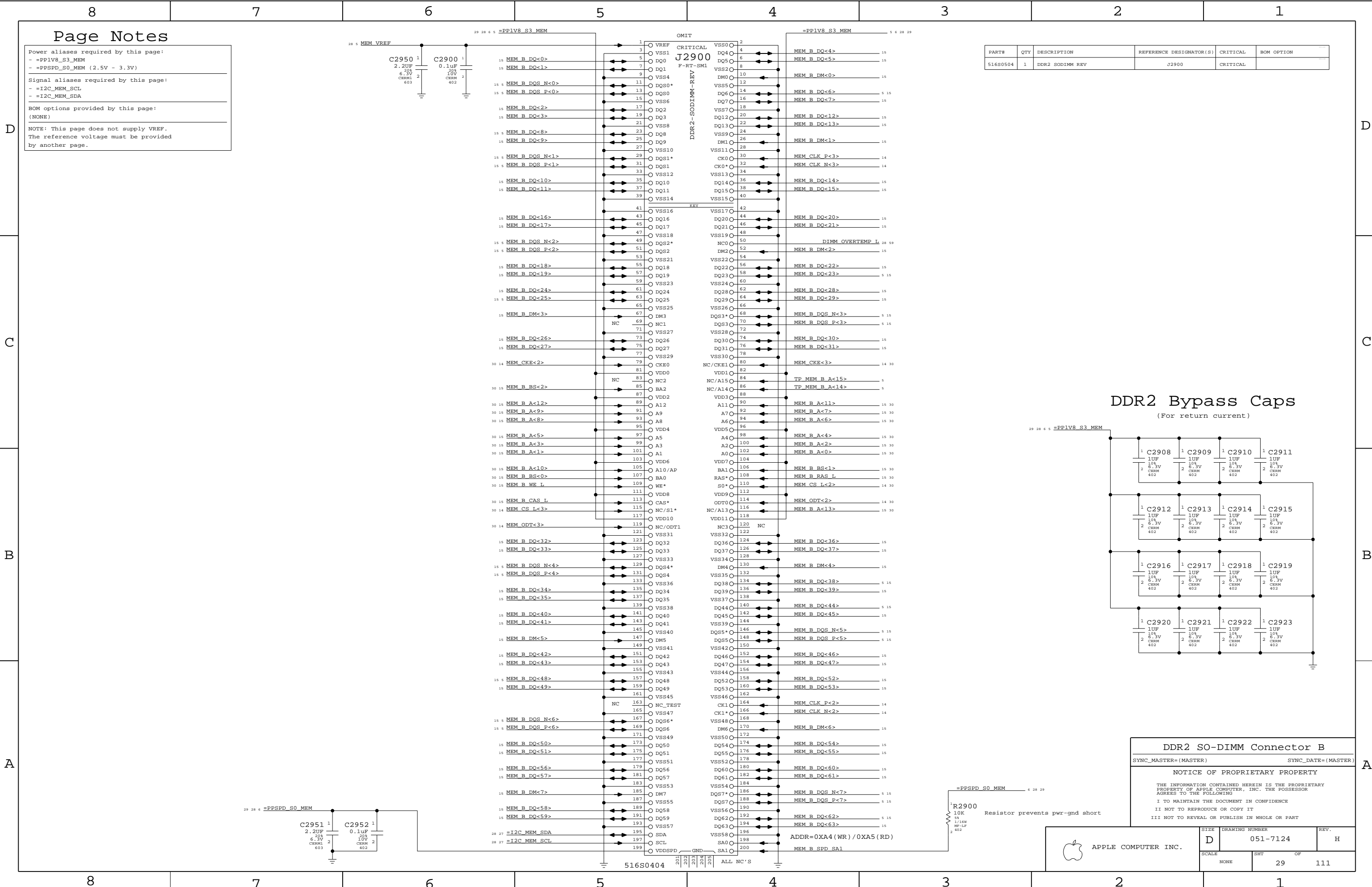
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
516S0503	1	DDR2 SODIMM STD	J2800	CRITICAL	

DDR2 Bypass Caps
(For return current)



DDR2 SO-DIMM Connector A	
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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 APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
	SCALE	SHT OF	
	NONE	28	111



Page Notes

Power aliases required by this page:

- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:

- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:

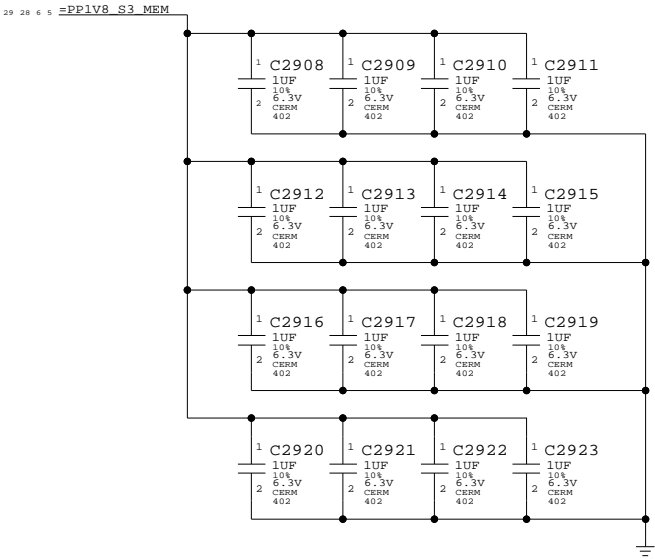
(NONE)

NOTE: This page does not supply VREF.
The reference voltage must be provided
by another page.

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
516S0504	1	DDR2 SODIMM REV	J2900	CRITICAL	

DDR2 Bypass Caps

(For return current)



DDR2 SO-DIMM Connector B

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER		REV.
	D	051-7124		H
SCALE		SHT	OF	
NONE		29	111	


```
Power aliases required by this page:
- =PP5V_S0_MEMVTT
- =PPlV8_S0_MEMVTT
- =PP0V9_S0_MEMVTT_LDO
```

```
Signal aliases required by this page:
(NONE)
```

```
BOM options provided by this page:
(NONE)
```

```
- =PP5V_S0_MEMVTT
- =PP1V8_S0_MEMVTT
- =PP0V9_S0_MEMVTT_LDO
```

(NONE)

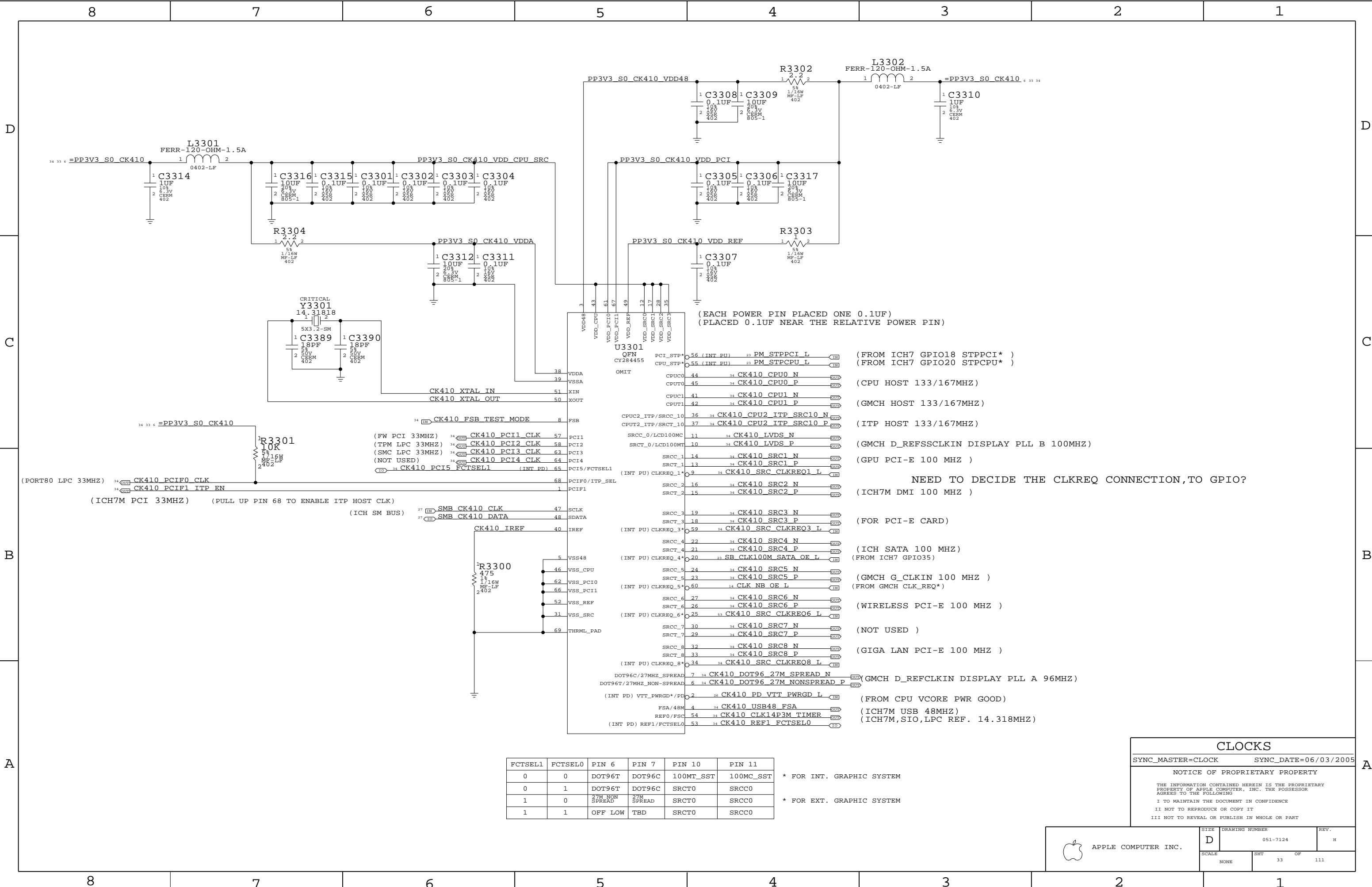
(NONE)

Schematic diagram of the U3100 BD3533FVM MEMVTT block. The block has pins VDDQ, VCC, VTT_IN, VREF, VTT, and EN. VDDQ is connected to PP5V_S0_MEMVTT through resistor R3100 (1k, 5%, 1/16W, MF-LP, 402). VCC is connected to PP1V8_S0_MEMVTT through resistor R3101 (220, 5%, 1/16W, MF-LP, 402). VTT_IN is connected to PP1V8_S0_MEMVTT. VREF is connected to MEMVTT_VREF. VTT is connected to MEMVTT_VREF. EN is connected to MEMVTT_EN. The block is also connected to ground. Capacitors C3101 (10uF, 20%, 6.3V, CERN, 805-1), C3110 (0.1uF, 20%, 10V, CERN, 402), C3109 (2.2uF, 10%, 6.3V, CERN, 603), C3102 (10uF, 20%, 6.3V, CERN, 805-1), and C3105 (150uF, 20%, 6.3V, POLY, SMC-LP) are shown. A note states: "If power inputs are not S0, MEMVTT_EN can be used to disable MEMVTT in sleep." A question mark is present: "?Can 5V be S0 if 1V8 is S3?"

SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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SIZE D	DRAWING NUMBER 051-7124	REV. H
SCALE NONE	SHT 31	OF 111



FCTSEL1	FCTSEL0	PIN 6	PIN 7	PIN 10	PIN 11
0	0	DOT96T	DOT96C	100MT_SST	100MC_SST
0	1	DOT96T	DOT96C	SRCT0	SRCC0
1	0	27M_NON_SPREAD	27M_SPREAD	SRCT0	SRCC0
1	1	OFF LOW	TBD	SRCT0	SRCC0

* FOR INT. GRAPHIC SYSTEM

* FOR EXT. GRAPHIC SYSTEM

CLOCKS

SYNC_MASTER=CLOCK

SYNC_DATE=06/03/2005

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SCALE
NONE

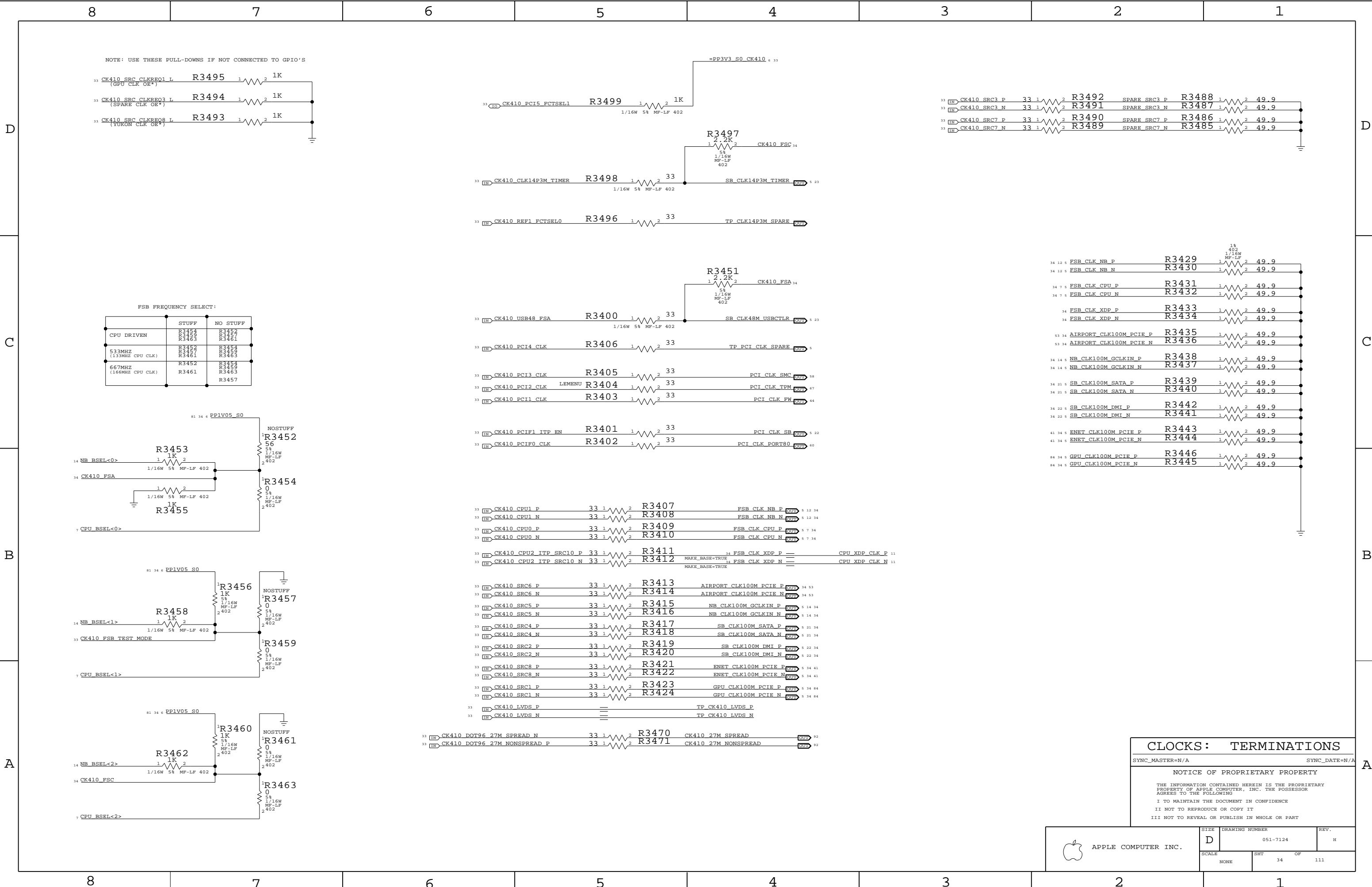
SIZE
D

DRAWING NUMBER
051-7124

SHT
33

REV.
H

OF
111



CLOCKS: TERMINATIONS

SYNC_MASTER=N/A SYNC_DATE=N/A

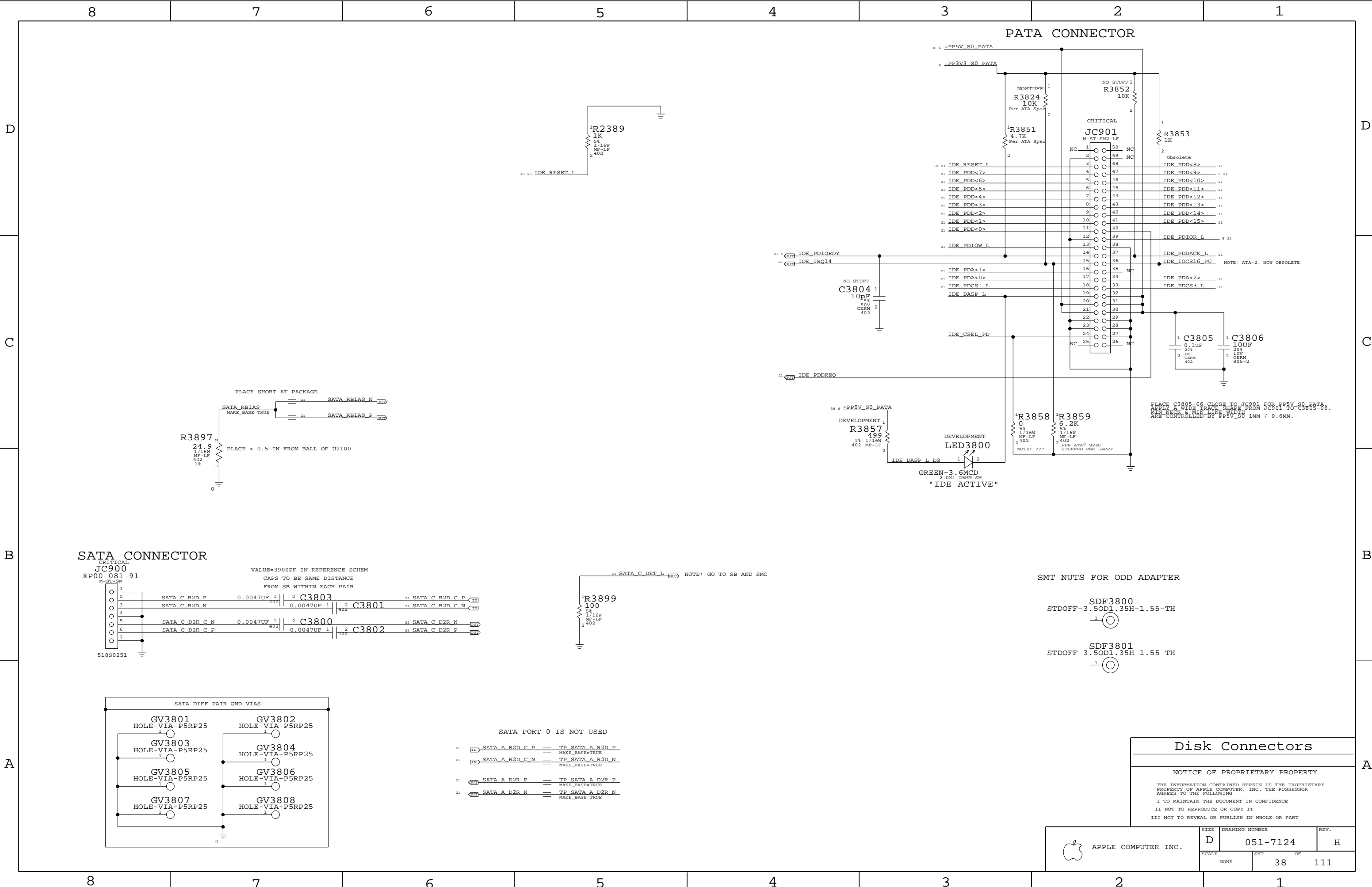
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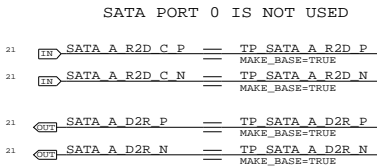
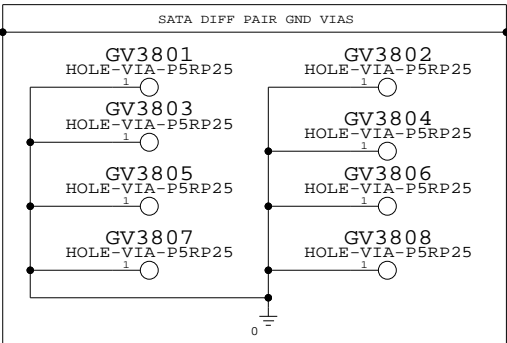
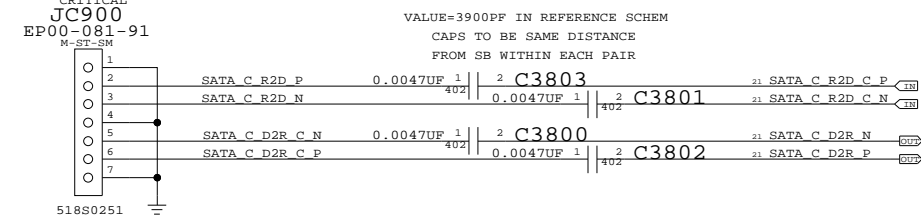
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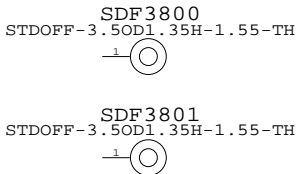
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SATA CONNECTOR



SMT NUTS FOR ODD ADAPTER



Disk Connectors

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SIZE D

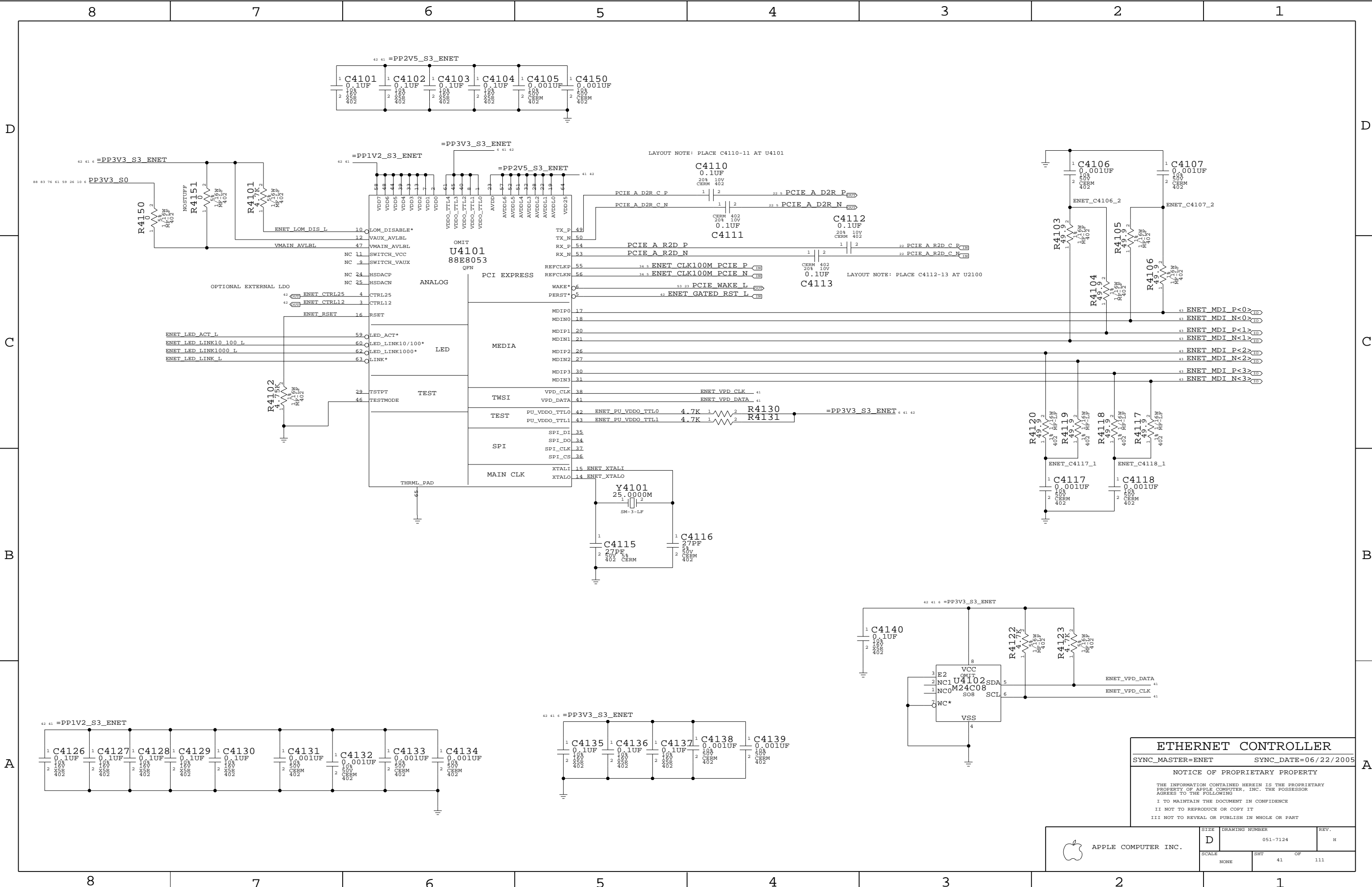
DRAWING NUMBER 051-7124

SCALE NONE

REV. H

SHT 38

OF 111



ETHERNET CONTROLLER
SYNC_MASTER=ENET SYNC_DATE=06/22/2005

NOTICE OF PROPRIETARY PROPERTY

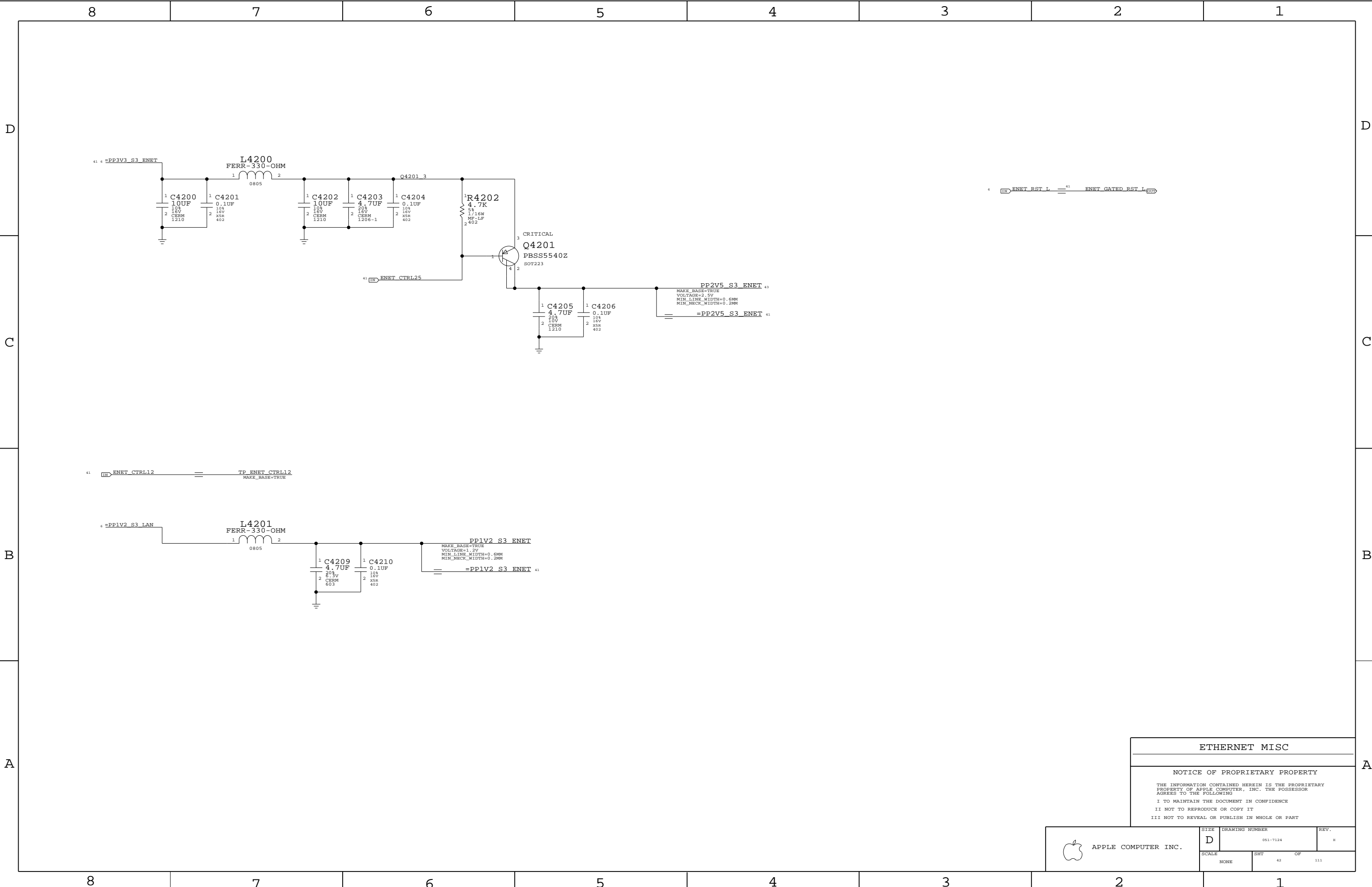
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 41	OF 111



ETHERNET MISC

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7124

REV.

H

SCALE

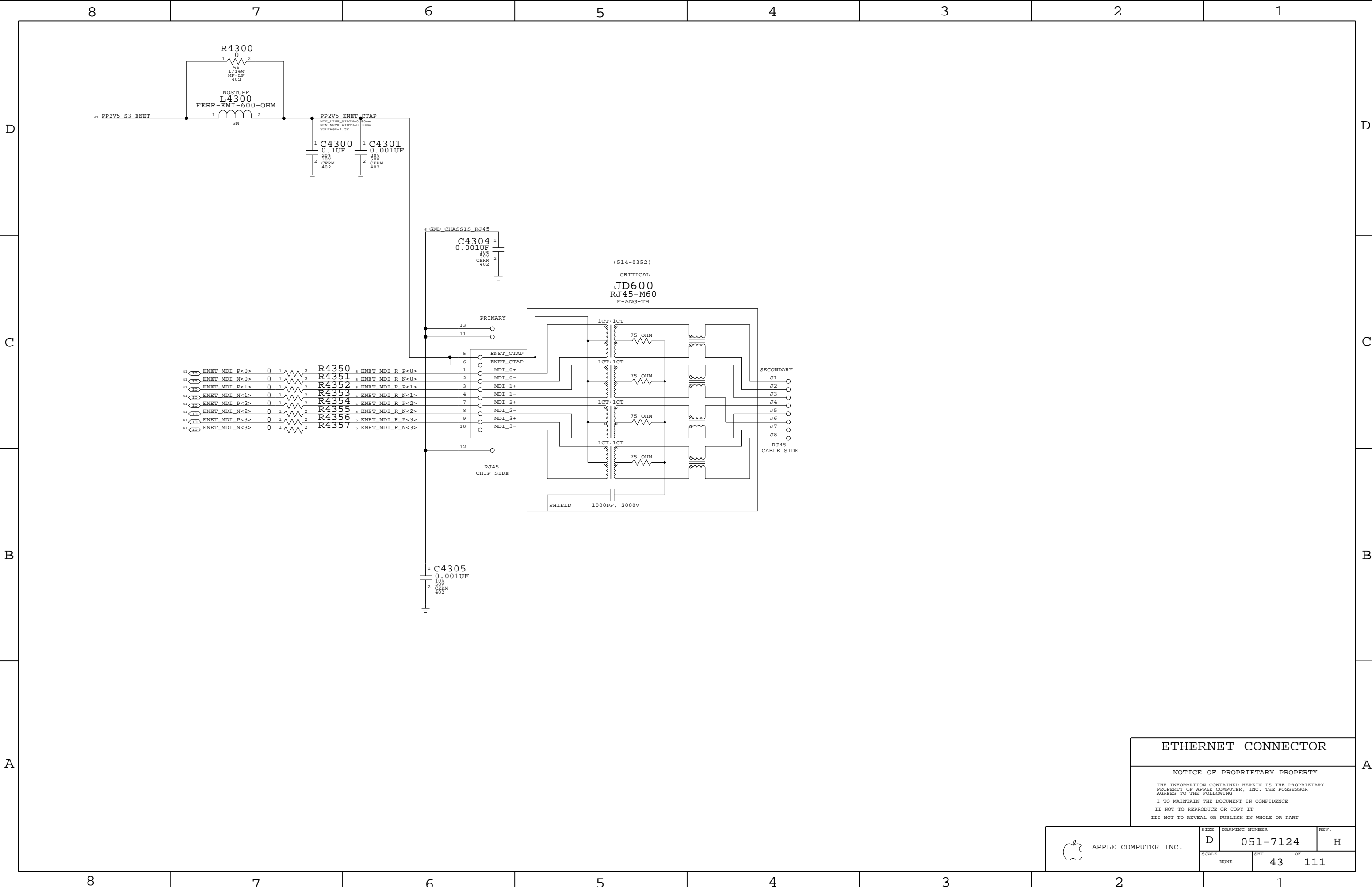
NONE

SHT

42

OF

111



ETHERNET CONNECTOR

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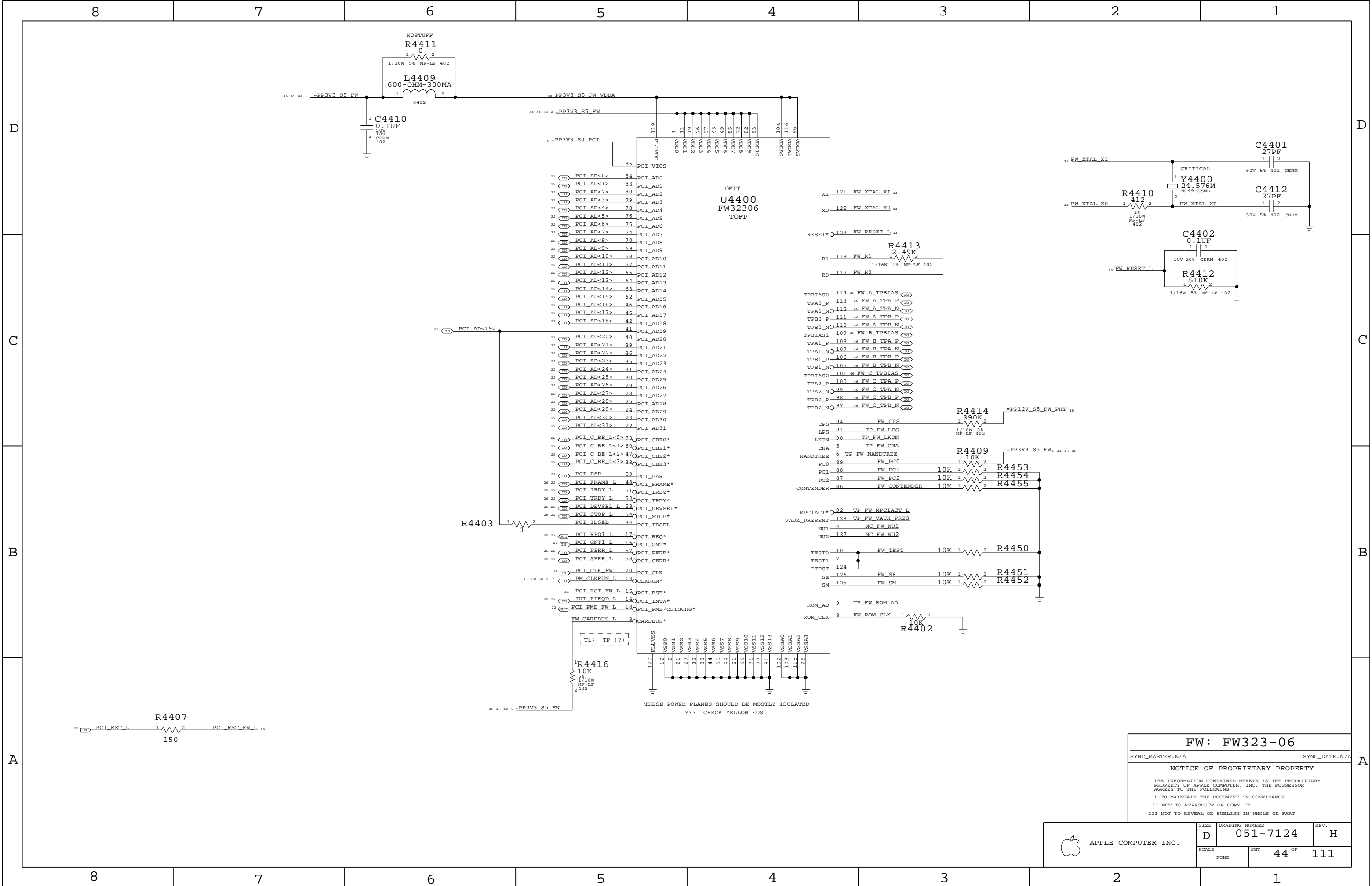
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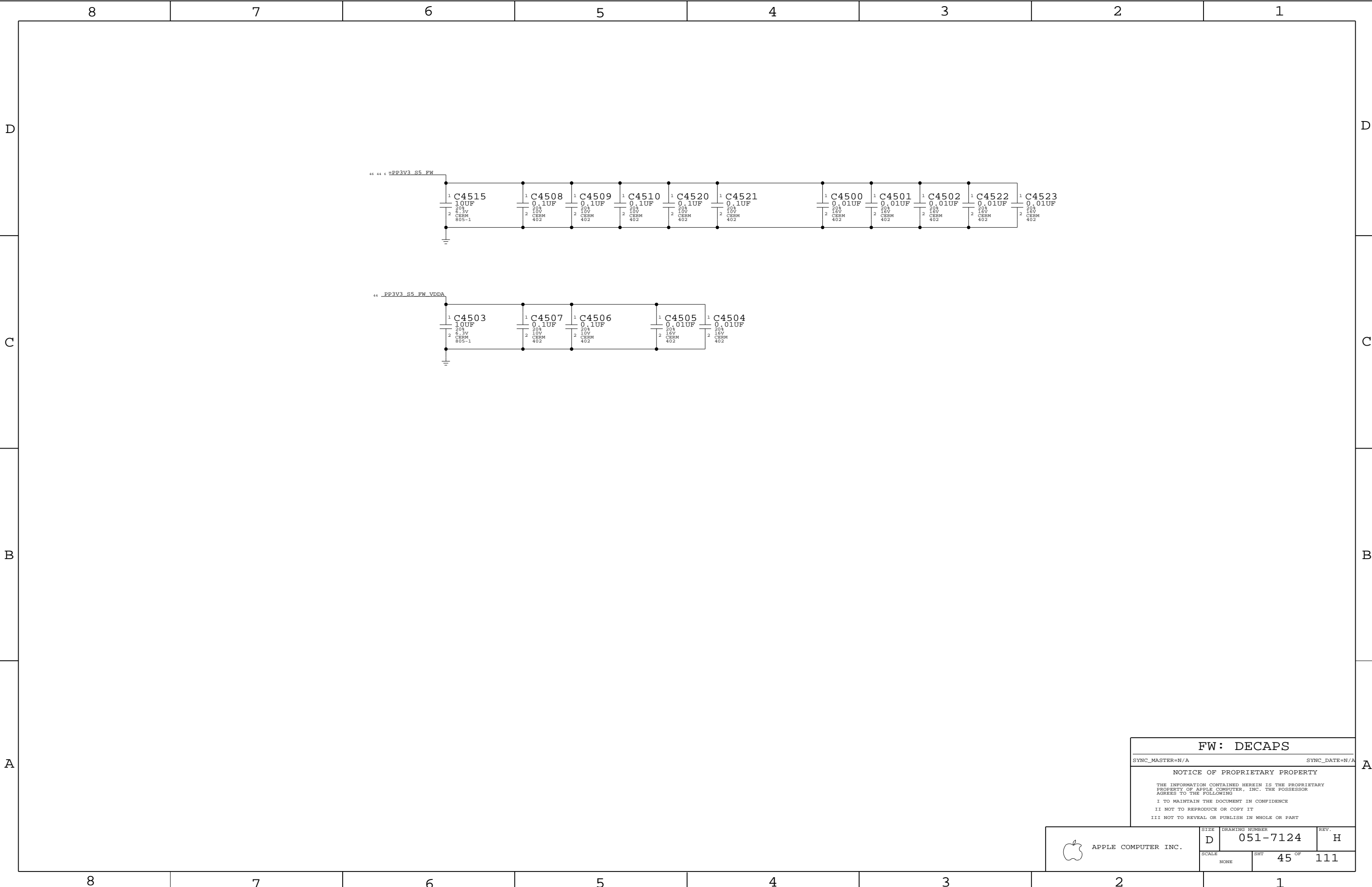
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	43	111





FW: DECAPS

SYNC_MASTER=N/A

SYNC_DATE=N/A


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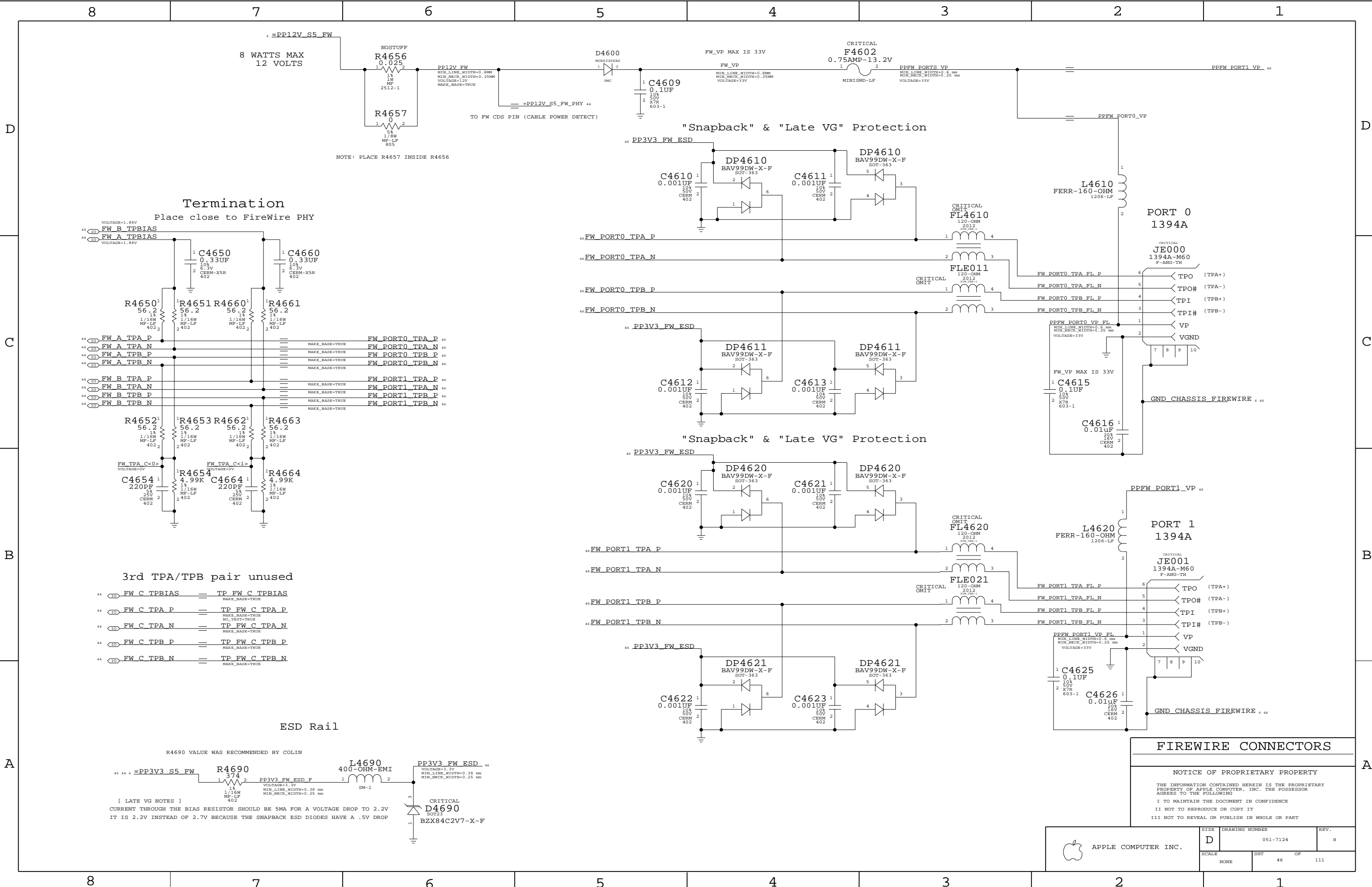
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SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	45	111



Termination
Place close to FireWire PHY

3rd TPA/TPB pair unused

ESD Rail

R4690 VALUE WAS RECOMMENDED BY COLIN

[LATE VG NOTES]
CURRENT THROUGH THE BIAS RESISTOR SHOULD BE 5MA FOR A VOLTAGE DROP TO 2.2V
IT IS 2.2V INSTEAD OF 2.7V BECAUSE THE SNAPBACK ESD DIODES HAVE A .5V DROP

"Snapback" & "Late VG" Protection

"Snapback" & "Late VG" Protection

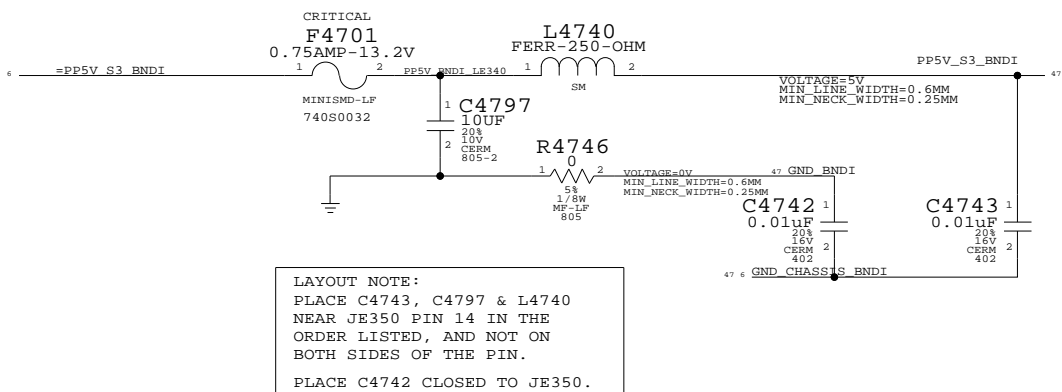
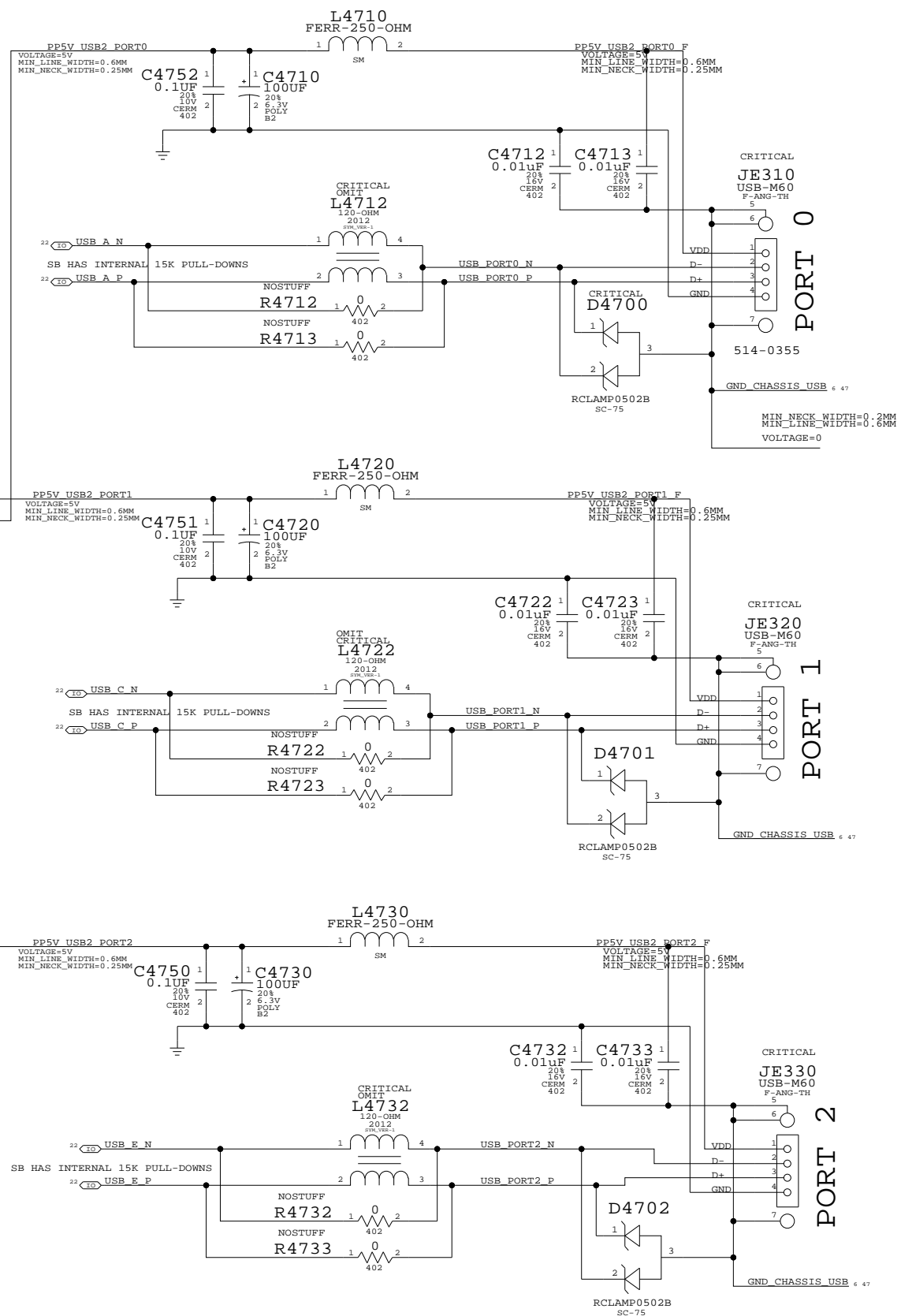
FIREWIRE CONNECTORS

NOTICE OF PROPRIETARY PROPERTY

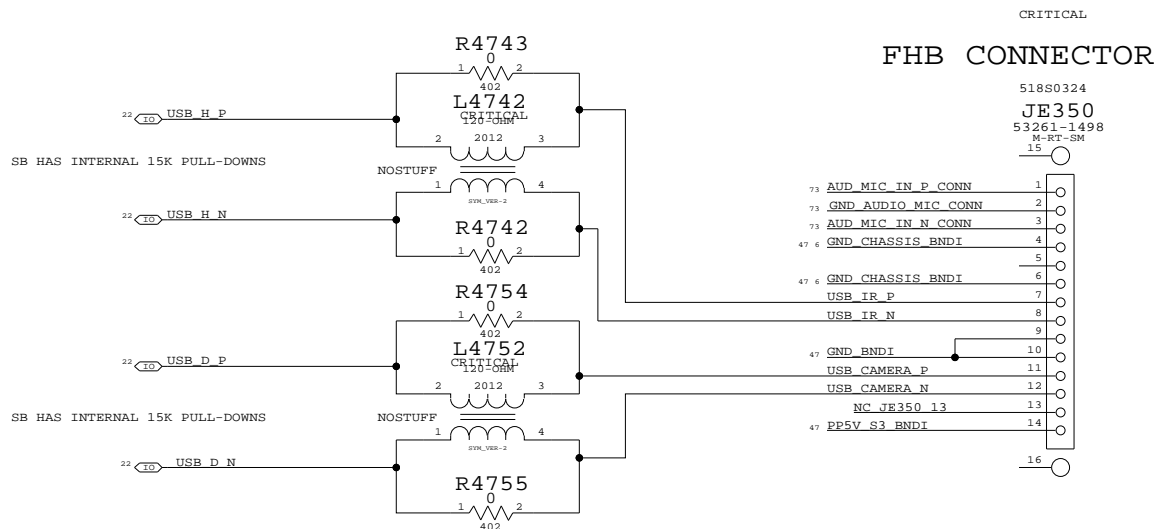
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	SCALE NONE	SHT 46	OF 111

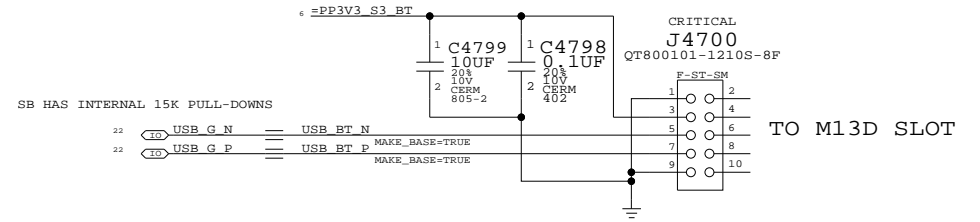
External USB Ports



LAYOUT NOTE:
PLACE C4743, C4797 & L4740
NEAR JE350 PIN 14 IN THE
ORDER LISTED, AND NOT ON
BOTH SIDES OF THE PIN.
PLACE C4742 CLOSED TO JE350.



BLUETOOTH



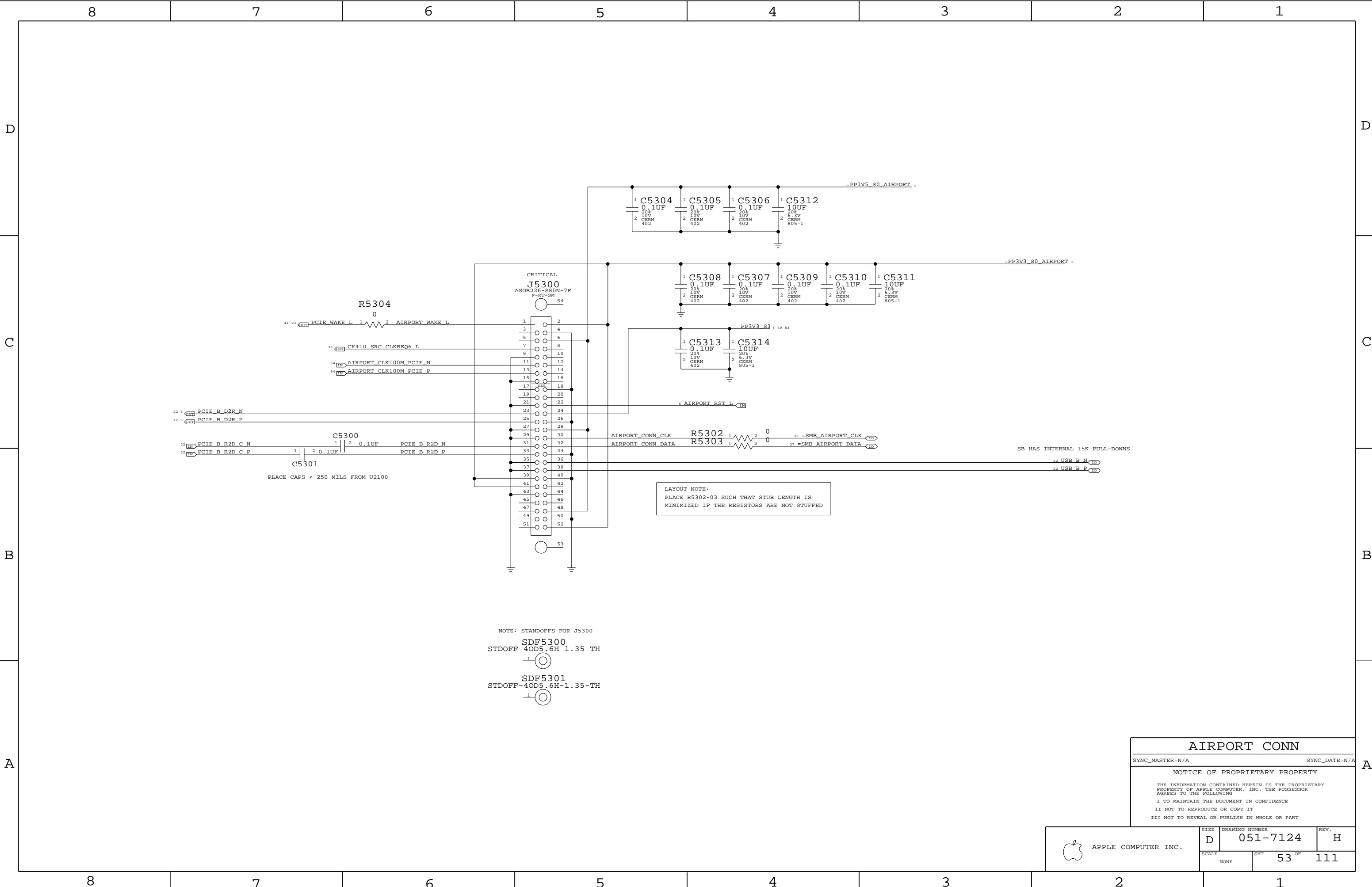
NOTE: STANDOFFS FOR J4700
SDF4700
STDOFF-40D4.5H-1.35-TH
SDF4701
STDOFF-40D4.5H-1.35-TH

USB Device Interfaces

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PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0355	3	USB RECEPTACLE, 4P, RIBLESS	JE310, JE320, JE330	CRITICAL	20_INCH_LCD

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 47	OF 111



AIRPORT CONN

SYNC_MASTER=N/A

SYNC_DATE=N/A


NOTICE OF PROPRIETARY PROPERTY

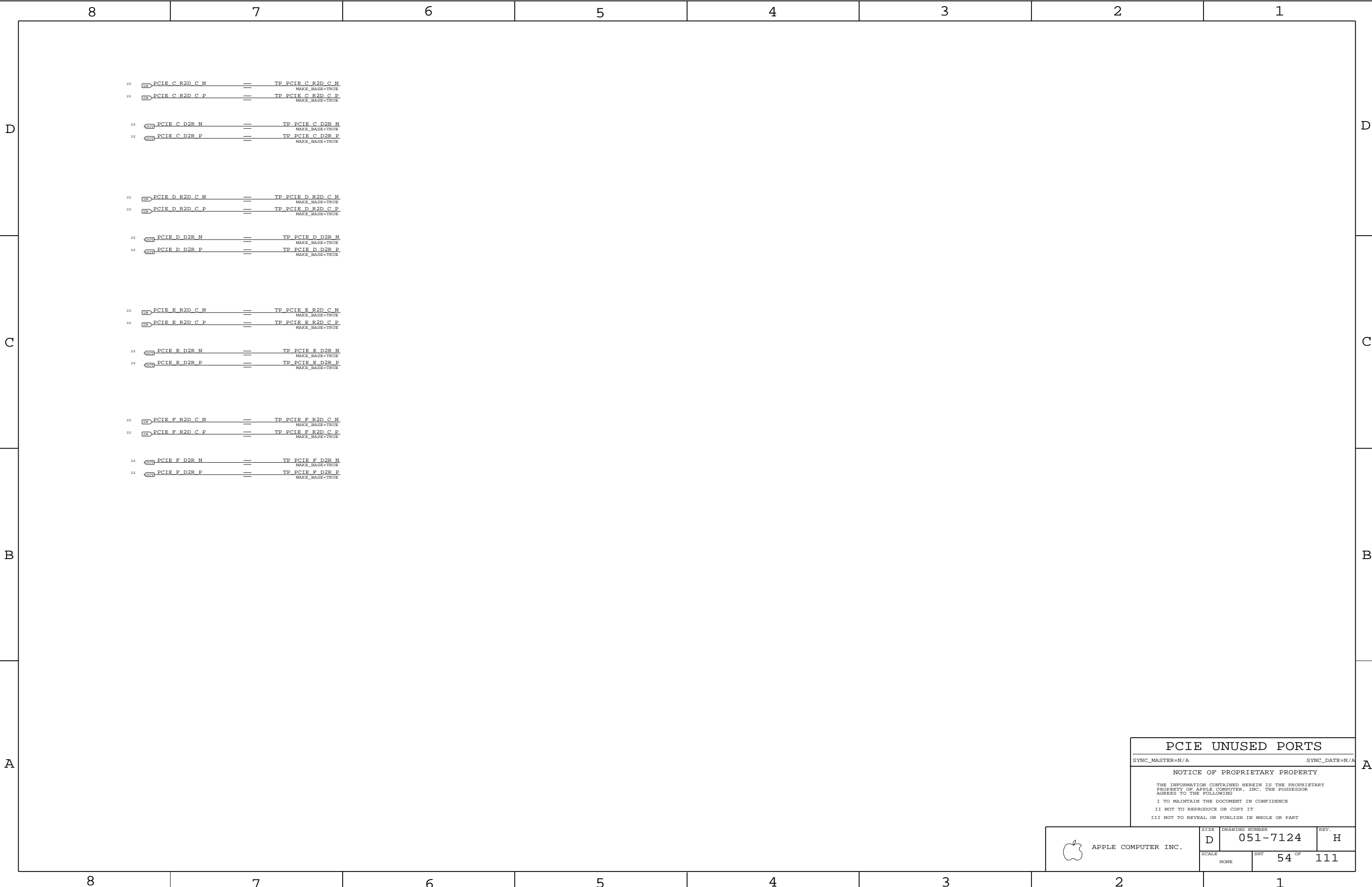
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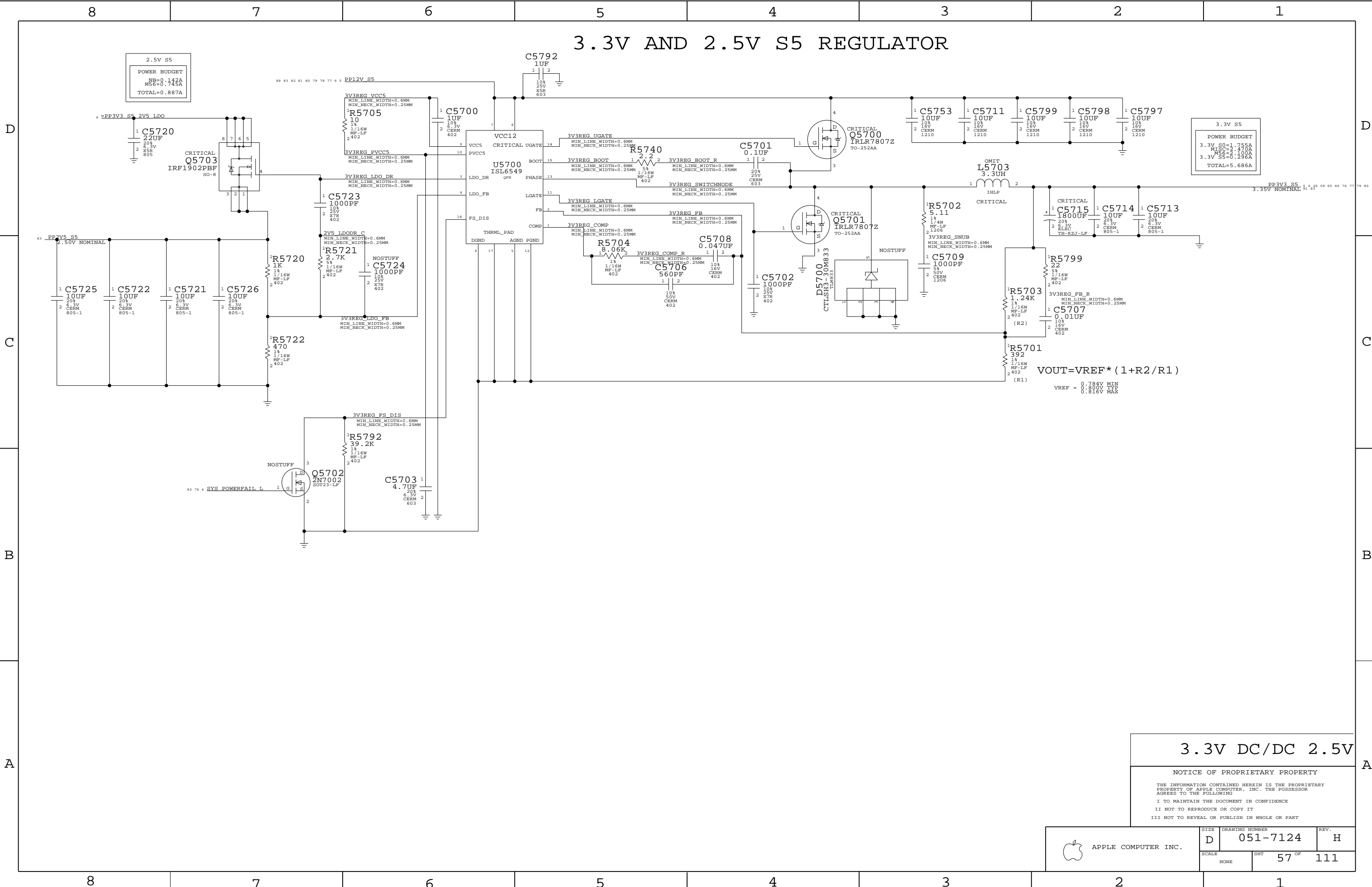
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

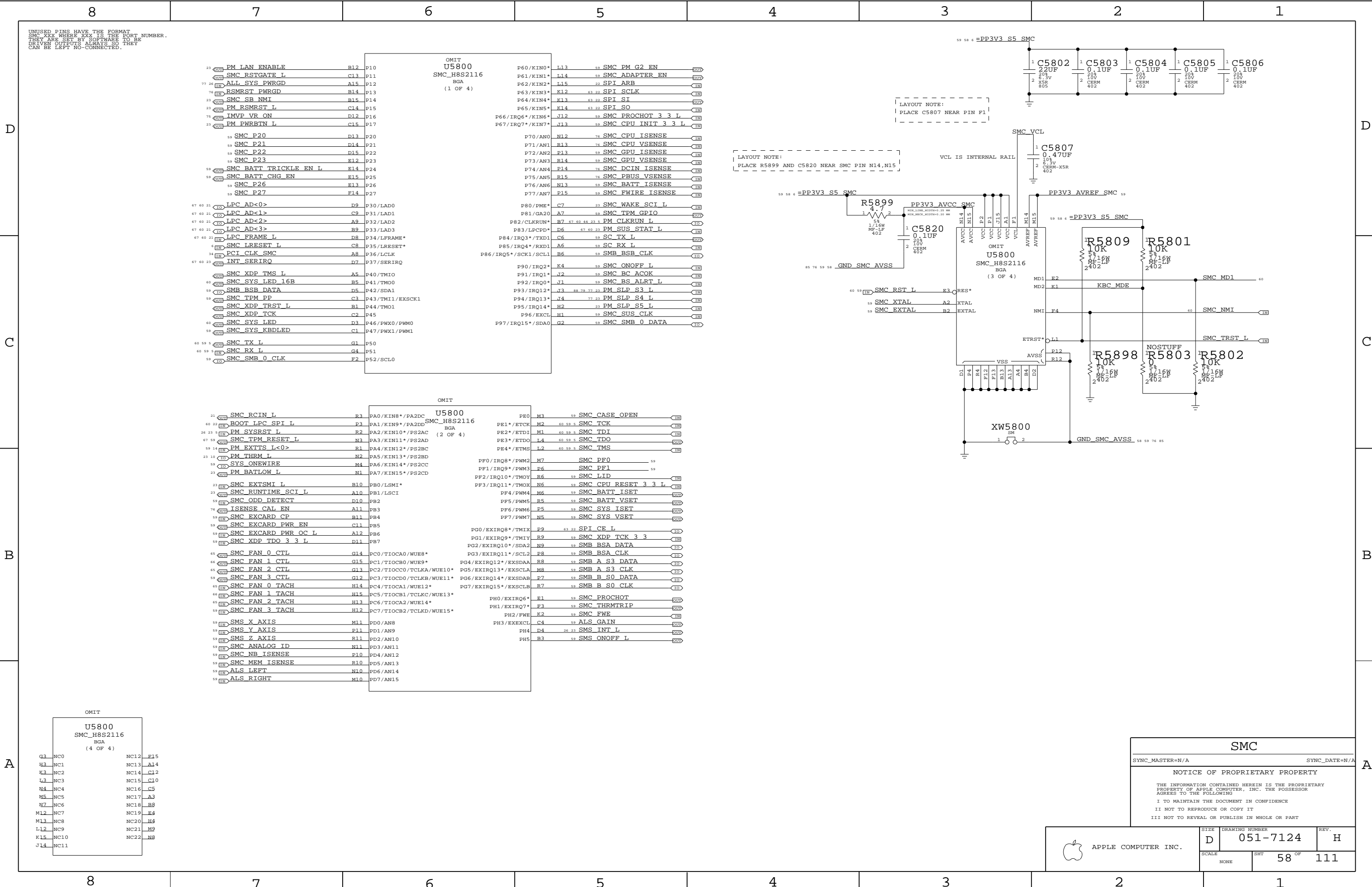
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

 APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE		SHT	OF
NONE		53	111







SMC

SYNC_MASTER=N/A SYNC_DATE=N/A

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D

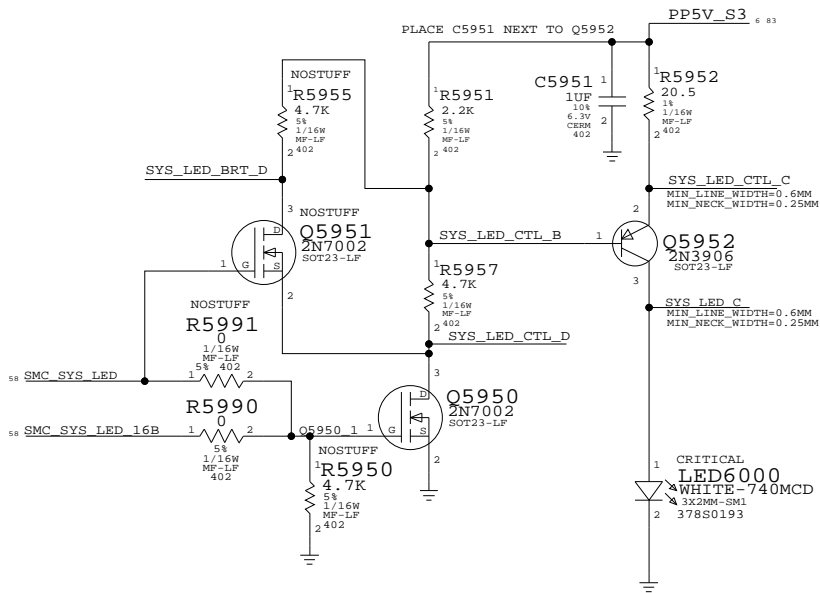
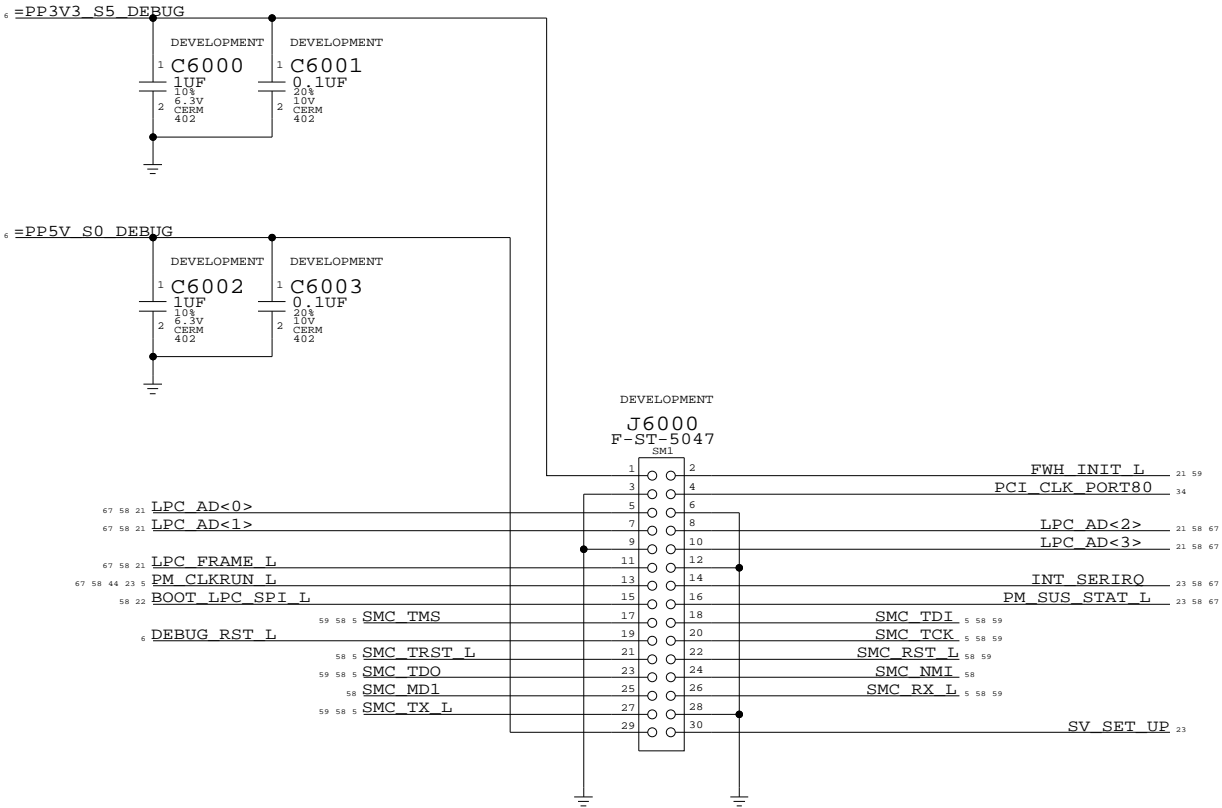
C

B

A

8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1



LPC+ CONN

SYNC_MASTER=N/A SYNC_DATE=N/A

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7124

REV.

H

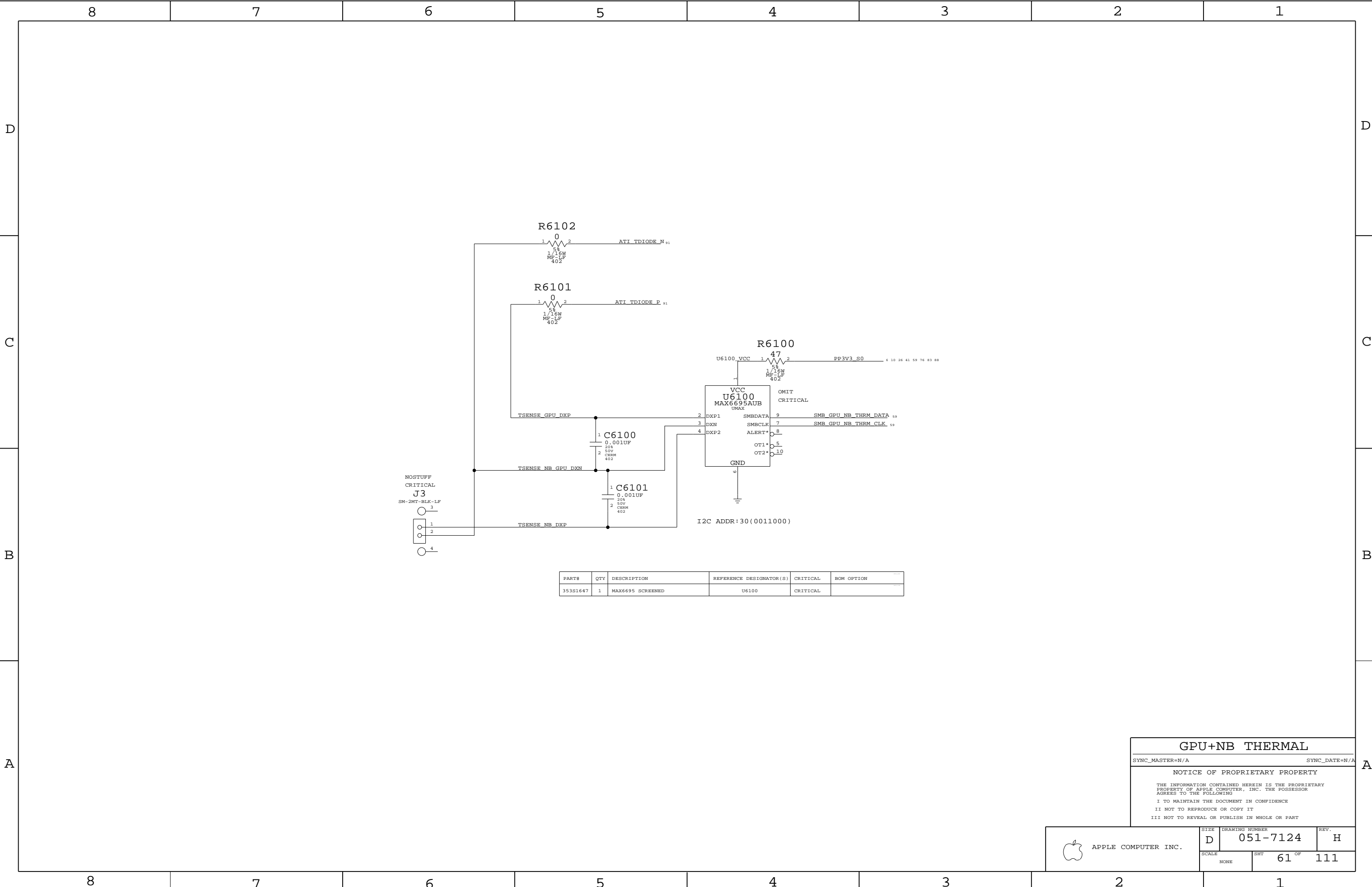
SCALE

NONE

SHT

60

111



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
353S1647	1	MAX6695 SCREENED	U6100	CRITICAL	

GPU+NB THERMAL

SYNC_MASTER=N/A

SYNC_DATE=N/A

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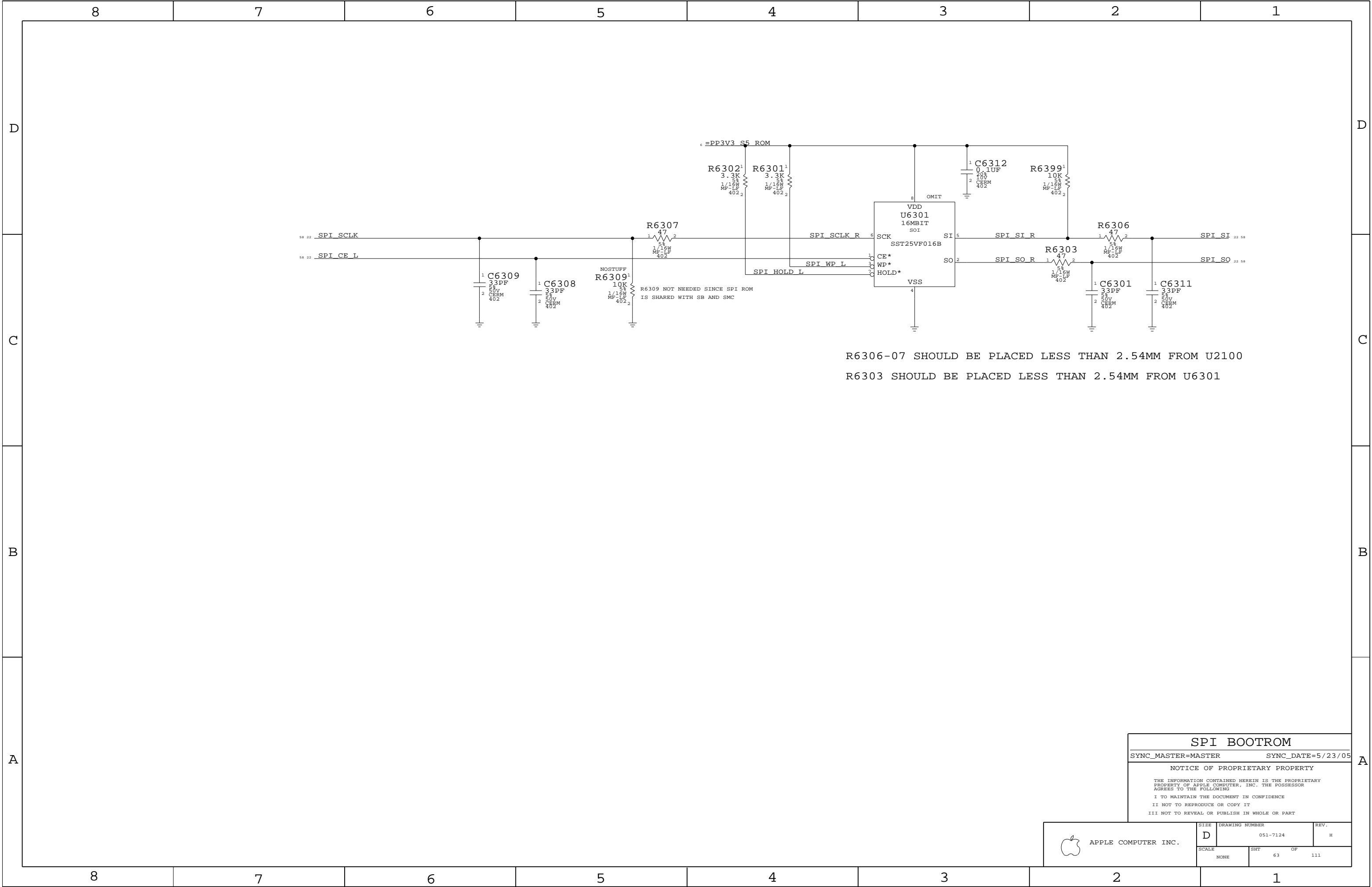
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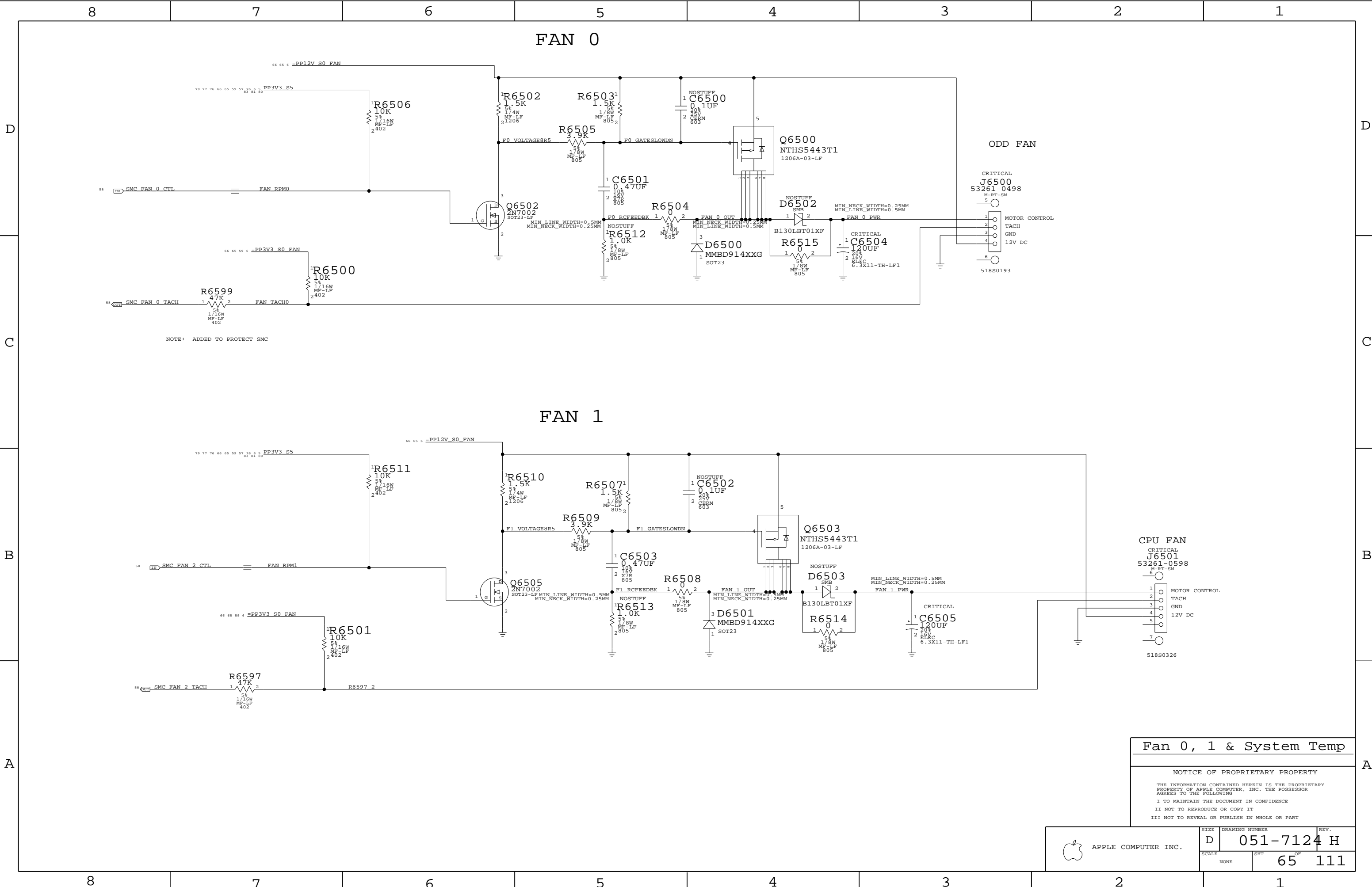
DRAWING NUMBER 051-7124

REV. H

SCALE NONE

SHT 61 OF 111





Fan 0, 1 & System Temp

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT		REV.
	NONE		65 ^{OF} 111

D

C

B

A

D

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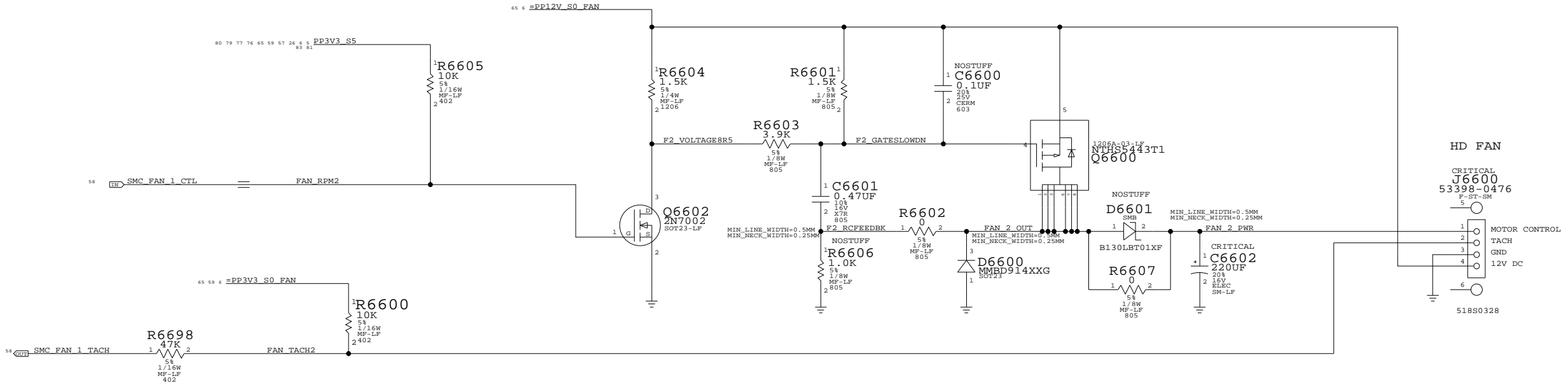
B

A

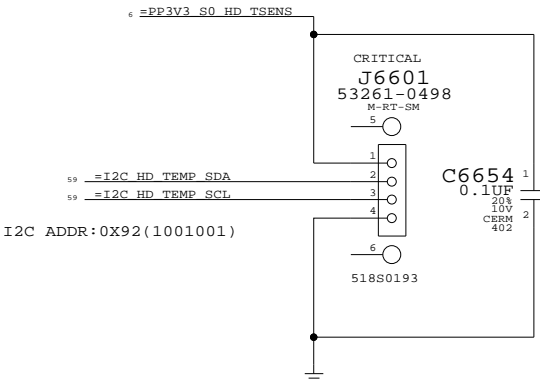
8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1

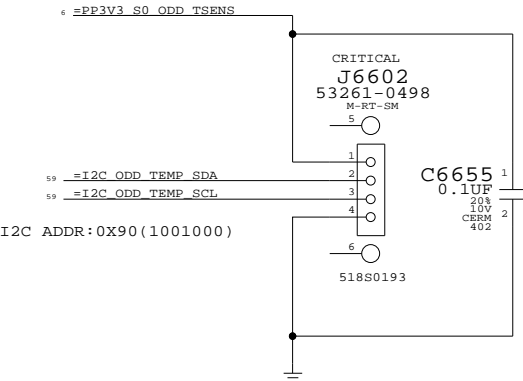
FAN 2



HD TEMP SENSOR



ODD TEMP SENSOR



Fan 2 & HD Temp

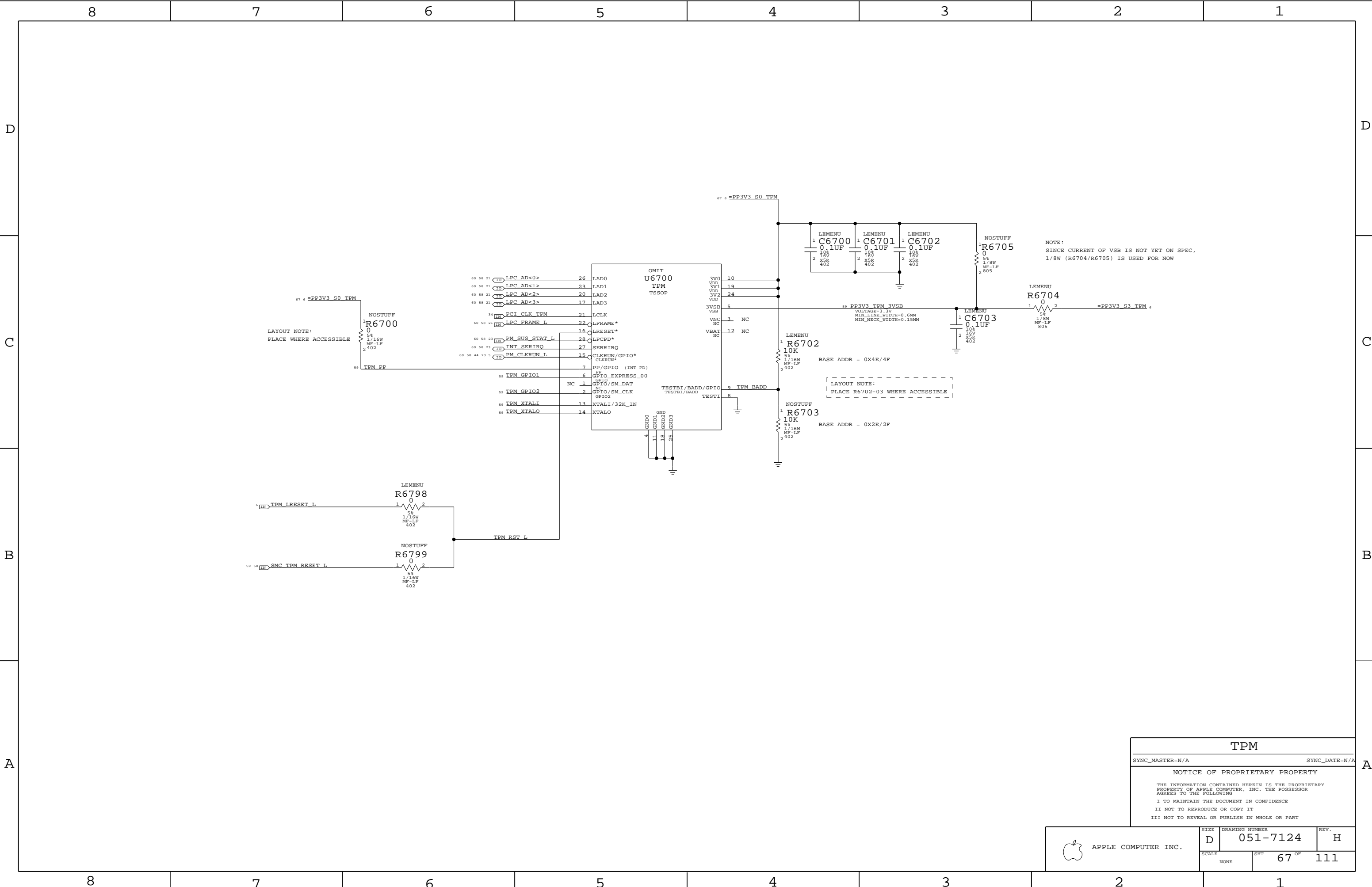
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SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	111
NONE	66 OF	



TPM

SYNC_MASTER=N/A SYNC_DATE=N/A

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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 67 OF 111	

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87654321

87654321

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0091	126S0092		C6802,C6803	FACTORY SHORTAGE
126S0091	126S0092		C7403,C7404	FACTORY SHORTAGE
353S1345	353S1458		U6800	FACTORY SHORTAGE

MIN LINE WIDTH=0.6MM
MIN NECK WIDTH=0.25MM
VOLTAGE=5V

=PP4V5_S0_AUDIO_ANALOG

L6802
180-OHM-1.5A

0603-LF

PP4V5 AUDIO ANALOG 68 74

AUDIO: CODEC

SYNC_MASTER=AUDIO SYNC_DATE=02/23/2006

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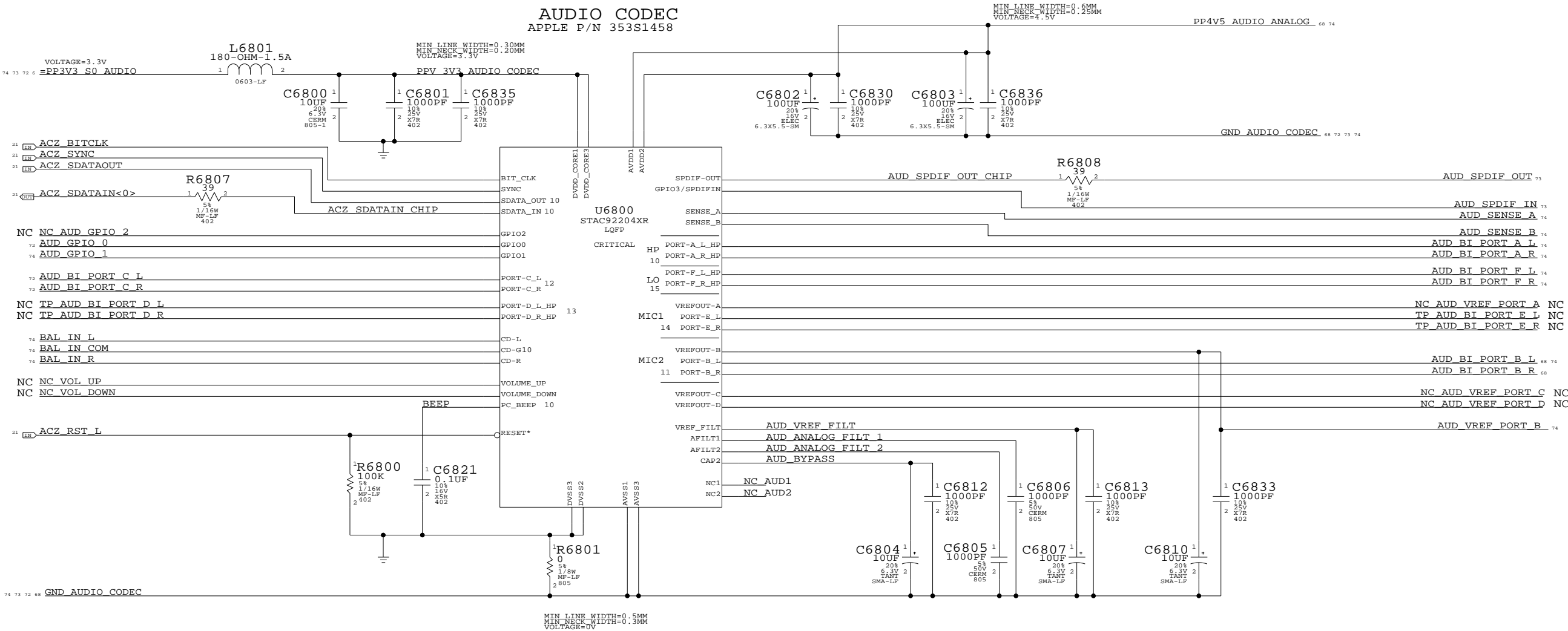
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



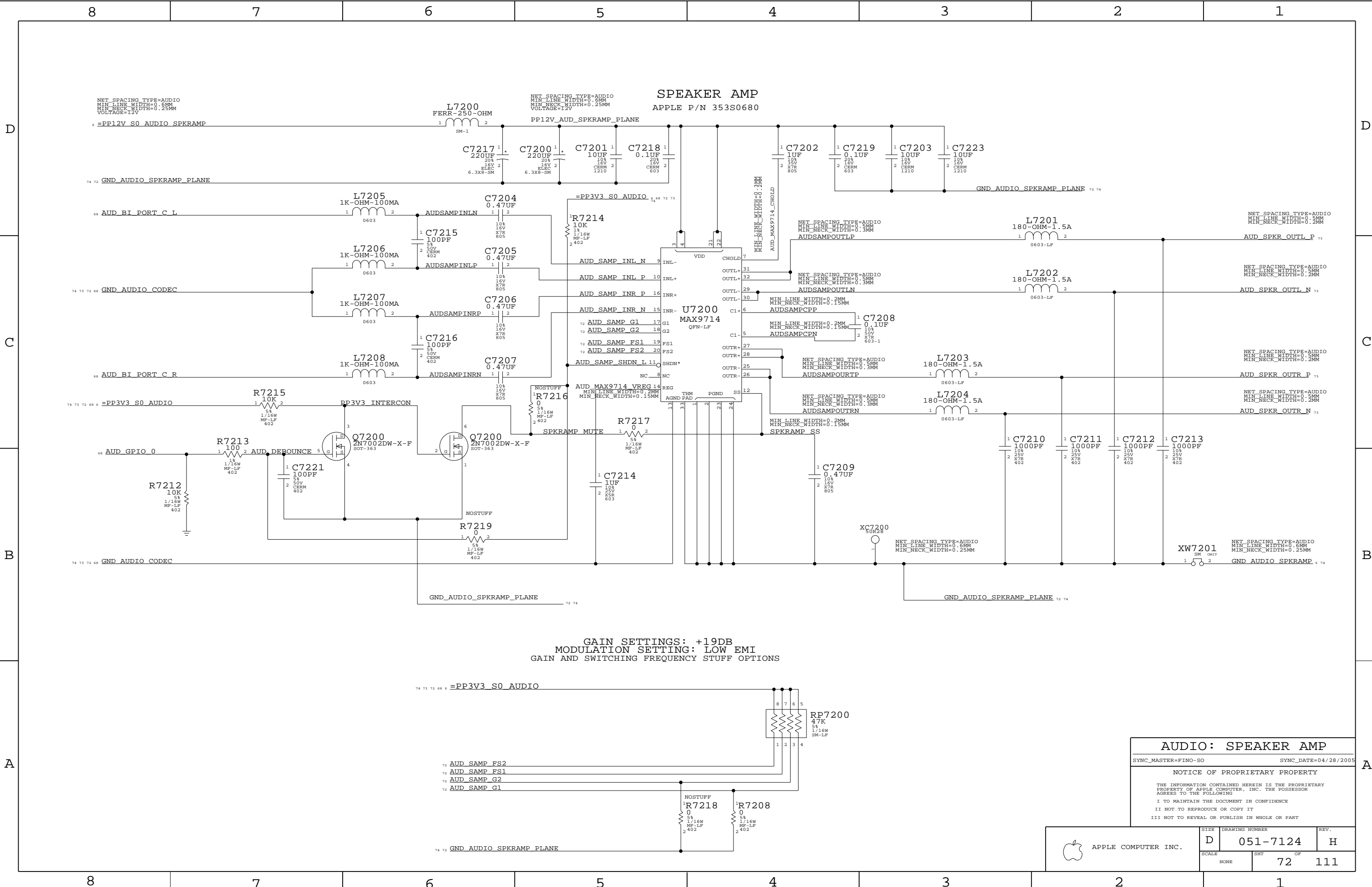
APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-7124 REV. H

SCALE NONE SHT 68 OF 111



AUD BI PORT B L 68
AUD BI PORT B R 68
MIC INPUT TO BOTH L&R



AUDIO: SPEAKER AMP

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005

NOTICE OF PROPRIETARY PROPERTY

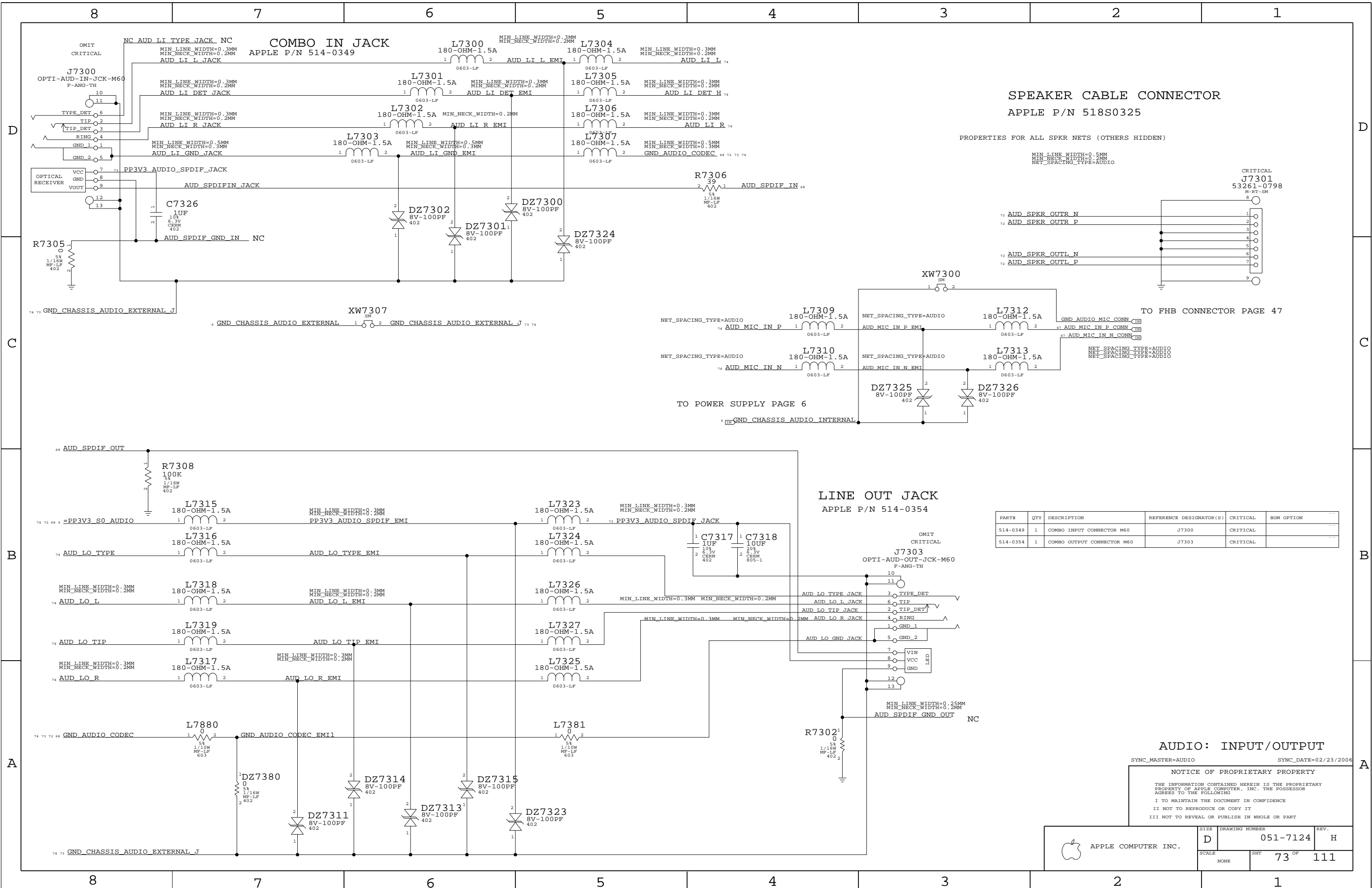
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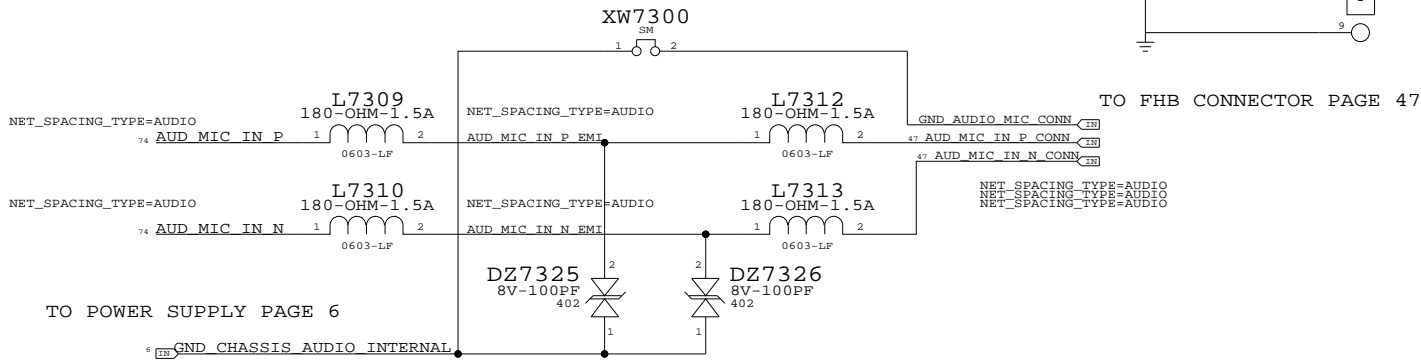
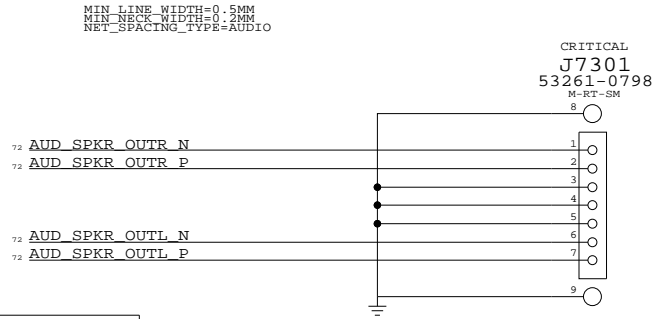
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT OF		
	NONE	72	111



SPEAKER CABLE CONNECTOR

APPLE P/N 518S0325

PROPERTIES FOR ALL SPKR NETS (OTHERS HIDDEN)



LINE OUT JACK

APPLE P/N 514-0354

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0349	1	COMBO INPUT CONNECTOR M60	J7300	CRITICAL	
514-0354	1	COMBO OUTPUT CONNECTOR M60	J7303	CRITICAL	

AUDIO: INPUT/OUTPUT

SYNC_MASTER=AUDIO SYNC_DATE=02/23/2006

NOTICE OF PROPRIETARY PROPERTY

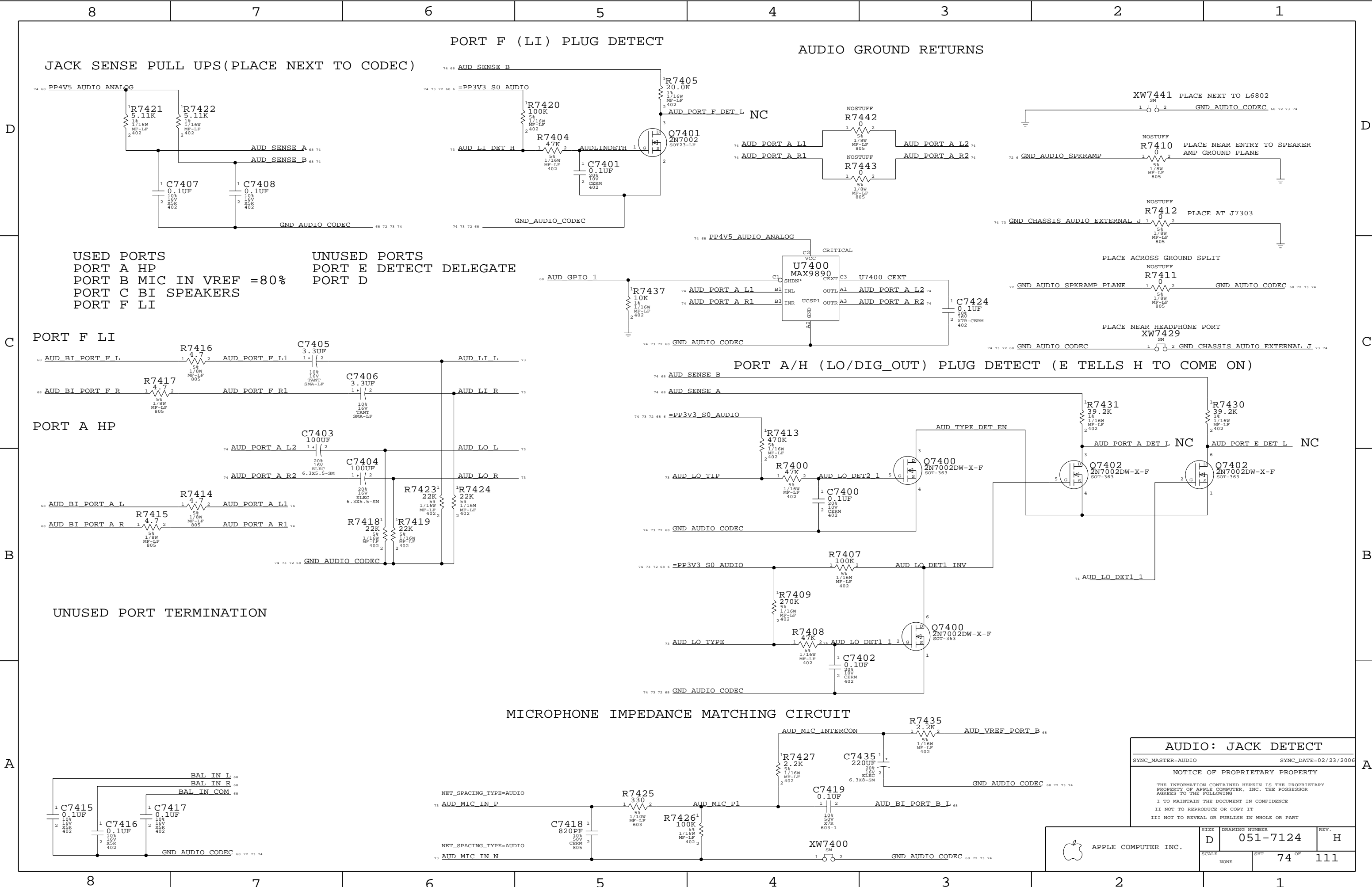
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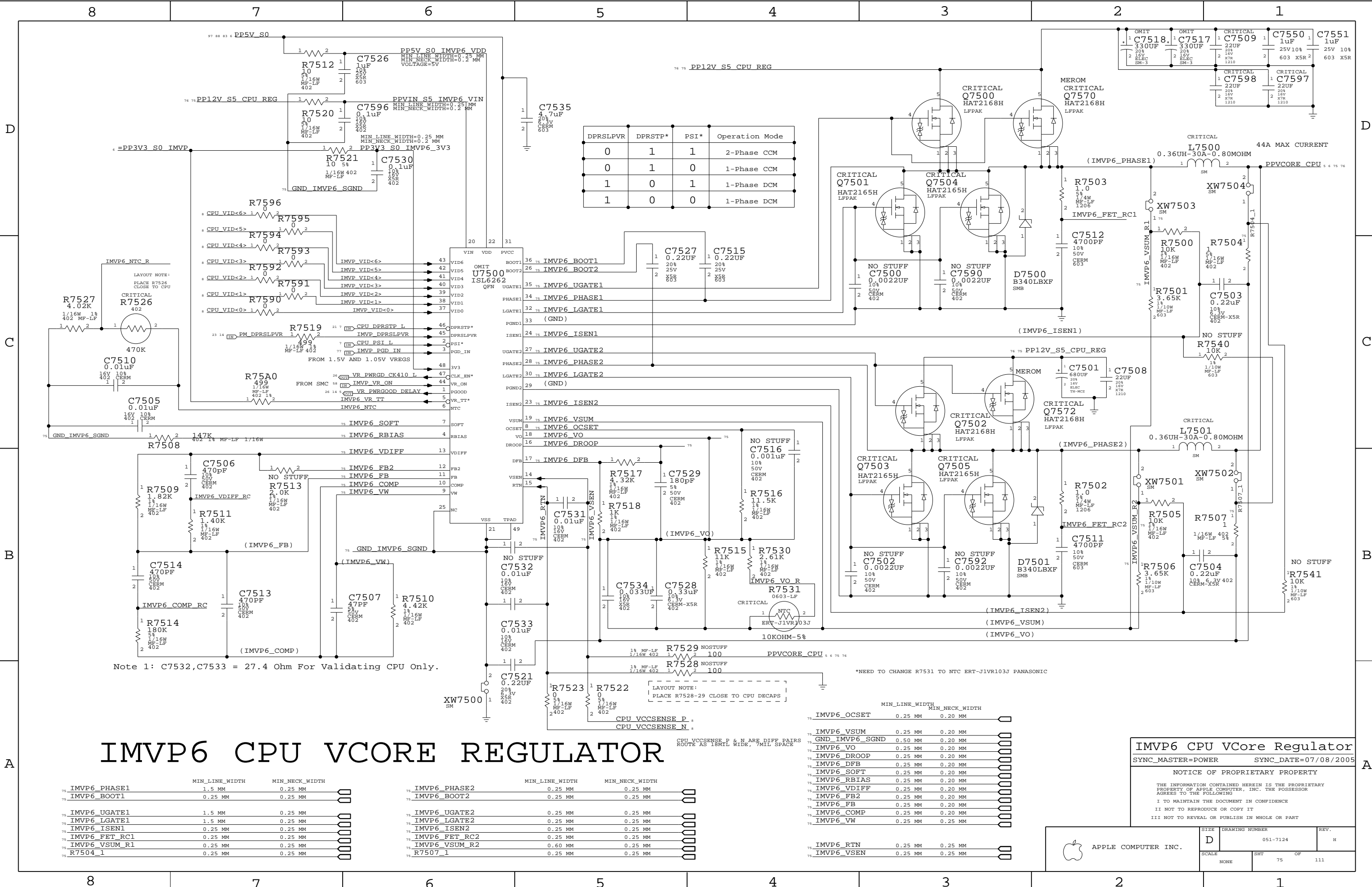
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT		REV.
	NONE		73 OF 111





IMVP6 CPU VCore Regulator

	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_PHASE1	1.5 MM	0.25 MM
IMVP6_BOOT1	0.25 MM	0.25 MM
IMVP6_UGATE1	1.5 MM	0.25 MM
IMVP6_LGATE1	1.5 MM	0.25 MM
IMVP6_ISEN1	0.25 MM	0.25 MM
IMVP6_FET_RC1	0.25 MM	0.25 MM
IMVP6_VSUM_R1	0.25 MM	0.25 MM
R7504_1	0.25 MM	0.25 MM

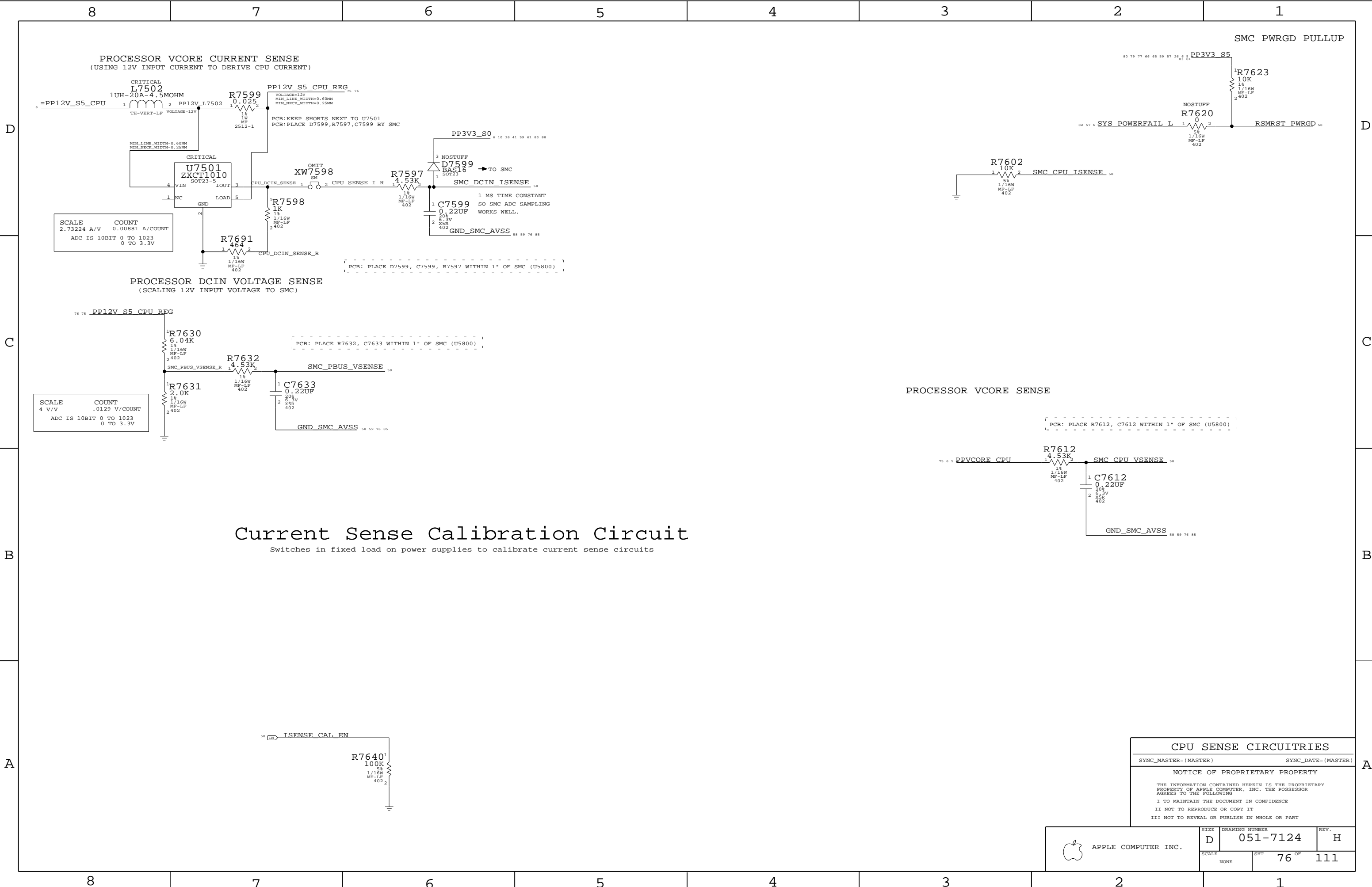
	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_PHASE2	0.25 MM	0.25 MM
IMVP6_BOOT2	0.25 MM	0.25 MM
IMVP6_UGATE2	0.25 MM	0.25 MM
IMVP6_LGATE2	0.25 MM	0.25 MM
IMVP6_ISEN2	0.25 MM	0.25 MM
IMVP6_FET_RC2	0.25 MM	0.25 MM
IMVP6_VSUM_R2	0.60 MM	0.25 MM
R7507_1	0.25 MM	0.25 MM

	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_OCSET	0.25 MM	0.20 MM
IMVP6_VSUM	0.25 MM	0.20 MM
GND_IMVP6_SGND	0.50 MM	0.20 MM
IMVP6_VO	0.25 MM	0.20 MM
IMVP6_DROOP	0.25 MM	0.20 MM
IMVP6_DFB	0.25 MM	0.20 MM
IMVP6_SOFT	0.25 MM	0.20 MM
IMVP6_RBIAS	0.25 MM	0.20 MM
IMVP6_VDIFF	0.25 MM	0.20 MM
IMVP6_FB2	0.25 MM	0.20 MM
IMVP6_FB	0.25 MM	0.20 MM
IMVP6_COMP	0.25 MM	0.20 MM
IMVP6_VW	0.25 MM	0.25 MM
IMVP6_RTIN	0.25 MM	0.25 MM
IMVP6_VSEN	0.25 MM	0.25 MM

IMVP6 CPU VCore Regulator
SYNC_MASTER=POWER SYNC_DATE=07/08/2005

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	D	051-7124	H
SCALE	NONE	SHT	75 OF 111



Current Sense Calibration Circuit

Switches in fixed load on power supplies to calibrate current sense circuits

CPU SENSE CIRCUITRIES

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

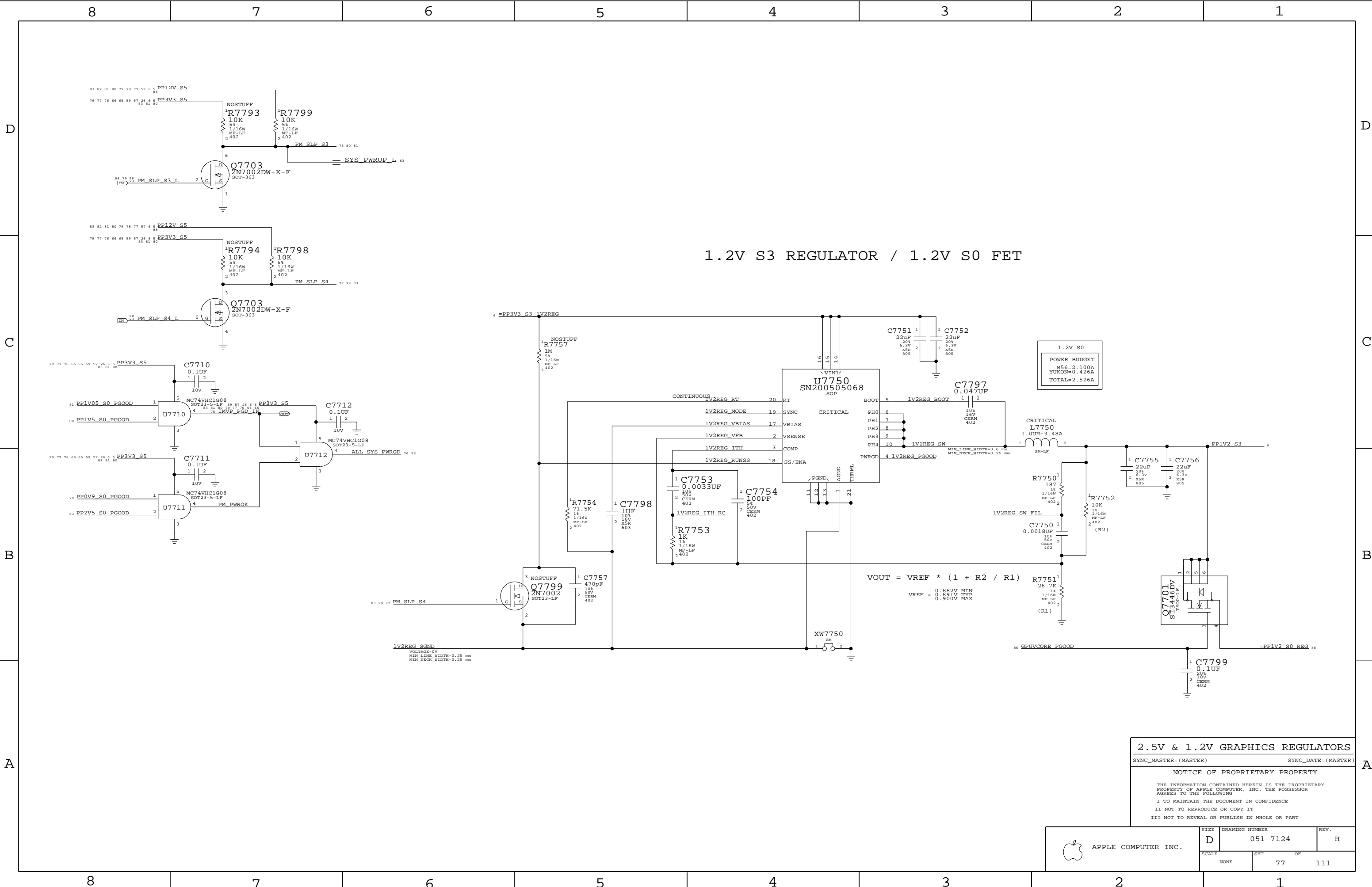
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. H
	SCALE NONE	SHT 76 OF 111	



2.5V & 1.2V GRAPHICS REGULATORS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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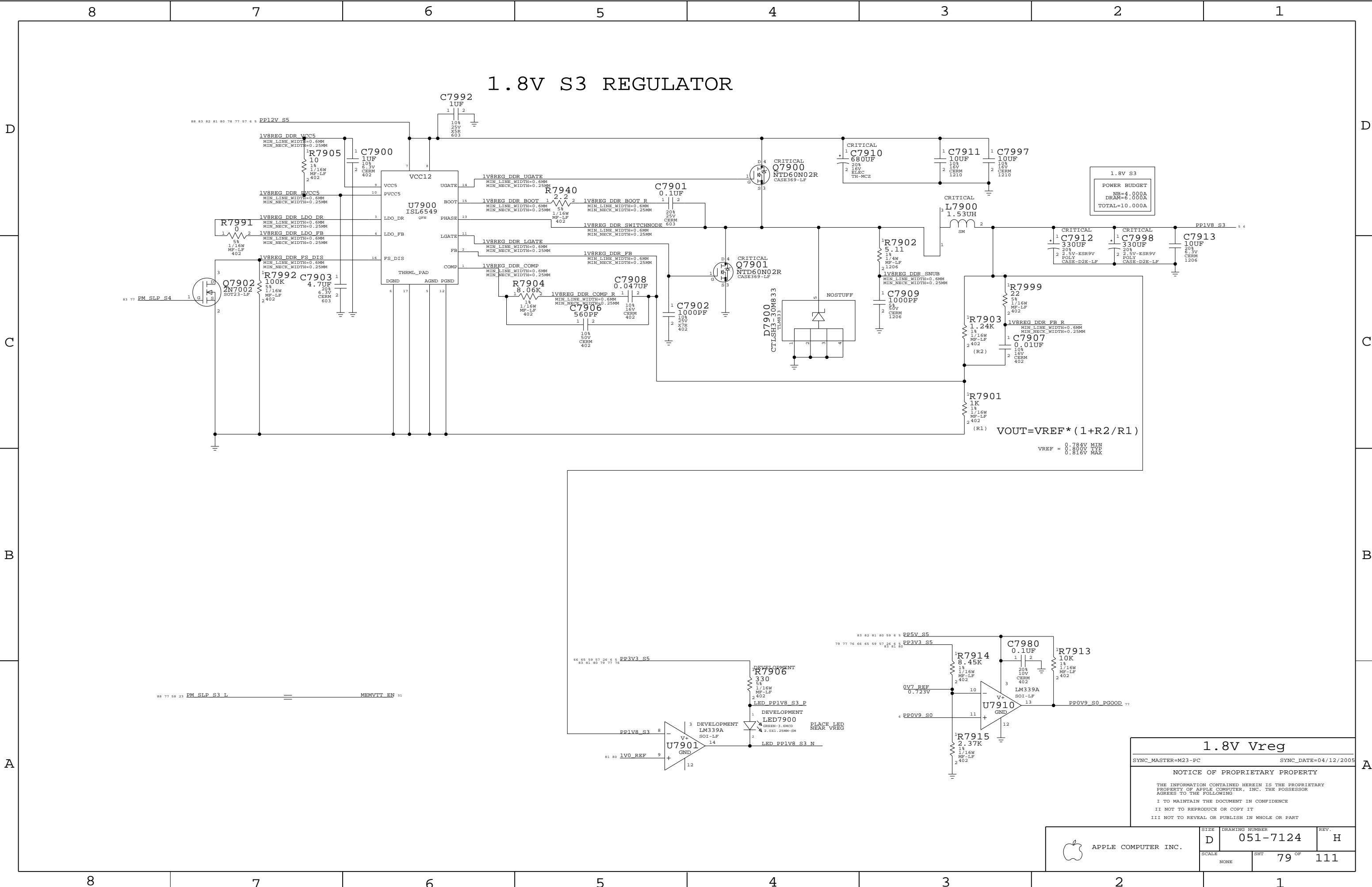
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	77	111



1.8V Vreg

SYNC_MASTER=M23-PC

SYNC_DATE=04/12/2005

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT	79 ^{OF}	111
NONE			

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1.5V Vreg
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SYNC_MASTER=FINO-PC                               SYNC_DATE=05/18/2005

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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
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D	051-7124	
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D	951, 121
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SCALE	SHT	80	OF	11
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NONE	00	11
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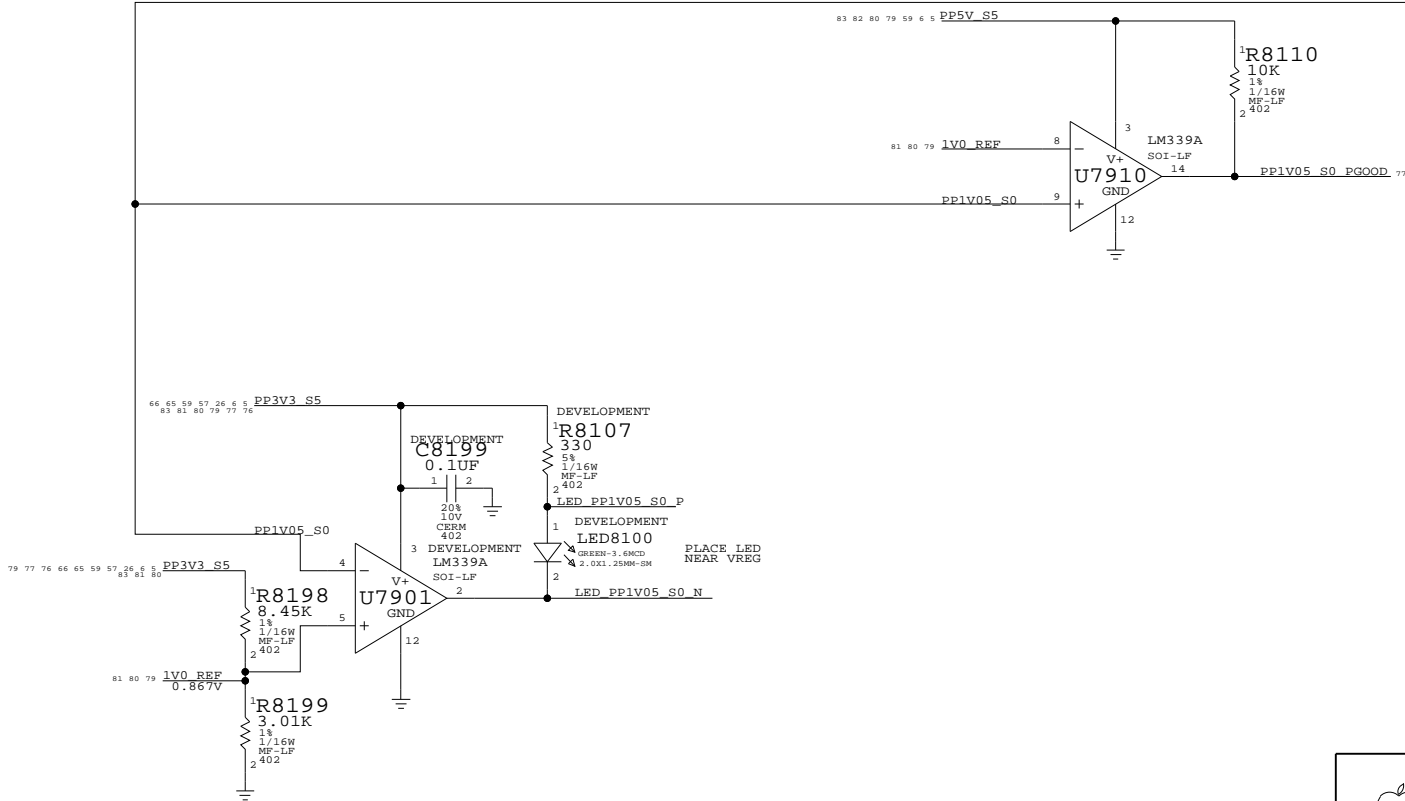
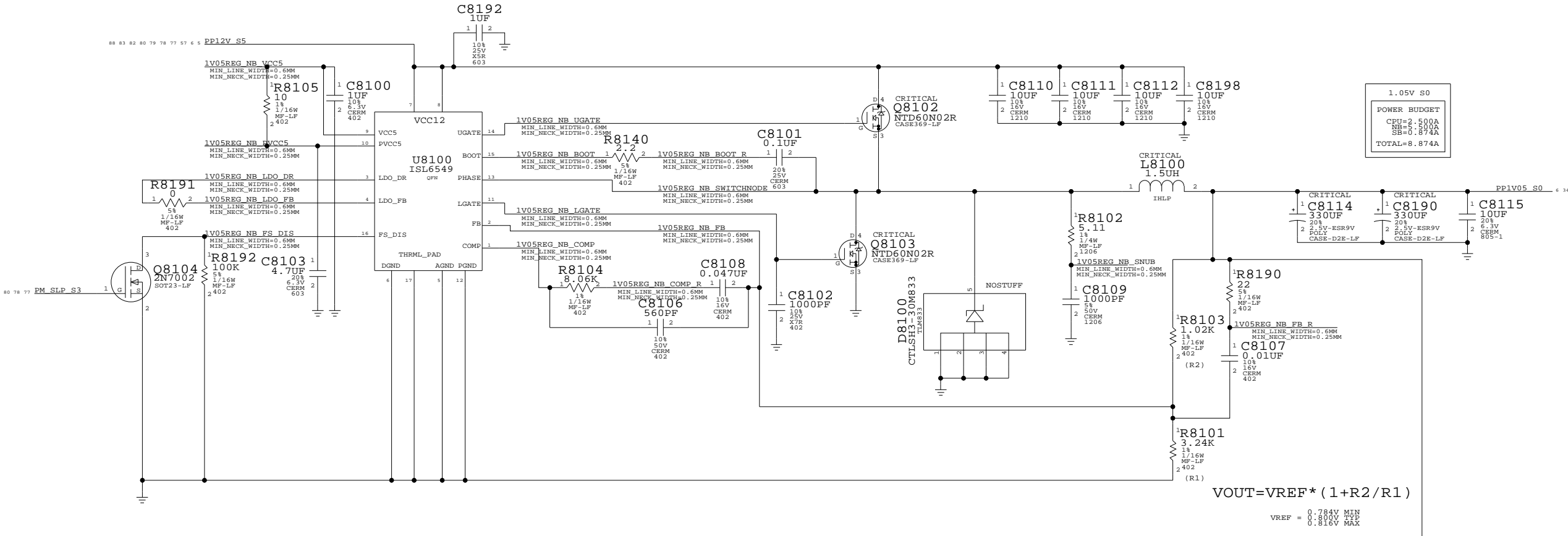
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1.05V S0 REGULATOR



1.05V VREG		
SYNC_MASTER=M38-RT		SYNC_DATE=05/18/2005
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE		SHT	81 OF 111
NONE			

8 7 6 5 4 3 2 1

5V S5 AND 5V AUDIO S5 REGULATOR

SYNC_MASTER= (MASTER) SYNC_DATE= (MASTER)

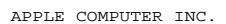
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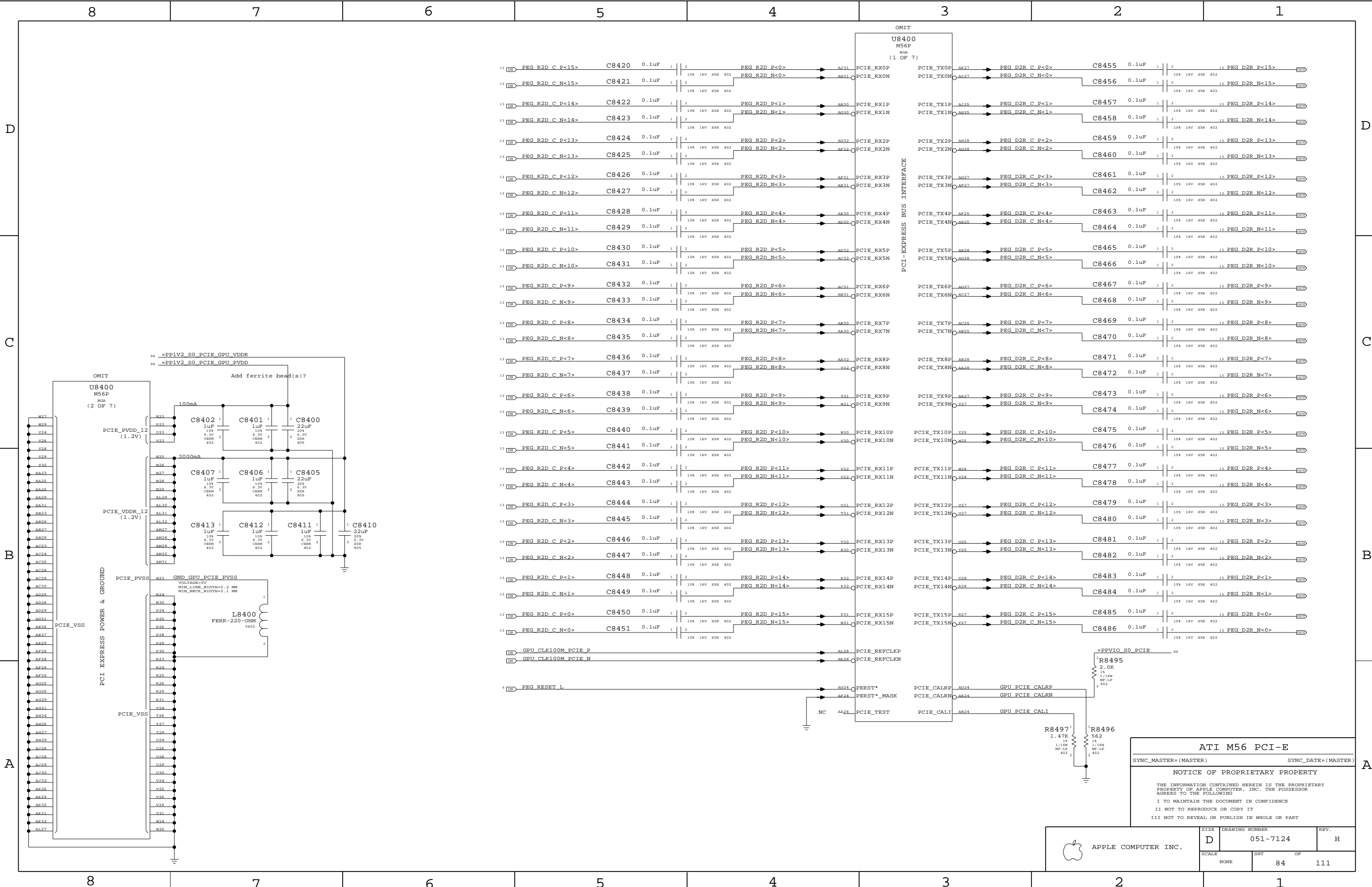
SIZE

DRAWING NUMBER

SCALE	

SHT C

111



ATI M56 PCI-E

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

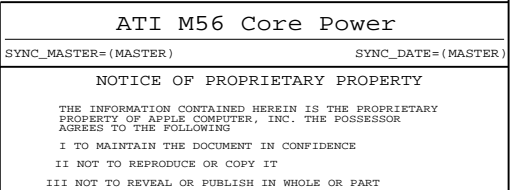
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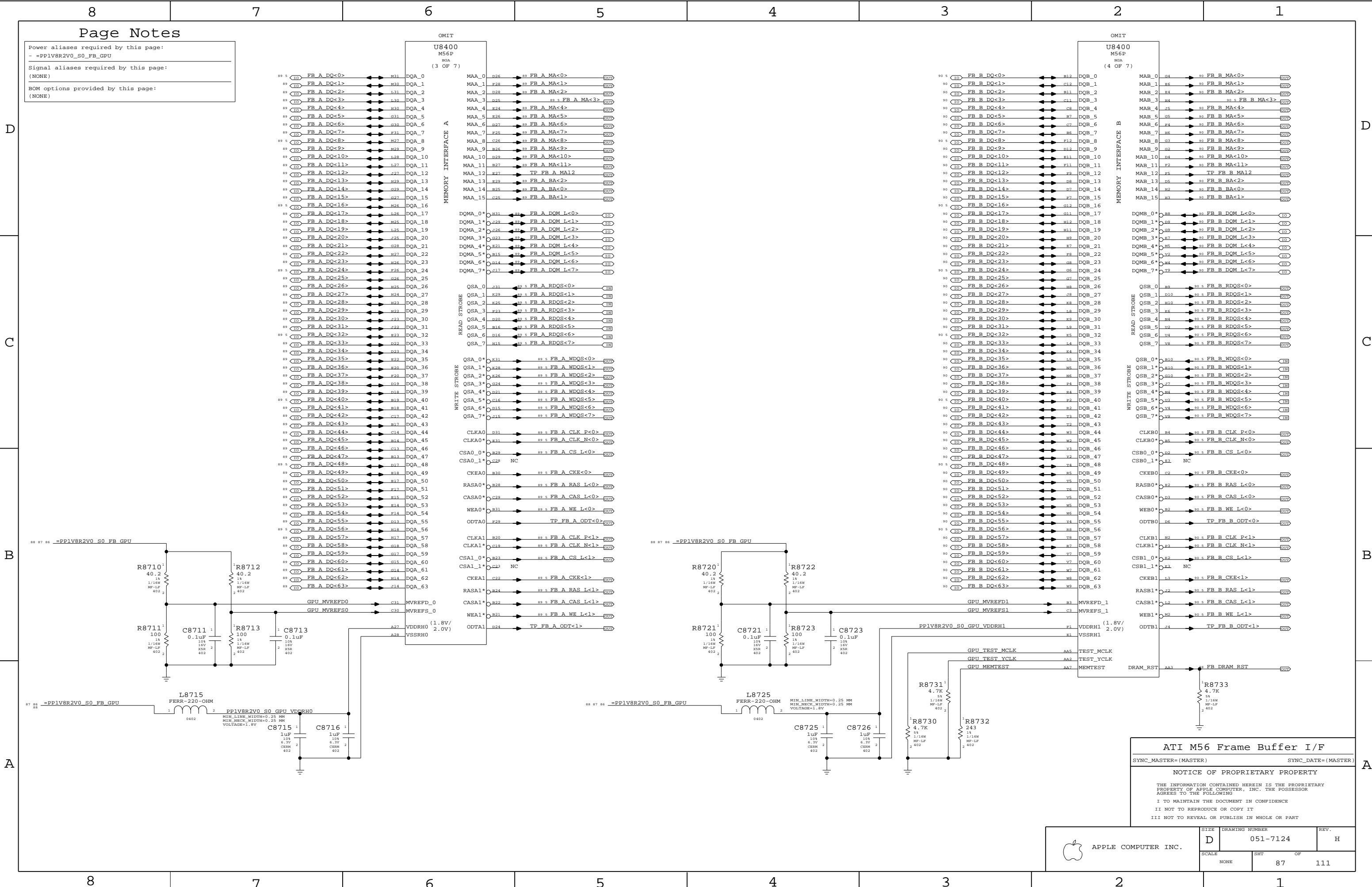
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE	SHT		OF
	NONE		111





Page Notes

Power aliases required by this page:
- =PP1V8R2V0_S0_FB_GPU

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

ATI M56 Frame Buffer I/F

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

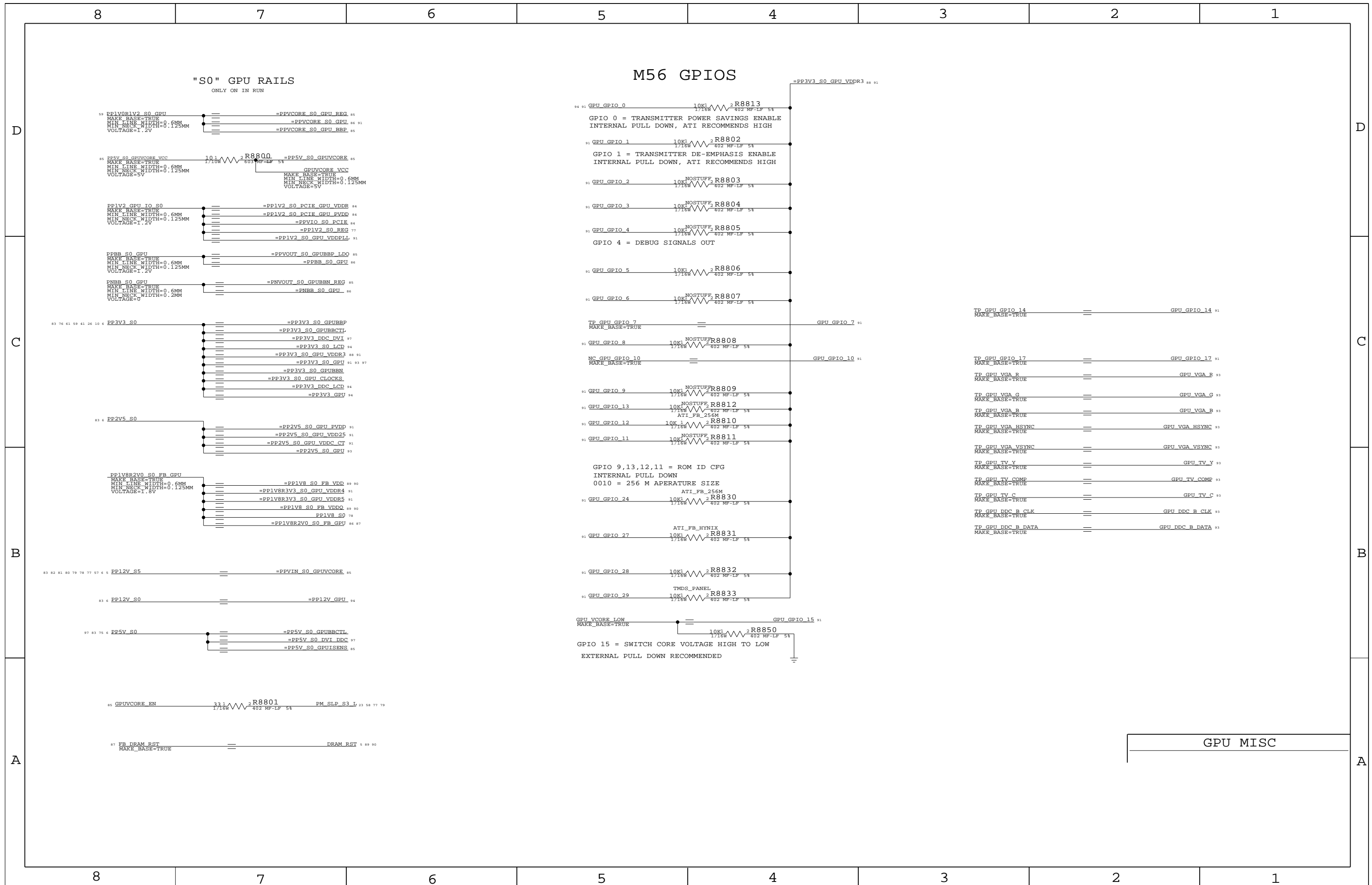
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	H
SCALE		SHT	OF
NONE		87	111



Power aliases required by this page:

- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQSignal aliases required by this page:
(NONE)BOM options provided by this page:
(NONE)

GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.

SIZE

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DRAWING NUMBER

051-7124

REV.

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SCALE

NONE

SHT

89

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111

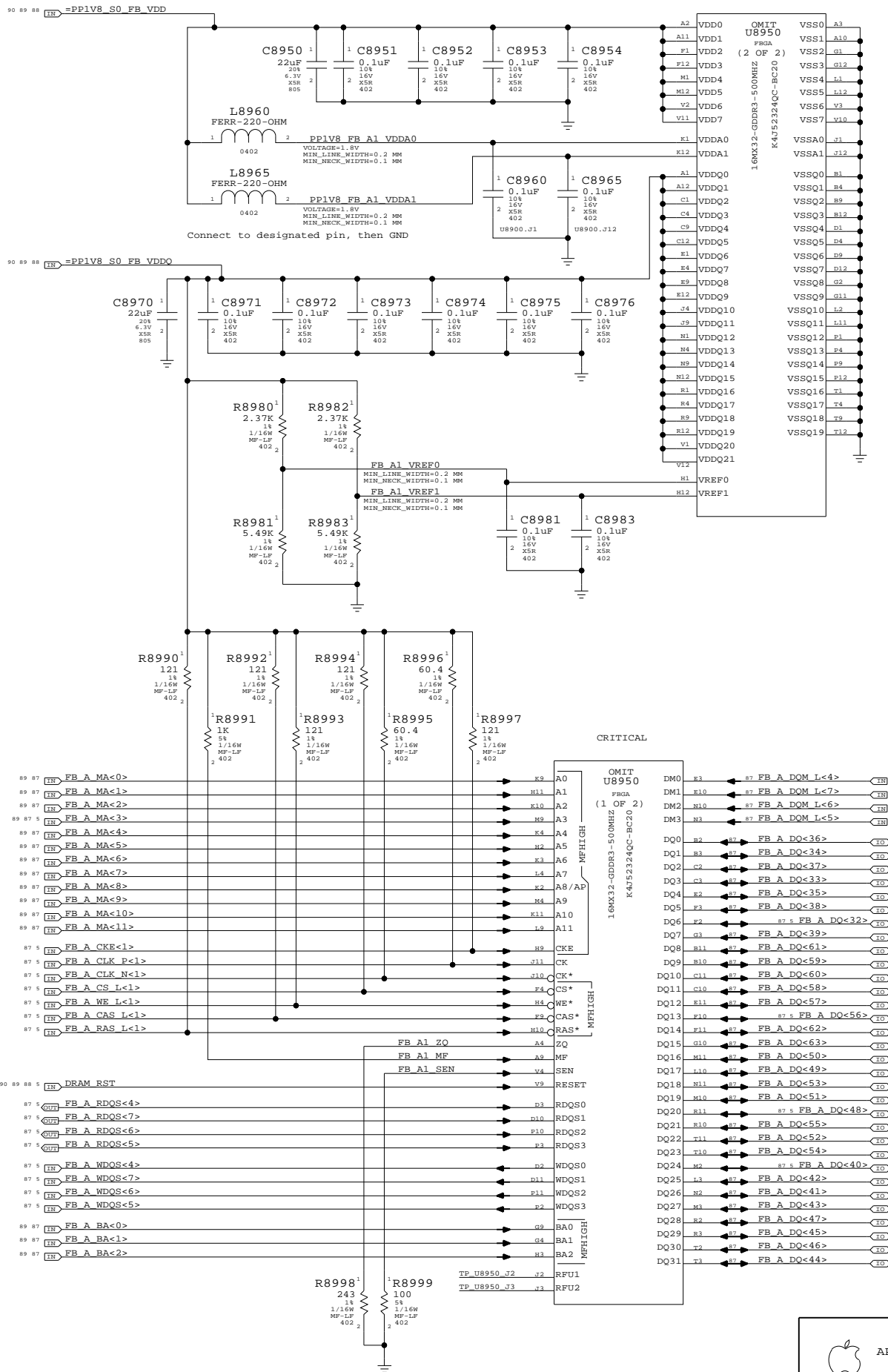
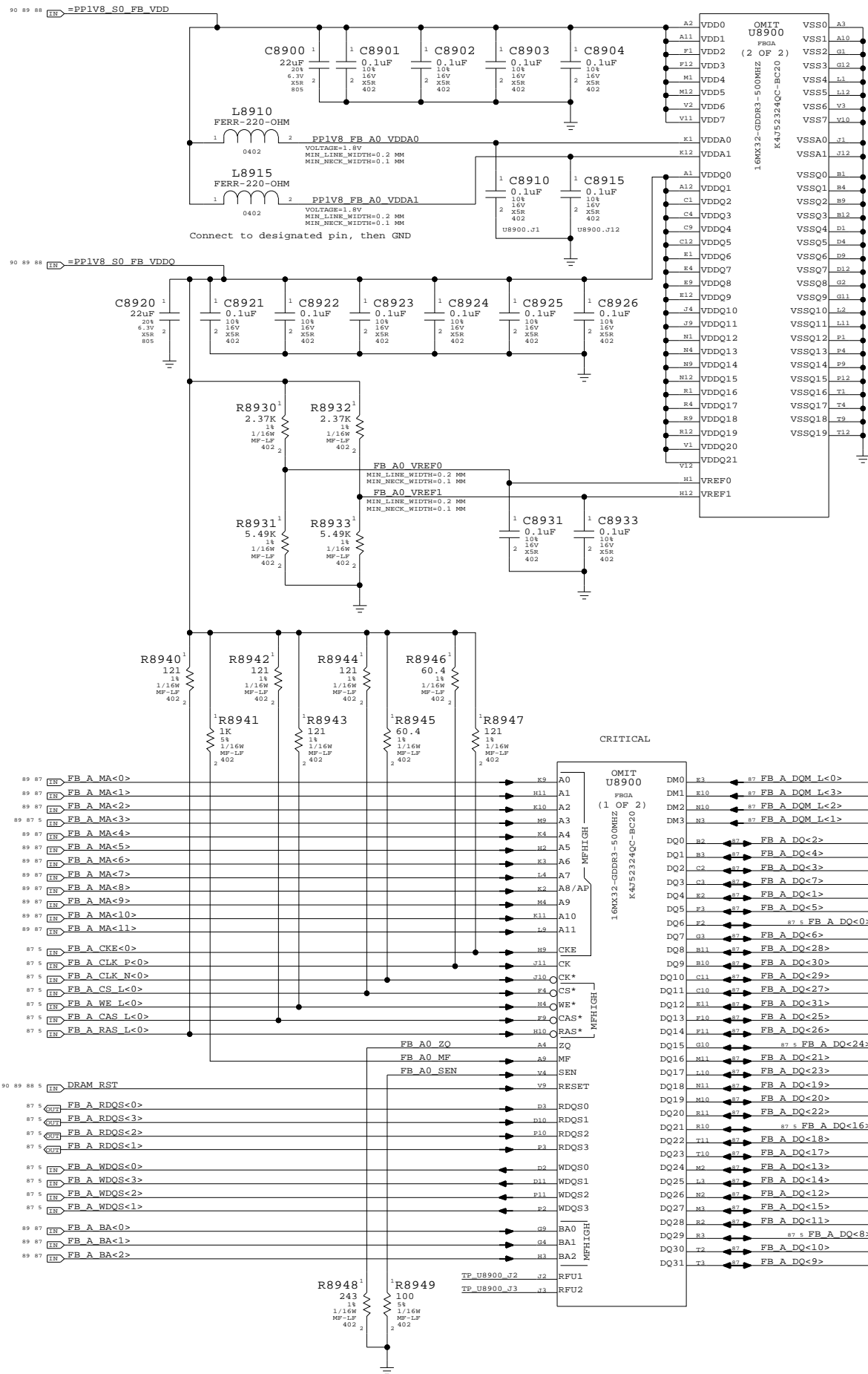


Diagram of the GDDR3 Frame Buffer A circuit, showing the connection of the FB A0 VREF0 and FB A0 VREF1 pins to the FB A0 VREF0 and FB A0 VREF1 pins. The circuit includes various components such as resistors (R8930, R8932, R8931, R8933, R8940, R8942, R8944, R8946, R8941, R8943, R8945, R8947), capacitors (C8900, C8901, C8902, C8903, C8904, C8920, C8921, C8922, C8923, C8924, C8925, C8926, C8931, C8933), and inductors (L8910, L8915). The diagram also shows the connection of the FB A0 VREF0 and FB A0 VREF1 pins to the FB A0 VREF0 and FB A0 VREF1 pins. The circuit is labeled with various components and their values, and the connections are shown with lines and arrows.

Power aliases required by this page:
 - =PP1V8_S0_FB_VDD
 - =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

GDWR3 Frame Buffer B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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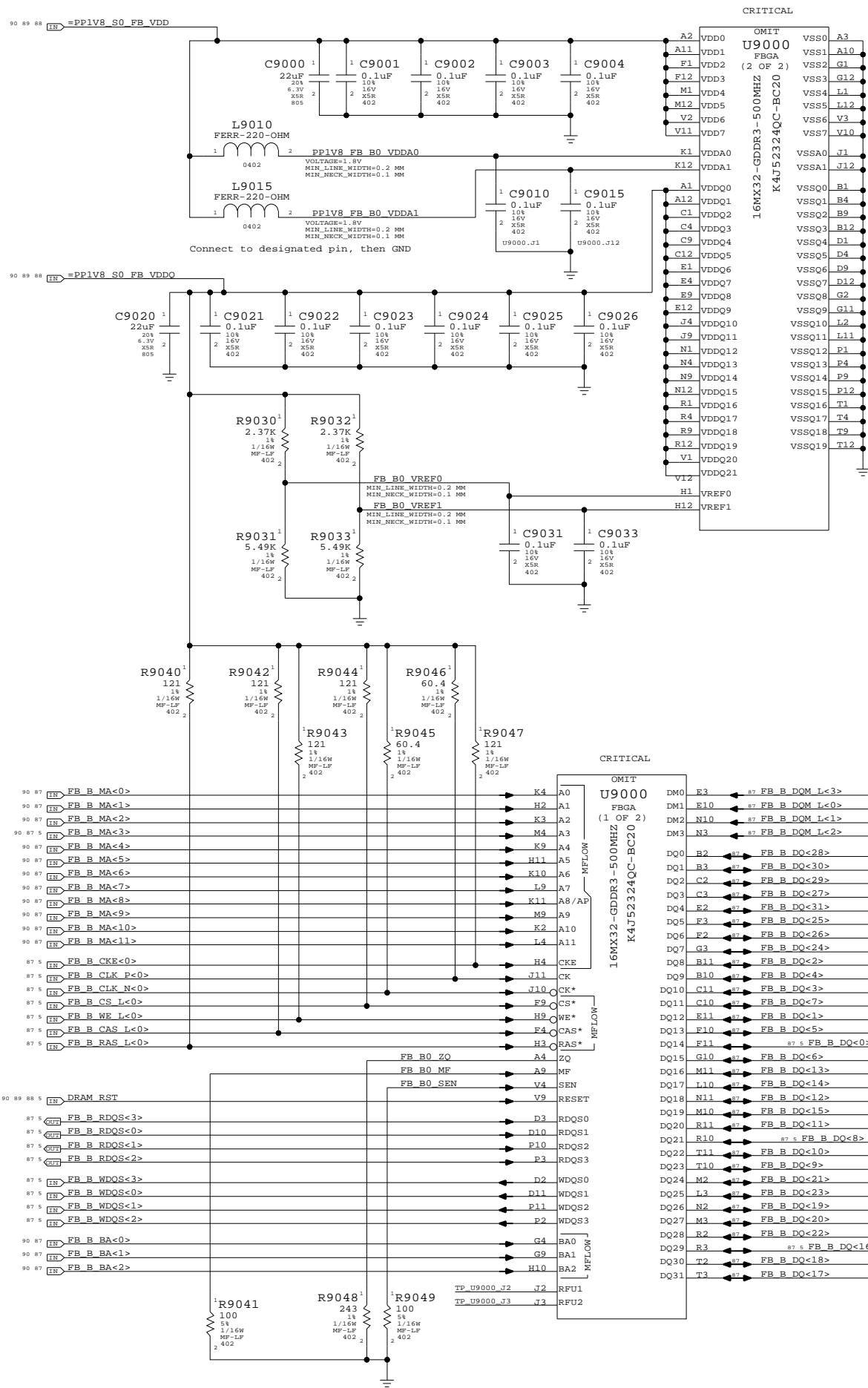
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	H
SCALE	SHT	OF
NONE	90	111



Page Notes

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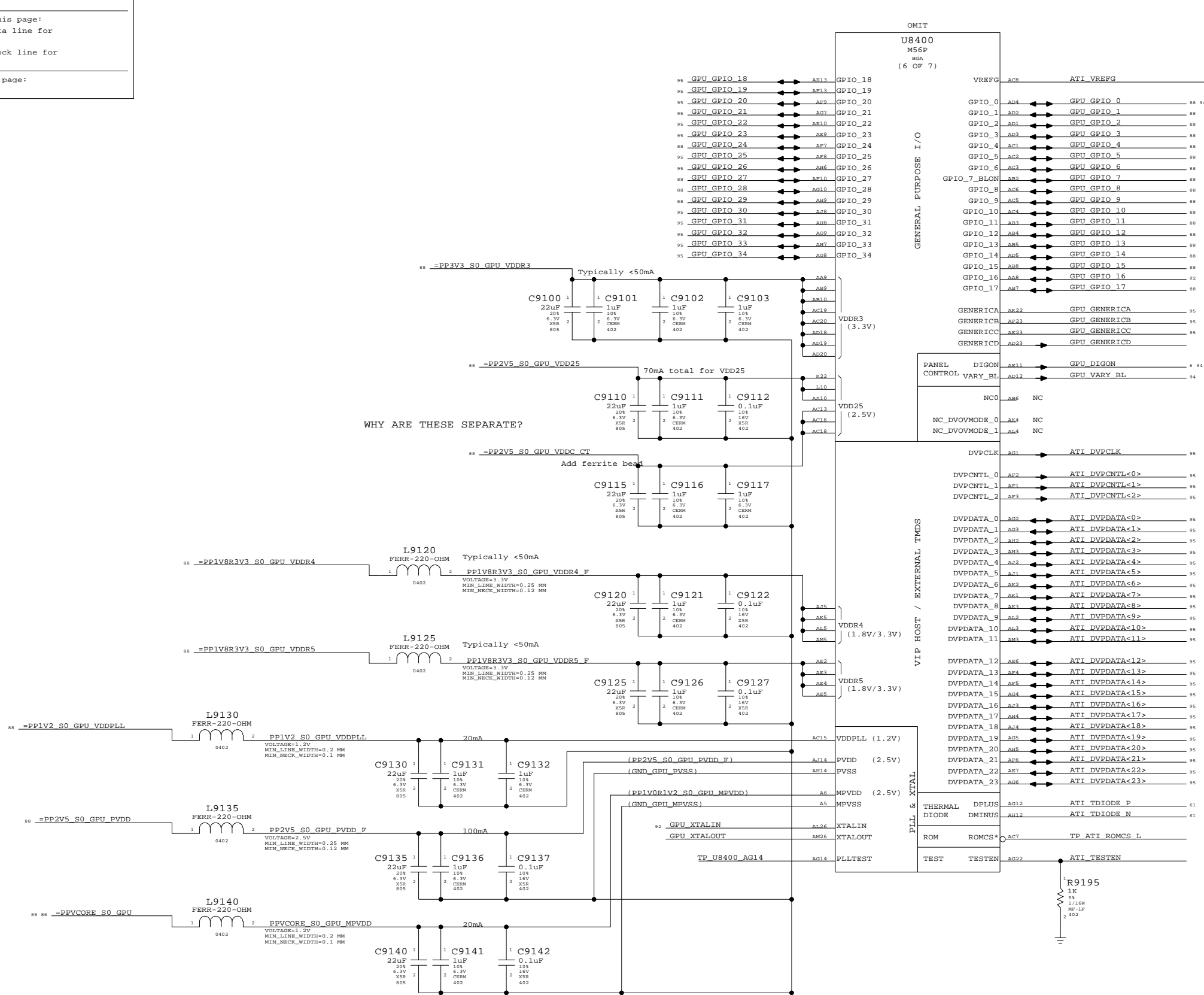
- =PP3V3_GPU_GPIOS
- =PP2V5_PVDD
- =PP1V8_GPU_LVDS_PLL

Signal aliases required by this page:

- =I2C_GPU_TMDS_SDA - I2C data line for external TMDS transmitters
- =I2C_GPU_TMDS_SCL - I2C clock line for external TMDS transmitters

BOM options provided by this page:

(NONE)



Page Notes

Power aliases required by this page:

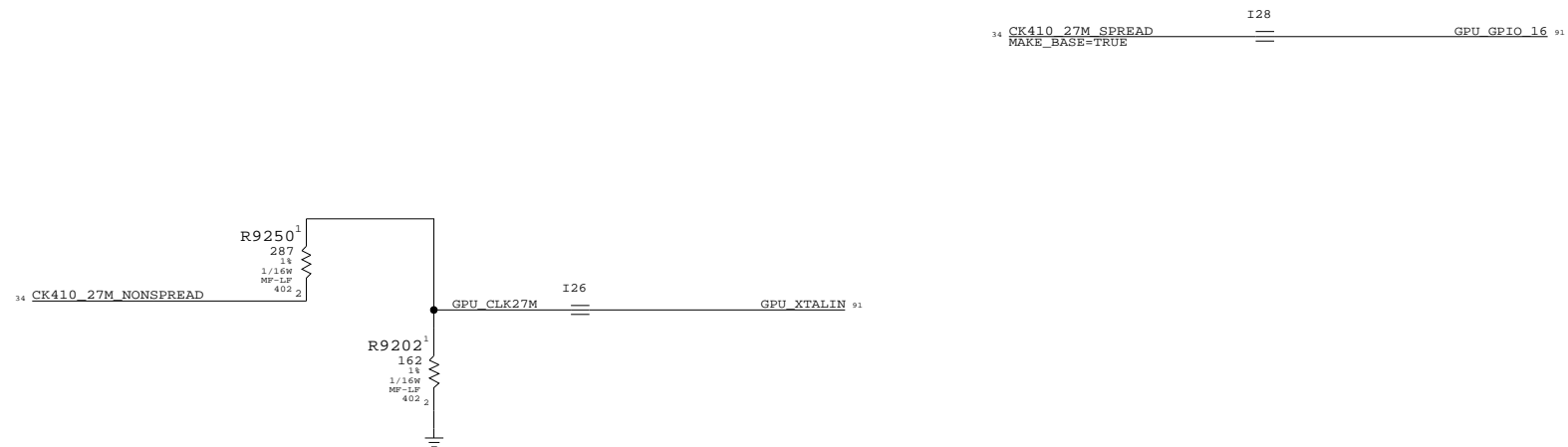
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- =PP3V3_GPU_CLOCKS          - =PP3V3_GPU_PWRSEQ
- =PPVIN_GPU_LVDDR_LDO        - =PP2V5_GPU_PWRSEQ
- =PP2V5_GPU_LVDDR_LDO        - =PP1V8_GPU_PWRSEQ
                                - =PP1V5_GPU_PWRSEQ
```

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

```
- GPU_SS                                - GPU_LVDDR_2V8
```



GPU CLOCKS

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SYNC_MASTER=BOZEMAN
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SYNC_DATE=05/21/2005	7
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APPLE COMPUTER INC.

SIZE
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SIZE	DRAWING NUMBER
D	051-7124

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SCALE	
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OF

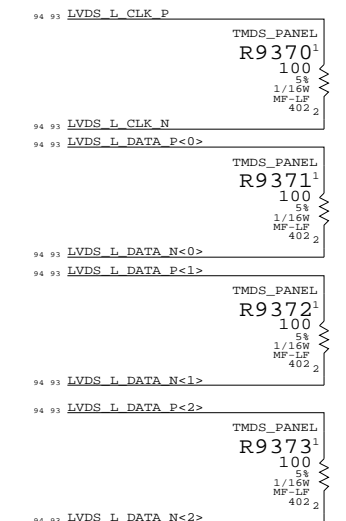
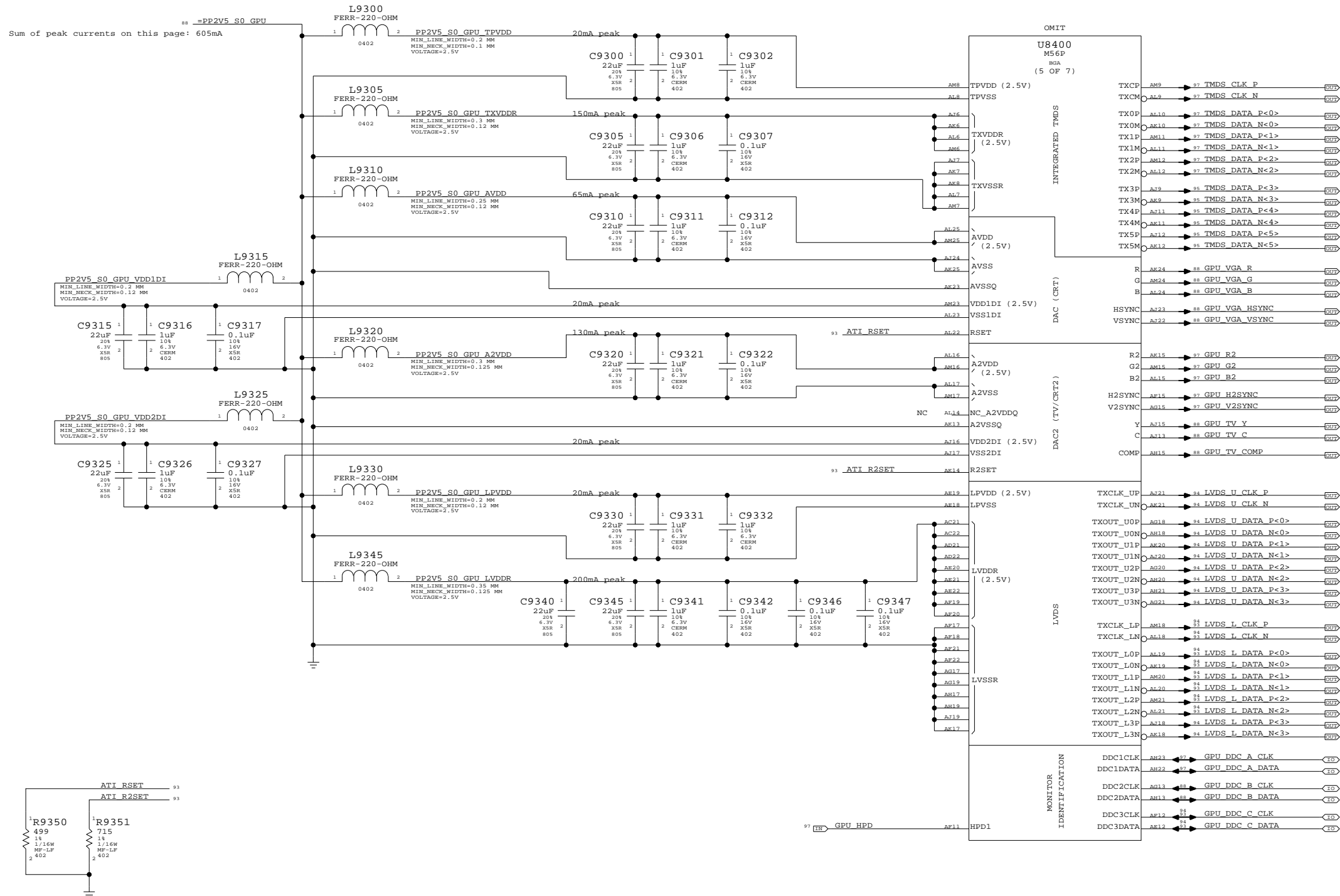
111

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Power aliases required by this page:
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- =Pp1V8R2V5_S0_GPU_LVDDR
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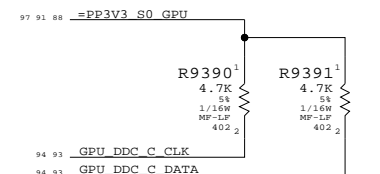
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Signal aliases required by this page:
(NONE)
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BOM options provided by this page:
(NONE)
```

TERMINATION FOR TMDS USAGE OF LVDS PINS
PLACE CLOSE TO GPU (U8400)



Composite/S-Video	VGA	Component
Y	G	Y
C	R	Pr
Comp	B	Pb



SYNC_MASTER= (MASTER)	SYNC_DATE= (MASTER)
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APPLE COMPUTER INC.

SIZE D	DRAWING NUMBER 051-7124	REV. H
SCALE NONE	SHT 93	OF 111

8 7 6 5 4 3 2 1

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8 7 6 5 4 3 2 1

LCD (LVDS) INTERFACE

100K pull-ups are for no-panel case (development)
Panel has 2K pull-ups

CRITICAL

J9402 53307-3072 F-ST-SM

SDF9400 STDOFF-3MMOD4.6MMH-1.35-TH

SDF9401 STDOFF-3MMOD4.6MMH-1.35-TH

Internal Display Conns

SYNC_MASTER=BOZEMAN SYNC_DATE=04/27/2005

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APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-7124 REV. H

SCALE NONE SHT 94 OF 111

SYNC_MASTER=BOZEMAN	SYNC_DATE=04/27/2005
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APPLE COMPUTER INC.

D	051-7124	H
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NONE	94	111
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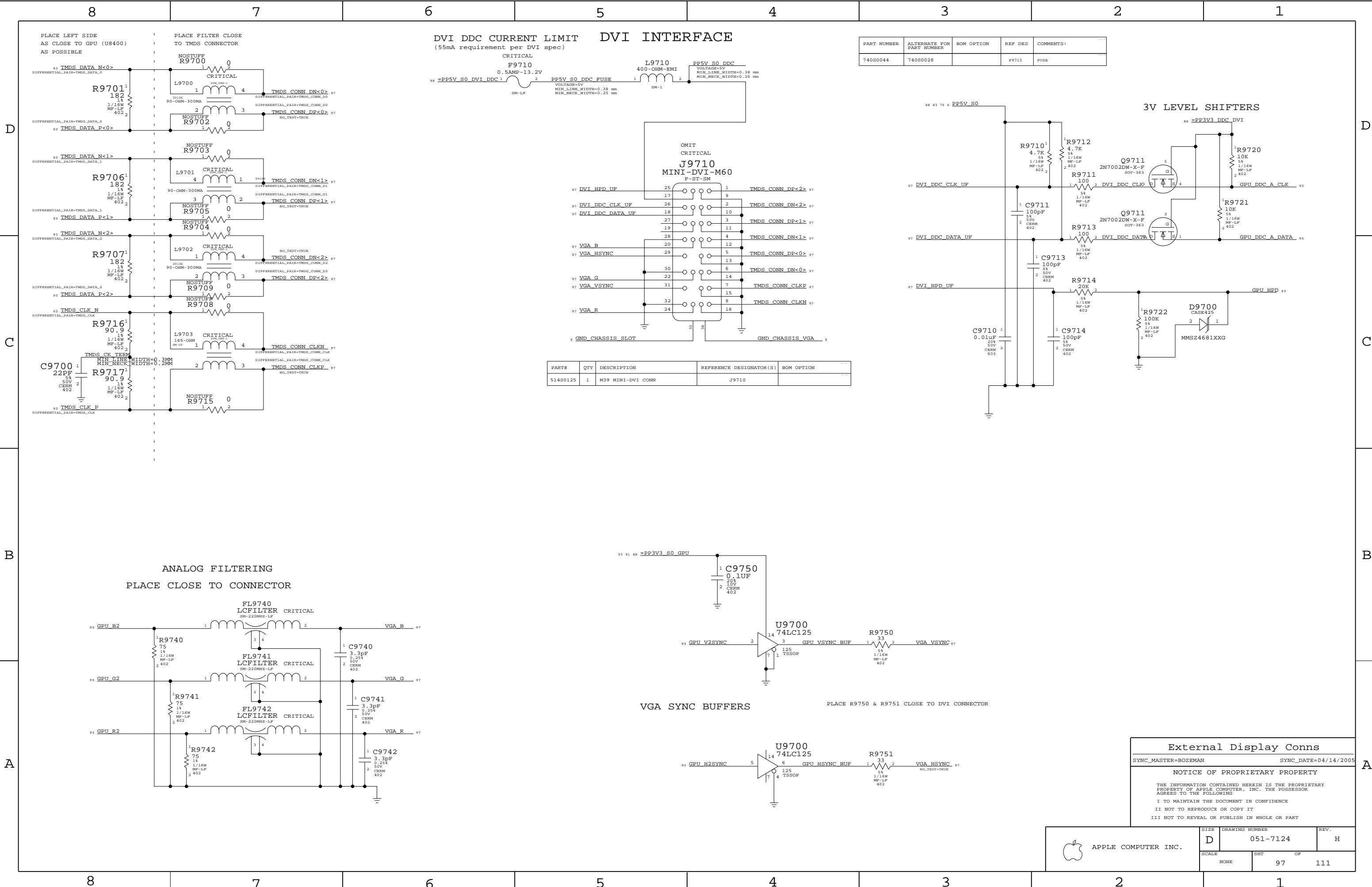
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	LPC_AD<2>	LPC_AD<2> - @m60_1ib.M60	21D4 58C7 60C1 67C6	LPC_AD<3>	LPC_AD<3> - @m60_1ib.M60	21D4 58C7 60C1 67C6	MEM_A_DQ<29>	MEM_A_DQ<29> - @m60_1ib.M60	1507 28C3	MEM_B_DQ<44>	MEM_B_DQ<44> - @m60_1ib.M60	5A7 15B4 29B3	NC_SMC_P27	NC_SMC_P27 - @m60_1ib.M60	58D7 59D6
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C4116 CAP_402 m60[41B5] C4117 CAP_402 m60[41B2] C4118 CAP_402 m60[41B2] C4126 CAP_402 m60[41A8] C4127 CAP_402 m60[41A8] C4128 CAP_402 m60[41A8] C4129 CAP_402 m60[41A8] C4130 CAP_402 m60[41A7] C4131 CAP_402 m60[41A7] C4132 CAP_402 m60[41A7] C4133 CAP_402 m60[41A6] C4134 CAP_402 m60[41A6] C4135 CAP_402 m60[41A5] C4136 CAP_402 m60[41A5] C4137 CAP_402 m60[41A5] C4138 CAP_402 m60[41A4] C4139 CAP_402 m60[41A4] C4140 CAP_402 m60[41B3] C4150 CAP_402 m60[41D5] C4200 CAP_1210 m60[42D8] C4201 CAP_402 m60[42D7] C4202 CAP_1210 m60[42D7] C4203 CAP_1206-1 m60[42D6] C4204 CAP_402 m60[42D6] C4205 CAP_1210 m60[42C5] C4206 CAP_402 m60[42C5] C4209 CAP_603 m60[42B7] C4210 CAP_402 m60[42B6] C4300 CAP_402 m60[43D7] C4301 CAP_402 m60[43D6] C4304 CAP_402 m60[43C6] C4305 CAP_402 m60[43B6] C4401 CAP_402 m60[44D1] C4402 CAP_402 m60[44C1] C4410 CAP_402 m60[44D6] C4412 CAP_402 m60[44D1] C4500 CAP_402 m60[45D4] C4501 CAP_402 m60[45D3] C4502 CAP_402 m60[45D3] C4503 CAP_805-1 m60[45C6] C4504 CAP_402 m60[45C4] C4505 CAP_402 m60[45C5] C4506 CAP_402 m60[45C5] C4507 CAP_402 m60[45C5] C4508 CAP_402 m60[45D5] C4509 CAP_402 m60[45D5] C4510 CAP_402 m60[45D5] C4515 CAP_805-1 m60[45D6] C4520 CAP_402 m60[45D5] C4521 CAP_402 m60[45D4] C4522 CAP_402 m60[45D3] C4523 CAP_402 m60[45D3] C4609 CAP_603-1 m60[46D5] C4610 CAP_402 m60[46D4] C4611 CAP_402 m60[46D4] C4612 CAP_402 m60[46C4] C4613 CAP_402 m60[46C4] C4615 CAP_603-1 m60[46C2] C4616 CAP_402 m60[46B2] C4620 CAP_402 m60[46B4] C4621 CAP_402 m60[46B4] C4622 CAP_402 m60[46A4] C4623 CAP_402 m60[46A4] C4625 CAP_603-1 m60[46A2] C4626 CAP_402 m60[46A2] C4650 CAP_402 m60[46C7] C4654 CAP_402 m60[46B8] C4660 CAP_402 m60[46C7] C4664 CAP_805-1 m60[46B7] C4700 CAP_805-1 m60[47C8] C4710 CAP_P_B2 m60[47D6] C4712 CAP_402 m60[47D5] C4713 CAP_402 m60[47D5] C4720 CAP_P_B2 m60[47C5] C4722 CAP_402 m60[47C5] C4723 CAP_402 m60[47C5] C4730 CAP_P_B2 m60[47B6] C4732 CAP_402 m60[47A5] C4733 CAP_402 m60[47A5] C4742 CAP_402 m60[47D2] C4743 CAP_402 m60[47D1] C4750 CAP_402 m60[47B6] C4751 CAP_402 m60[47C6] C4752 CAP_402 m60[47D6] C4796 CAP_402 m60[47C8] C4797 CAP_805-2 m60[47D3] C4798 CAP_402 m60[47A2] C4799 CAP_805-2 m60[47A2] C5300 CAP_402 m60[53B7] C5301 CAP_402 m60[53B7] C5304 CAP_402 m60[53D5] C5305 CAP_402 m60[53D5] C5306 CAP_402 m60[53D4] C5307 CAP_402 m60[53C4] C5308 CAP_402 m60[53C5] C5309 CAP_402 m60[53C4] C5310 CAP_402 m60[53C4] C5311 CAP_805-1 m60[53C3] C5312 CAP_805-1 m60[53D4] C5313 CAP_402 m60[53C5] C5314 CAP_805-1 m60[53C4] C5700 CAP_402 m60[57D6] C5701 CAP_603 m60[57D4] C5702 CAP_402 m60[57C4] C5703 CAP_603 m60[57B6] C5706 CAP_402 m60[57C5] C5707 CAP_402 m60[57C2] C5708 CAP_402 m60[57C4] C5709 CAP_1206 m60[57C3] C5711 CAP_1210 m60[57D3] C5713 CAP_805-1 m60[57D2] C5714 CAP_805-1 m60[57D2] C5715 CAP_P_TH-KZJ-LF m60[57D2] C5720 CAP_805 m60[57D8] C5721 CAP_805-1 m60[57C8] C5722 CAP_805-1 m60[57C8] C5723 CAP_402 m60[57D7] C5724 CAP_402 m60[57C6] C5725 CAP_805-1 m60[57C8] C5726 CAP_805-1 m60[57C7] C5753 CAP_1210 m60[57D3]	A	C5792 CAP_603 m60[57D5] C5797 CAP_1210 m60[57D2] C5798 CAP_1210 m60[57D2] C5799 CAP_1210 m60[57D3] C5800 CAP_402 m60[59B8] C5801 CAP_402 m60[59B8] C5802 CAP_805 m60[58D3] C5803 CAP_402 m60[58D2] C5804 CAP_402 m60[58D2] C5805 CAP_402 m60[58D2] C5806 CAP_402 m60[58D1] C5807 CAP_402 m60[58D2] C5820 CAP_402 m60[58C3] C5900 CAP_402 m60[59D8] C5901 CAP_402 m60[59D8] C5902 CAP_402 m60[59B7] C5903 CAP_402 m60[59A8] C5919 CAP_402 m60[59B4] C5940 CAP_402 m60[59A4] C5941 CAP_402 m60[59A3										

8			7			6			5			4			3			2			1		
D	C7516	CAP_402	m60[75B4]	C8310	CAP_603-1	m60[83D8]	C8631	CAP_402	m60[86C6]	C9122	CAP_402	m60[91B5]	D										
	C7517	CAP_P_SM-3	m60[75D2]	C8311	CAP_603-1	m60[83D7]	C8632	CAP_402	m60[86C5]	C9125	CAP_805	m60[91B5]											
	C7518	CAP_P_SM-3	m60[75D2]	C8315	CAP_603-1	m60[83B5]	C8633	CAP_402	m60[86C5]	C9126	CAP_402	m60[91B5]											
	C7521	CAP_402	m60[75A6]	C8316	CAP_603	m60[83A6]	C8634	CAP_402	m60[86C5]	C9127	CAP_402	m60[91B5]											
	C7526	CAP_603	m60[75D6]	C8317	CAP_603-1	m60[83B8]	C8650	CAP_805	m60[86B7]	C9130	CAP_805	m60[91A6]											
	C7527	CAP_603	m60[75C5]	C8318	CAP_603-1	m60[83B7]	C8651	CAP_805	m60[86B7]	C9131	CAP_402	m60[91A6]											
	C7528	CAP_402	m60[75B5]	C8319	CAP_603	m60[83A7]	C8652	CAP_805	m60[86B7]	C9132	CAP_402	m60[91A5]											
	C7529	CAP_402	m60[75B5]	C8320	CAP_402	m60[83A7]	C8653	CAP_805	m60[86B6]	C9135	CAP_805	m60[91A6]											
	C7530	CAP_402	m60[75D6]	C8321	CAP_603-1	m60[83C5]	C8655	CAP_402	m60[86B6]	C9136	CAP_402	m60[91A6]											
	C7531	CAP_402	m60[75B5]	C8325	CAP_603-1	m60[83D6]	C8656	CAP_402	m60[86B6]	C9137	CAP_402	m60[91A5]											
C	C7532	CAP_402	m60[75B6]	C8326	CAP_603-1	m60[83D5]	C8657	CAP_402	m60[86B6]	C9140	CAP_805	m60[91A6]	C										
	C7533	CAP_402	m60[75B6]	C8398	CAP_603	m60[83C2]	C8658	CAP_402	m60[86B5]	C9141	CAP_402	m60[91A6]											
	C7534	CAP_402	m60[75B5]	C8399	CAP_603	m60[83D2]	C8659	CAP_402	m60[86B5]	C9142	CAP_402	m60[91A5]											
	C7535	CAP_603	m60[75D5]	C8400	CAP_805	m60[84C7]	C8660	CAP_402	m60[86B5]	C9191	CAP_402	m60[91D2]											
	C7550	CAP_603	m60[75D1]	C8401	CAP_402	m60[84C7]	C8661	CAP_402	m60[86B6]	C9300	CAP_805	m60[93C6]											
	C7551	CAP_603	m60[75D1]	C8402	CAP_402	m60[84C7]	C8662	CAP_402	m60[86B6]	C9301	CAP_402	m60[93C6]											
	C7590	CAP_402	m60[75C3]	C8405	CAP_805	m60[84B7]	C8663	CAP_402	m60[86B6]	C9302	CAP_402	m60[93C5]											
	C7592	CAP_402	m60[75B3]	C8406	CAP_402	m60[84B7]	C8664	CAP_402	m60[86B5]	C9305	CAP_805	m60[93C6]											
	C7596	CAP_402	m60[75D6]	C8407	CAP_402	m60[84B7]	C8665	CAP_402	m60[86B5]	C9306	CAP_402	m60[93C6]											
	C7597	CAP_1210	m60[75D1]	C8410	CAP_805	m60[84B6]	C8666	CAP_402	m60[86B5]	C9307	CAP_402	m60[93C5]											
B	C7598	CAP_1210	m60[75D1]	C8411	CAP_402	m60[84B7]	C8667	CAP_402	m60[86B6]	C9310	CAP_805	m60[93C6]	B										
	C7599	CAP_402	m60[76D6]	C8412	CAP_402	m60[84B7]	C8668	CAP_402	m60[86B6]	C9311	CAP_402	m60[93C6]											
	C7612	CAP_402	m60[76B2]	C8413	CAP_402	m60[84B7]	C8669	CAP_402	m60[86B6]	C9312	CAP_402	m60[93C5]											
	C7633	CAP_402	m60[76C7]	C8420	CAP_402	m60[84D5]	C8670	CAP_402	m60[86B5]	C9315	CAP_805	m60[93B8]											
	C7710	CAP_402	m60[77C7]	C8421	CAP_402	m60[84D5]	C8671	CAP_402	m60[86B5]	C9316	CAP_402	m60[93B8]											
	C7711	CAP_402	m60[77B7]	C8422	CAP_402	m60[84D5]	C8672	CAP_402	m60[86B5]	C9317	CAP_402	m60[93B7]											
	C7712	CAP_402	m60[77C7]	C8423	CAP_402	m60[84D5]	C8673	CAP_402	m60[86B6]	C9320	CAP_805	m60[93B6]											
	C7750	CAP_402	m60[77B2]	C8424	CAP_402	m60[84D5]	C8674	CAP_402	m60[86B6]	C9321	CAP_402	m60[93B6]											
	C7751	CAP_805	m60[77C3]	C8425	CAP_402	m60[84D5]	C8675	CAP_402	m60[86B6]	C9322	CAP_402	m60[93B5]											
	C7752	CAP_805	m60[77C3]	C8426	CAP_402	m60[84D5]	C8676	CAP_402	m60[86B5]	C9325	CAP_805	m60[93B8]											
A	C7753	CAP_402	m60[77B5]	C8427	CAP_402	m60[84D5]	C8677	CAP_402	m60[86B5]	C9326	CAP_402	m60[93B8]	A										
	C7754	CAP_402	m60[77B4]	C8428	CAP_402	m60[84D5]	C8678	CAP_402	m60[86B5]	C9327	CAP_402	m60[93B7]											
	C7755	CAP_805	m60[77B2]	C8429	CAP_402	m60[84C5]	C8679	CAP_402	m60[86A6]	C9330	CAP_805	m60[93B6]											
	C7756	CAP_805	m60[77B2]	C8430	CAP_402	m60[84C5]	C8680	CAP_402	m60[86A6]	C9331	CAP_402	m60[93B6]											
	C7757	CAP_402	m60[77B5]	C8431	CAP_402	m60[84C5]	C8681	CAP_402	m60[86A6]	C9332	CAP_402	m60[93B5]											
	C7797	CAP_402	m60[77C3]	C8432	CAP_402	m60[84C5]	C8682	CAP_402	m60[86A5]	C9340	CAP_805	m60[93A6]											
	C7798	CAP_603	m60[77B5]	C8433	CAP_402	m60[84C5]	C8683	CAP_402	m60[86A5]	C9341	CAP_402	m60[93A6]											
	C7799	CAP_402	m60[77A2]	C8434	CAP_402	m60[84C5]	C8690	CAP_805	m60[86D5]	C9342	CAP_402	m60[93A5]											
	C7800	CAP_1210	m60[78C3]	C8435	CAP_402	m60[84C5]	C8691	CAP_402	m60[86D5]	C9345	CAP_805	m60[93A6]											
	C7801	CAP_P_TH-MCZ	m60[78C4]	C8436	CAP_402	m60[84C5]	C8692	CAP_402	m60[86D5]	C9346	CAP_402	m60[93A5]											
A	C7802	CAP_603	m60[78C6]	C8437	CAP_402	m60[84C5]	C8711	CAP_402	m60[87B7]	C9347	CAP_402	m60[93A5]	A										
	C7803	CAP_603	m60[78B7]	C8438	CAP_402	m60[84C5]	C8713	CAP_402	m60[87B7]	C9400	CAP_603-1	m60[94C7]											
	C7804	CAP_402	m60[78C7]	C8439	CAP_402	m60[84C5]	C8715	CAP_402	m60[87A7]	C9401	CAP_402	m60[94C6]											
	C7805	CAP_603	m60[78C5]	C8440	CAP_402	m60[84B5]	C8716	CAP_402	m60[87A6]	C9410	CAP_402	m60[94C6]											
	C7806	CAP_805-1	m60[78B3]	C8441	CAP_402	m60[84B5]	C8721	CAP_402	m60[87B4]	C9420	CAP_1210	m60[94C5]											
	C7807	CAP_P_TH	m60[78B3]	C8442	CAP_402	m60[84B5]	C8723	CAP_402	m60[87B4]	C9470	CAP_402	m60[94B2]											
	C7809	CAP_402	m60[78B3]	C8443	CAP_402	m60[84B5]	C8725	CAP_402	m60[87A4]	C9700	CAP_402	m60[97C8]											
	C7810	CAP_402	m60[78B5]	C8444	CAP_402	m60[84B5]	C8726	CAP_402	m60[87A3]	C9710	CAP_603	m60[97C3]											
	C7811	CAP_402	m60[78B5]	C8445	CAP_402	m60[84B5]	C8900	CAP_805	m60[89D7]	C9711	CAP_402	m60[97D3]											
	C7813	CAP_1206	m60[78B4]	C8446	CAP_402	m60[84B5]	C8901	CAP_402	m60[89D7]	C9713	CAP_402	m60[97C2]											
A	C7814	CAP_402	m60[78B5]	C8447	CAP_402	m60[84B5]	C8902	CAP_402	m60[89D7]	C9714	CAP_402	m60[97C2]	A										
	C7897	CAP_805-1	m60[78B2]	C8448	CAP_402	m60[84B5]	C8903	CAP_402	m60[89D7]	C9740	CAP_402	m60[97A7]											
	C7899	CAP_1210	m60[78C3]	C8449	CAP_402	m60[84B5]	C8904	CAP_402	m60[89D6]	C9741	CAP_402	m60[97A6]											
	C7900	CAP_402	m60[79D6]	C8450	CAP_402	m60[84B5]	C8910	CAP_402	m60[89D7]	C9742	CAP_402	m60[97A6]											
	C7901	CAP_603	m60[79D5]	C8451	CAP_402	m60[84B5]	C8915	CAP_402	m60[89D6]	C9750	CAP_402	m60[97B4]											
	C7902	CAP_402	m60[79C5]	C8455	CAP_402	m60[84D2]	C8920	CAP_805	m60[89C8]	D2500	DIODE_SCHOT_SOT23	m60[25C8]											
	C7903	CAP_603	m60[79C7]	C8456	CAP_402	m60[84D2]	C8921	CAP_402	m60[89C8]	D2501	DIODE_SCHOT_SOT23	m60[25D8]											
	C7906	CAP_402	m60[79C5]	C8457	CAP_402	m60[84D2]	C8922	CAP_402	m60[89C7]	D2600	DIODE_SCHOT_SOT23	m60[26D8]											
	C7907	CAP_402	m60[79C3]	C8458	CAP_402	m60[84D2]	C8923	CAP_402	m60[89C7]	D2601	DIODE_SCHOT_SOT23	m60[26C8]											
	C7908	CAP_402	m60[79C5]	C8459	CAP_402	m60[84D2]	C8924	CAP_402	m60[89C7]	D4600	DIODE_SMC	m60[46D5]											
A	C7909	CAP_1206	m60[79C3]	C8460	CAP_402	m60[84D2]	C8925	CAP_402	m60[89C7]	D4690	ZENER_SOT23	m60[46A6]	A										
	C7910	CAP_P_TH-MCZ	m60[79D4]	C8461	CAP_402	m60[84D2]	C8926	CAP_402	m60[89C6]	D4700	DIODE_SCHOT_3P_A_SC-	m60[47C5]											
	C7911	CAP_1210	m60[79D3]	C8462	CAP_402	m60[84D2]	C8931	CAP_402	m60[89C7]	75													
	C7912	CAP_P_CASE-D2E-LF	m60[79C2]	C8463	CAP_402	m60[84D2]	C8933	CAP_402	m60[89C6]	D4701	DIODE_SCHOT_3P_A_SC-	m60[47B5]											
	C7913	CAP_1206	m60[79C2]	C8464	CAP_402	m60[84C2]	C8930	CAP_805	m60[89D4]	75													
	C7980	CAP_402	m60[79A3]	C8465	CAP_402	m60[84C2]	C8951	CAP_402	m60[89D4]	D4702	DIODE_SCHOT_3P_A_SC-	m60[47A5]											
	C7992	CAP_603	m60[79D6]	C8466	CAP_402	m60[84C2]	C8952	CAP_402	m60[89D4]	75													
	C7997	CAP_1210	m60[79D3]	C8467	CAP_402	m60[84C2]	C8953	CAP_402	m60[89D3]	D5700	DIODE_SCHOT_5P_TLM83	m60[57C4]											
	C7998	CAP_P_CASE-D2E-LF	m60[79C2]	C8468	CAP_402	m60[84C2]	C8954	CAP_402	m60[89D3]	3													
	C8000	CAP_1210	m60[80D3]	C8469	CAP_402	m60[84C2]	C8960	CAP_402	m60[89D3]	D6500	DIODE_SOT23	m60[65C4]											
A	C8001	CAP_P_CASE-D2E-LF	m60[80C2]	C8470	CAP_402	m60[84C2]	C8965	CAP_402	m60[89D3]	D6501	DIODE_SOT23	m60[65B4]	A										
	C8002	CAP_603	m60[80D6]	C8471	CAP_402	m60[84C2]	C8970	CAP_805	m60[89C5]	D6502	DIODE_SCHOT_SMB	m60[65C4]											
	C8003	CAP_402	m60[80C3]	C8472	CAP_402	m60[84C2]	C8971	CAP_402	m60[89C4]	D6503	DIODE_SCHOT_SMB	m60[65B4]											
	C8004	CAP_1206	m60[80C3]	C8473	CAP_402	m60[84C2]	C8972	CAP_402	m60[89C4]	D6600	DIODE_SCHOT23	m60[66C4]											
	C8005	CAP_402	m60[80C4]	C8474	CAP_402	m60[84C2]	C8973	CAP_402	m60[89C4]	D6601	DIODE_SCHOT_SMB	m60[66C3]											
	C8006	CAP_402	m60[80D6]	C8475	CAP_402	m60[84B2]	C8974	CAP_402	m60[89C4]	D7500	DIODE_SCHOT_SMB	m60[75C3]											
	C8009	CAP_603	m60[80C6]	C8476	CAP_402	m60[84B2]	C8975	CAP_402	m60[89C3]	D7501	DIODE_SCHOT_SMB	m60[75B2]											
	C8010	CAP_603	m60[80C4]	C8477	CAP_402	m60[84B2]	C8976	CAP_402	m60[89C3]	D7599	DIODE_SOT23	m60[76D6]											
	C8011	CAP_402	m60[80C5]	C8478	CAP_402	m60[84B2]	C8981	CAP_402	m60[89C3]	D7800	DIODE_SCHOT_5P_TLM83	m60[78B4]											
	C8012	CAP_402	m60[80C5]	C8479	CAP_402	m60[84B2]	C8983	CAP_402	m60[89C3]	3													
A	C8015	CAP_1210	m60[80D3]	C8480	CAP_402	m60[84B2]	C9000	CAP_805	m60[90D7]	D7900	DIODE_SCHOT_5P_TLM83	m60[79C4]	A										
	C8016	CAP_805-1	m60[80C2]	C8481	CAP_402	m60[84B2]	C9001	CAP_402	m60[90D7]	3													
	C8090	CAP_1210	m60[80D3]	C8482	CAP_402	m60[84B2]	C9002	CAP_402	m60[90D7]	D8000	DIODE_SCHOT_5P_TLM83	m60[80C4]											
	C8091	CAP_1210	m60																				

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D	GV3801	HOLE_VIA	m60[38A8]					L7324	IND_0603-LF	m60[73B5]		PP627	PROBEPOINT_SM	m60[5C8]		PP8908	PROBEPOINT_SM	m60[5B5]													
	GV3802	HOLE_VIA	m60[38A7]					L7325	IND_0603-LF	m60[73A5]		PP628	PROBEPOINT_SM	m60[5C8]		PP8909	PROBEPOINT_SM	m60[5B5]													
	GV3803	HOLE_VIA	m60[38A8]					L7326	IND_0603-LF	m60[73B5]		PP629	PROBEPOINT_SM	m60[5C8]		PP8910	PROBEPOINT_SM	m60[5B5]													
	GV3804	HOLE_VIA	m60[38A7]					L7327	IND_0603-LF	m60[73A5]		PP630	PROBEPOINT_SM	m60[5C8]		PP8911	PROBEPOINT_SM	m60[5B5]													
	GV3805	HOLE_VIA	m60[38A8]					L7381	IND_0603-LF	m60[73A5]		PP631	PROBEPOINT_SM	m60[5D6]		PP8912	PROBEPOINT_SM	m60[5B5]													
	GV3806	HOLE_VIA	m60[38A7]					L7500	IND_SM	m60[75D1]		PP632	PROBEPOINT_SM	m60[5D6]		PP8913	PROBEPOINT_SM	m60[5B5]													
	GV3807	HOLE_VIA	m60[38A8]					L7501	IND_SM	m60[75B2]		PP633	PROBEPOINT_SM	m60[5D6]		PP8914	PROBEPOINT_SM	m60[5A5]													
	GV3808	HOLE_VIA	m60[38A7]					L7502	IND_TH-VERT-LF	m60[76D8]		PP634	PROBEPOINT_SM	m60[5D6]		PP8915	PROBEPOINT_SM	m60[5A5]													
	J3	CON_2RTSM_125_SM-2MT	m60[61B6]					L7750	IND_SM-LF	m60[77B2]		PP635	PROBEPOINT_SM	m60[5D6]		PP8916	PROBEPOINT_SM	m60[5A5]													
		-BLK-LF						L7800	IND_3P_SM	m60[78C3]		PP636	PROBEPOINT_SM	m60[5D6]		PP8920	PROBEPOINT_SM	m60[5A5]													
C	J600	CON_M14RT_D_THA_M-RT	m60[6D7]					L7880	IND_0603-LF	m60[73A7]		PP637	PROBEPOINT_SM	m60[5D6]		PP8921	PROBEPOINT_SM	m60[5A5]													
		-TH1						L7900	IND_3P_SM	m60[79C3]		PP638	PROBEPOINT_SM	m60[5D6]		PP8922	PROBEPOINT_SM	m60[5A5]													
	J0700	CPU_YONAH_SKT_BGA	m60[7C3 7D7]					L8000	IND_IHLP	m60[80C3]		PP639	PROBEPOINT_SM	m60[5D6]		PP8923	PROBEPOINT_SM	m60[5A5]													
	J0700	CPU_YONAH_SKT_BGA	m60[8D4 8D8]					L8100	IND_IHLP	m60[81C3]		PP640	PROBEPOINT_SM	m60[5D6]		PP8924	PROBEPOINT_SM	m60[5A5]													
	J1000	CON_2RTSM_125_SM-2MT	m60[10B7]					L8203	IND_IHLP	m60[82D3]		PP641	PROBEPOINT_SM	m60[5D6]		PP8925	PROBEPOINT_SM	m60[5A5]													
		-BLK-LF						L8400	IND_0402	m60[84B7]		PP642	PROBEPOINT_SM	m60[5D6]		PP8926	PROBEPOINT_SM	m60[5A5]													
	J1101	CON_F28RT_S2MT_SM_F-	m60[11C2]					L8520	IND_IHLP	m60[85C3]		PP643	PROBEPOINT_SM	m60[5D6]		PP8927	PROBEPOINT_SM	m60[5A5]													
		RT-SM						L8715	IND_0402	m60[87A7]		PP644	PROBEPOINT_SM	m60[5D6]		PP8928	PROBEPOINT_SM	m60[5A5]													
	J2600	BATTERY_2P_SM	m60[26C8]					L8725	IND_0402	m60[87A4]		PP645	PROBEPOINT_SM	m60[5D6]		PP8929	PROBEPOINT_SM	m60[5A5]													
	J2800	CON_F200RT_DDR2DIMM_	m60[28D5]					L8910	IND_0402	m60[89D7]		PP646	PROBEPOINT_SM	m60[5D6]		PP8930	PROBEPOINT_SM	m60[5A5]													
B		5MT_SM_F-RT-SM1						L8915	IND_0402	m60[89D7]		PP647	PROBEPOINT_SM	m60[5D6]		PP8931	PROBEPOINT_SM	m60[5A5]													
	J2900	CON_F200RT_DDR2DIMM_	m60[29D5]					L8960	IND_0402	m60[89D4]		PP648	PROBEPOINT_SM	m60[5D6]		PP8932	PROBEPOINT_SM	m60[5A5]													
		5MT_SM_F-RT-SM1						L8965	IND_0402	m60[89D4]		PP649	PROBEPOINT_SM	m60[5D6]		PP8933	PROBEPOINT_SM	m60[5A5]													
	J2901	CON_F45T_S2MT_SM_F-S	m60[59C7]					L9010	IND_0402	m60[90D7]		PP650	PROBEPOINT_SM	m60[5D6]		PP8934	PROBEPOINT_SM	m60[5A5]													
		T-SM						L9015	IND_0402	m60[90D7]		PP651	PROBEPOINT_SM	m60[5D6]		PP8935	PROBEPOINT_SM	m60[5A5]													
	J2903	CON_M2ST_S2MT_SM_M-S	m60[59C8]					L9060	IND_0402	m60[90D4]		PP652	PROBEPOINT_SM	m60[5C6]		PP8936	PROBEPOINT_SM	m60[5A5]													
		T-SM						L9065	IND_0402	m60[90D4]		PP653	PROBEPOINT_SM	m60[5C6]		PP9000	PROBEPOINT_SM	m60[5B4]													
	J4700	CON_F10ST_D_SMA_F-ST	m60[47A2]					L9120	IND_0402	m60[91B6]		PP654	PROBEPOINT_SM	m60[5C6]		PP9001	PROBEPOINT_SM	m60[5B4]													
		-SM						L9125	IND_0402	m60[91B6]		PP655	PROBEPOINT_SM	m60[5C6]		PP9002	PROBEPOINT_SM	m60[5B4]													
	J5300	CON_F52RT_D2MT_SM_F-	m60[53C5]					L9130	IND_0402	m60[91B7]		PP656	PROBEPOINT_SM	m60[5C6]		PP9003	PROBEPOINT_SM	m60[5B4]													
A		RT-SM						L9135	IND_0402	m60[91A7]		PP657	PROBEPOINT_SM	m60[5C6]		PP9004	PROBEPOINT_SM	m60[5B4]													
	J6000	CON_F30STSM_5047_SM1	m60[60C3]					L9140	IND_0402	m60[91A7]		PP658	PROBEPOINT_SM	m60[5C6]		PP9005	PROBEPOINT_SM	m60[5B4]													
	J6500	CON_M4RT_S2MT_SM_M-R	m60[65D3]					L9300	IND_0402	m60[93C7]		PP659	PROBEPOINT_SM	m60[5C6]		PP9006	PROBEPOINT_SM	m60[5B4]													
		T-SM						L9305	IND_0402	m60[93C7]		PP660	PROBEPOINT_SM	m60[5C6]		PP9007	PROBEPOINT_SM	m60[5B4]													
	J6501	CON_M5RT_S2MT_SM_M-	m60[65B2]					L9310	IND_0402	m60[93C7]		PP661	PROBEPOINT_SM	m60[5C6]		PP9008	PROBEPOINT_SM	m60[5B4]													
		RT-SM						L9315	IND_0402	m60[93C7]		PP662	PROBEPOINT_SM	m60[5C6]		PP9009	PROBEPOINT_SM	m60[5B4]													

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D	Q8300	TRA_IRF7413_SO-8	m60[83D2]	R2303	RES_402	m60[23D3]	R3446	RES_402	m60[34B1]	R5809	RES_402	m60[58C2]	D										
	Q8301	TRA_IRF7413_SO-8	m60[83C2]	R2305	RES_402	m60[23D3]	R3451	RES_402	m60[34C4]	R5815	RES_402	m60[59B3]											
	Q8302	TRA_2N7002_SOT23-LF	m60[83C3]	R2306	RES_402	m60[23B7]	R3452	RES_402	m60[34B7]	R5817	RES_402	m60[59B3]											
	Q8303	TRA_2N7002_SOT23-LF	m60[83D3]	R2307	RES_402	m60[23A7]	R3453	RES_402	m60[34B8]	R5818	RES_402	m60[59B3]											
	Q8310	TRA_S13446DV_TSOP-LF	m60[83D8]	R2308	RES_402	m60[23B7]	R3454	RES_402	m60[34B7]	R5819	RES_402	m60[59B3]											
	Q8311	TRA_2N7002_SOT23-LF	m60[83C7]	R2309	RES_402	m60[23A7]	R3455	RES_402	m60[34B8]	R5821	RES_402	m60[59B3]											
	Q8312	TRA_S13446DV_TSOP-LF	m60[83C5]	R2310	RES_402	m60[23A7]	R3456	RES_402	m60[34B7]	R5822	RES_402	m60[59B3]											
	Q8313	TRA_IRF7410_SO-8	m60[83B6]	R2311	RES_402	m60[23A7]	R3457	RES_402	m60[34B7]	R5823	RES_402	m60[59B3]											
	Q8315	TRA_2N7002_SOT23-LF	m60[83B5]	R2313	RES_402	m60[23A7]	R3458	RES_402	m60[34B8]	R5824	RES_402	m60[59B3]											
	Q8316	TRA_2N7002_SOT23-LF	m60[83C5]	R2314	RES_402	m60[23A7]	R3459	RES_402	m60[34A7]	R5825	RES_402	m60[59B3]											
C	Q8317	TRA_S13446DV_TSOP-LF	m60[83D5]	R2316	RES_402	m60[23D7]	R3460	RES_402	m60[34A7]	R5826	RES_402	m60[59B3]	C										
	Q8318	TRA_DUAL_2N7002A_SOT563	m60[83A8 83A7]	R2317	RES_402	m60[23D7]	R3461	RES_402	m60[34A7]	R5827	RES_402	m60[59C5]											
	Q8319	TRA_S13446DV_TSOP-LF	m60[83C8]	R2318	RES_402	m60[23D7]	R3462	RES_402	m60[34A8]	R5828	RES_402	m60[59B3]											
	Q8320	TRA_2N7002_SOT23-LF	m60[83B7]	R2319	RES_402	m60[23D2]	R3463	RES_402	m60[34A7]	R5829	RES_402	m60[59C3]											
	Q8520	TRA_HAT2168H_LFPAK	m60[85D4]	R2320	RES_402	m60[23D7]	R3470	RES_402	m60[34A5]	R5830	RES_402	m60[59C3]											
	Q8521	TRA_HAT2168H_LFPAK	m60[85C4]	R2323	RES_402	m60[23D5]	R3471	RES_402	m60[34A5]	R5831	RES_402	m60[59C3]											
	Q8522	TRA_HAT2165H_LFPAK	m60[85C5]	R2326	RES_402	m60[23D6]	R3485	RES_402	m60[34D1]	R5832	RES_402	m60[59C5]											
	Q9400	TRA_S13443DV_TSOP-LF	m60[94C7]	R2327	RES_402	m60[23D6]	R3486	RES_402	m60[34D1]	R5833	RES_402	m60[59B3]											
	Q9401	TRA_2N7002_SOT23-LF	m60[94C8]	R2343	RES_402	m60[23D1]	R3487	RES_402	m60[34D1]	R5898	RES_402	m60[58C2]											
	Q9711	TRA_2N7002DW_SOT-363	m60[97D2 97C2]	R2388	RES_402	m60[23A3]	R3488	RES_402	m60[34D1]	R5899	RES_402	m60[58D3]											
B	R75A0	RES_402	m60[75C7]	R2389	RES_402	m60[38D5]	R3489	RES_402	m60[34D2]	R5900	RES_402	m60[59D7]	B										
	R85A0	RES_402	m60[85D1]	R2390	RES_402	m60[23B3]	R3490	RES_402	m60[34D2]	R5903	RES_402	m60[59D2]											
	R600	RES_402	m60[6A7]	R2395	RES_402	m60[23D7]	R3491	RES_402	m60[34D2]	R5904	RES_402	m60[59D2]											
	R602	RES_402	m60[6A8]	R2396	RES_402	m60[23D6]	R3492	RES_402	m60[34D2]	R5905	RES_402	m60[59D2]											
	R603	RES_402	m60[6B1]	R2397	RES_402	m60[23D6]	R3493	RES_402	m60[34D7]	R5906	RES_402	m60[59D2]											
	R605	RES_603	m60[6A6]	R2398	RES_402	m60[23D8]	R3494	RES_402	m60[34D7]	R5907	RES_402	m60[59B7]											
	R611	RES_402	m60[6B7]	R2399	RES_402	m60[23C1]	R3495	RES_402	m60[34D7]	R5910	RES_402	m60[59D2]											
	R612	RES_402	m60[6B7]	R2500	RES_603	m60[25A8]	R3496	RES_402	m60[34C5]	R5911	RES_402	m60[59D2]											
	R614	RES_402	m60[6B7]	R2501	RES_402	m60[25C8]	R3497	RES_402	m60[34D4]	R5912	RES_402	m60[59D2]											
	R615	RES_402	m60[6B7]	R2502	RES_402	m60[25D8]	R3498	RES_402	m60[34D5]	R5913	RES_402	m60[59D2]											
A	R616	RES_402	m60[6A7]	R2600	RES_402	m60[26C7]	R3499	RES_402	m60[34D5]	R5914	RES_402	m60[59D2]	A										
	R617	RES_402	m60[6A7]	R2606	RES_402	m60[26C7]	R3824	RES_402	m60[38D2]	R5915	RES_402	m60[59D2]											
	R618	RES_402	m60[6C7]	R2607	RES_402	m60[26C8]	R3851	RES_402	m60[38D3]	R5916	RES_402	m60[59C2]											
	R619	RES_402	m60[6B7]	R2609	RES_402	m60[26D7]	R3852	RES_402	m60[38D2]	R5917	RES_402	m60[59C2]											
	R0701	RES_402	m60[7C6]	R2611	RES_402	m60[26D5]	R3853	RES_402	m60[38D2]	R5919	RES_402	m60[59B4]											
	R0702	RES_402	m60[7D6]	R2612	RES_402	m60[26D5]	R3857	RES_402	m60[38B3]	R5920	RES_402	m60[59B5]											
	R0703	RES_402	m60[7C5]	R2622	RES_402	m60[26D4]	R3858	RES_402	m60[38B3]	R5921	RES_402	m60[59B5]											
	R0704	RES_402	m60[7C5]	R2623	RES_402	m60[26D2]	R3859	RES_402	m60[38B2]	R5922	RES_402	m60[59B5]											
	R0705	RES_402	m60[7B4]	R2624	RES_402	m60[26D2]	R3897	RES_402	m60[38B7]	R5923	RES_402	m60[59B5]											
	R0706	RES_402	m60[7B5]	R2625	RES_402	m60[26D2]	R3899	RES_402	m60[38B5]	R5924	RES_402	m60[59B5]											

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D	R7306	RES_402	m60[73D4]	R8007	RES_402	m60[80A4]	R8999	RES_402	m60[89A4]	U2603	SN74LVC1G04_SOT23-5	m60[26A7]	U2698	MC74VHC1G08_SOT23-5-LF	m60[26C4]	U2699	MAX6816_SOT143	m60[26C5]	U3100	LREG_BD3533FVM_MSOP-8	m60[31C5]	U3301	CLK_GEN_CY284455_QFN	m60[33C5]	U4101	88E8053_QFN	m60[41D5]	U4102	EEPROM_M24C08_S08	m60[41A3]	U4400	FW32306_TOFP	m60[44D5]	U4700	SW1_TPS2043_S01	m60[47C8]	U5700	ISL6549_QFN	m60[57D6]	U5800	SMC_H8S2116_BGA	m60[58A8 58C3 58C6 58D6]	U5900	VDET_RNSVD_SOT23-5	m60[59D8]	U5940	VREF_REF3133_SOT23-3	m60[59A4]	U5999	COMPARATOR_LM3393_S01	m60[59A8 59A8]	U6100	MAX6695_UMAX	m60[61C4]	U6301	FLASH_SST25VF016B_SO	m60[63D3]	U6700	TPM_TSSOP	m60[67C5]	U6800	AUDIO_STAC92204XR_LQ	m60[68D5]	U7200	MAX9714_QFN-LF	m60[72C5]	U7400	MAX9890_UCSP1	m60[74C4]	U7500	ISL6262_QFN	m60[75C6]	U7501	ZXCT1010_SOT23-5	m60[76D7]	U7710	MC74VHC1G08_SOT23-5-LF	m60[77C7]	U7711	MC74VHC1G08_SOT23-5-LF	m60[77B7]	U7712	MC74VHC1G08_SOT23-5-LF	m60[77B7]	U7750	SN200505068_SOP	m60[77C4]	U7800	ISL6549_QFN	m60[78C7]	U7900	ISL6549_QFN	m60[79D6]	U7901	COMPARATOR_LM339A_SO	m60[79A5]	U7901	COMPARATOR_LM339A_SO	m60[80A4]	U7901	COMPARATOR_LM339A_SO	m60[81A5]	U7910	COMPARATOR_LM339A_SO	m60[79A3]	U7910	COMPARATOR_LM339A_SO	m60[80B2]	U7910	COMPARATOR_LM339A_SO	m60[81B3]	U7910	COMPARATOR_LM339A_SO	m60[83B3]	U8000	ISL6549_QFN	m60[80D6]	U8100	ISL6549_QFN	m60[81D6]	U8200	ISL6549_QFN	m60[82D6]	U8400	ATI_M56P_BGA	m60[84C8 84D4]	U8400	ATI_M56P_BGA	m60[86D4]	U8400	ATI_M56P_BGA	m60[87D2 87D6]	U8400	ATI_M56P_BGA	m60[91D4]	U8400	ATI_M56P_BGA	m60[93C4]	U8500	ISL6269_QFN	m60[85D6]	U8595	OPAMP_LMV2011_SOT23-5	m60[85D2]	U8900	SGRAM_16MX32_GDDR3_1	m60[89D6 89B6]	U8950	SGRAM_16MX32_GDDR3_1	m60[89D3 89B3]	U9000	SGRAM_16MX32_GDDR3_1	m60[90D6 90B6]	U9050	SGRAM_16MX32_GDDR3_1	m60[90D3 90B3]	U9470	MC74VHC1G08_SOT23-5-LF	m60[94B2]	U9700	74LCL125_TSSOP	m60[6A8 6A6]	U9700	74LCL125_TSSOP	m60[97A4 97A4]	XC7200	MTGHOLE	m60[72B3]	XW602	SHORT_SM	m60[6C2]	XW605	SHORT_SM	m60[6A5]	XW5800	SHORT_SM	m60[58B3]	XW5900	SHORT_SM	m60[59B1]	XW7201	SHORT_SM	m60[72B2]	XW7300	SHORT_SM	m60[73C3]	XW7307	SHORT_SM	m60[73C6]	XW7400	SHORT_SM	m60[74A4]	XW7429	SHORT_SM	m60[74C2]	XW7441	SHORT_SM	m60[74D2]	XW7500	SHORT_SM	m60[75A6]	XW7501	SHORT_SM	m60[75B2]	XW7502	SHORT_SM	m60[75B1]	XW7503	SHORT_SM	m60[75D2]	XW7504	SHORT_SM	m60[75D1]	XW7598	SHORT_SM	m60[76D7]	XW7750	SHORT_SM	m60[77A4]	XW8500	SHORT_SM	m60[85C6]	Y2600	CRYSTAL_4PIN_SM-LF	m60[26D8]	Y3301	CRYSTAL_5K3.2-SM	m60[33C7]	Y4101	CRYSTAL_SM-3-LF	m60[41B5]	Y4400	CRYSTAL_HC49-USMD	m60[44D2]	Y5800	CRYSTAL_SM-3	m60[59B8]	Y6700	CRYSTAL_4PIN_SM-LF	m60[59B7]	ZH500	HOLE_VIA	m60[5C1]	ZH501	HOLE_VIA	m60[5C1]	ZH502	HOLE_VIA	m60[5C1]	ZH503	HOLE_VIA	m60[5C1]	ZH504	HOLE_VIA	m60[5B1]	ZH505	HOLE_VIA	m60[5B1]	ZH506	HOLE_VIA	m60[5B1]	ZH507	HOLE_VIA	m60[5B1]	ZH508	HOLE_VIA	m60[5B1]	ZH509	HOLE_VIA	m60[5B1]	ZH510	HOLE_VIA	m60[5C1]	ZH511	HOLE_VIA	m60[5C1]	ZH512	HOLE_VIA	m60[5C1]	ZH513	HOLE_VIA	m60[5C1]	ZH514	HOLE_VIA	m60[5B1]	ZH515	HOLE_VIA	m60[5B1]	ZH516	HOLE_VIA	m60[5B1]	ZH517	HOLE_VIA	m60[5B1]	ZH518	HOLE_VIA	m60[5B1]	ZH519	HOLE_VIA	m60[5B1]	ZH520	HOLE_VIA	m60[5C1]	ZH521	HOLE_VIA	m60[5C1]	ZH522	HOLE_VIA	m60[5C1]	ZH523	HOLE_VIA	m60[5C1]	ZH524	HOLE_VIA	m60[5B1]	ZH525	HOLE_VIA	m60[5B1]																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																						
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